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SOFTWARE FOR CUSTOMER ENGINEERS III - INTERNALS

Duration: 5 Days

Maximum Class Size: 10 Students

Prerequisites: Cray Employee
Advanced Troubleshooting Dump Analysis
Six months site experience (COS Preferred)

Course Description: A systems level course teaching you the terminology and skills the analysts use in determining the reason for an operating system hang. The course is focused on an analysis of four memory dumps to determine the cause of the software hang. COS/IOS system components are examined together for an operating system library overview. Inter-Program communication is studied by examining history trace entries.

Course Content:

1. COS/IOS Overview
2. System Programs
3. Program Switching and Control
4. System Resources
5. Station Protocol
6. Inter-Task Communication
7. Inter-IOP Communication
8. COS Dump Analysis
9. IOS Dump Analysis
10. Dump Exercises

Course Objectives:

1. Locate a needed system program and determine it's function.
2. Draw Central, Local and Buffer Memory Maps.
3. Analyze COS and IOS Memory Dumps for cause of hang.
4. Locate Hang Messages in dump and Hang Macro in the Listing.
5. Analyze COS and IOS History Trace Buffer entries.

Motivation:

1. To communicate better with customers, operators, and analyst.
2. The Engineer becomes more self sufficient.
3. Improves your understanding of system operation.
4. To help isolate problems that fail online only.
5. Increased reliability by improving isolation of two time hits.
6. More time for analyst to spend on software problems.
7. Future Cray products require stronger software background.

SOFTWARE FOR CUSTOMER ENGINEERS III

Monday	Tuesday	Wednesday	Thursday	Friday
COS/IOS Overview	Program Switching	Inter-Task Communication	COS Dump Analysis	IOS Dump Analysis
Functions	XP's		Hang Message	Hang Message
Components	SMOD		Hang Macro	Hang Macro
EXEC	Any Packet		History Trace	History Trace
KERNEL	Disk Activity Link		Program Regs	Program Regs
Exercise 1	Exercise 3	Exercise 6		Exercise 9

System Programs COS 15 Tasks IOS 500 Overlays Exercise 2	System Resources Disk BMR SSD CM LM BM Exercise 4 Station Protocol LCP Message Code SCB Exercise 5	Inter-IOP Communication Exercise 7	Exercise 8	Learning Logs Evaluations
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COURSE MATERIALS

Software for Customer Engineers III	Workbook
COS Internal Reference	SM-0040
COS Table Descriptions	SM-0045
IOS Internal Reference	SM-0046
IOS Table Descriptions	SM-0007
FE Station Protocol Reference	SM-0042
2 IOS Dumps	
2 COS Dumps	
EXEC Listing	
KERNEL Listing	
Necessary Task and Overlay Listings	
EXP	
HSPTEST	

READING ASSIGNMENT

Monday Night:

SM-0040	pages 1-1 to 1-7 1-14 to 1-28	COS Basics
SM-0046	pages 1-1 to 1-7 2-1 to 2-12 3-1 to 3-8	IOS Introduction Kernel Disk Subsystem
SM-0042	pages 1-1 to 4-2	Station Protocol

Tuesday Night:

SM-0040	pages 2-1 to 2-10 2-70 to 2-96 3-1 to 3-8	EXEC EXEC Debug Aids Tasks
SM-0046	pages 2-42 to 2-47	IOS Communication
SM-0046	Chapter 10	IOS Debug Aids

Wednesday Night:

SM-0040	Chapter 2-17 to 2-41 Chapter 8-1 to 8-40	EXEC Requests System Action Requests
SM-0046	Chapter 2-11 to 2-38	Kernel Service Requests

Thursday Night:

SM-0046	Chapter 9 Appendix A	IOS Macros Dump Analysis
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EVALUATION METHOD

EVALUATION OF YOUR PROGRESS IN GAINING EXPERTISE IN THESE SKILLS IS ACCOMPLISHED BY ASSIGNING A COMPETENCY LEVEL TO EACH SKILL.

Level

- 0 No knowledge and no experience.
- 1 Has some knowledge and limited experience with this skill, but not sufficient to contribute in a work environment.
- 2 Can perform some parts of this skill satisfactorily but requires instruction and supervision to perform the entire skill.
- 3 Can perform some parts of this skill satisfactorily but requires periodic supervision and/or assistance.
- 4 Can perform this skill satisfactorily without assistance and/or supervision.
- 5 Can perform this skill with proficiency in speed and quality without supervision or assistance.
- 6 Can perform this skill with initiative and adaptability to special situations without supervision or assistance.
- 7 Can perform this skill and can lead others in performing it.

Successfully completing this course should give you a competency level of three (3) for most skills. Experience on the job will continue to increase your competency level.

Software for Customer Engineers III

Date: _____

Participant's Name: _____

Instructor's Name: _____

Region/Country: _____

LEARNING LOG

CESW III								
Skills At the end of the course the learner is able to:								
Locate a specified System Program or routine and determine its function.								
Draw Central Memory, Local and Buffer Memory Maps.								
Analyze IOS and COS Memory Dumps.								
Locate Hang Messages and Hang Macro.								
Analyze History Trace Buffer Entries.								
Levels	0	1	2	3	*	5	6	7
								No Basis For Judgement

Sessions attended/held _____ / _____

Exercises completed/assigned _____ / _____

Labs attended/held _____ / _____

This learning log is intended as an aid to the learner in establishing goals and plotting progress. It is not intended as an indicator of job performance and therefore should not be used in determining future job actions.

*Maximum level discernible by the instructor in an instructional environment.

INSTRUCTOR'S FEEDBACK

Sessions attended/held _____ /
Exercises completed/assigned _____ /
Labs attended/held _____ /

MET THE PREREQUISITES OF THE COURSE

yes

was over qualified

Specifics:

SELF APPRAISAL

Specifics:

WAS ACTIVE AND ATTENTIVE IN CLASS

not at all to a normal degree exceptionally so

Specifics:

MADE GOOD USE OF LAB TIME

Specifics:

MADE GOOD USE OF TERMINAL TIME

Specifics:

KEPT UP WITH THE REST OF THE CLASS

fell behind the class yes . was ahead of the class

Specifics:

SHOWS A POSITIVE ATTITUDE ABOUT WORKING AT CRAY

Specifics:

Comments:

These are subjective appraisals based on the instructors brief and limited observations of the learners behavior during the class.

Software for Customer Engineers III

Date: _____

Participant's Name: _____

Instructor's Name: _____

Region/Country: _____

LEARNING LOG

CESW III								
Skills At the end of the course the learner is able to:								
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Levels	0	1	2	3	*	5	6	7
								No Basis For Judgement

Sessions attended/held ____ / ____

Exercises completed/assigned ____ / ____

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2010/04/20 10:00:00 2010/04/20 10:00:00

Date:

BarCode/Printed 2010/04/20 10:00:00

Impression Date:

Report/Document ID:

REPORTING TO

CEM III

SP112

At the end of the course the learners will be able to:

Recognise the basic parts of a document

Produce a document using

Open Office Writer 2003

and Microsoft Word 2003

Identify 100 and 200 Mebibytes

Groups

Recognise print Measured and

Print Mebibytes

Identify Microsoft Word

File

Identify Microsoft Word

File

Stage	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	
Topic	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	
Score	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100

RESULTS STATEMENT: The following table shows the results of the learners in the class.

COS/IOS Overview

1

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(

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LEARNING OBJECTIVES

With the aid of all reference material, upon completion of the COS/IOS Overview module, the Learner should be able to:

1. Explain what COS Functions are.
2. Explain what IOS Functions are.
3. List the Software Components and their functions.

COS FUNCTIONS

An operating system is a collection of programs which monitor and control the flow of work presented to the computer in the form of jobs.

The Cray Operating System (COS) is a multiprogramming operating system designed to process jobs which are submitted from one or more front-end computers.

COS consists of memory and mass storage resident programs for:

Scheduling, Loading, Supervising of Jobs

Allocating System Resources

Initiating and Controlling I/O Operations

Handling System Errors

Logging System Activities

Accounting and Statistical Analysis

Types of Operating Systems:

Batch - All job step processing is contained in the first file of the job.

 - Many users submit their jobs and the computer executes these jobs without user interaction.

 - Recognized by SUBMIT command.

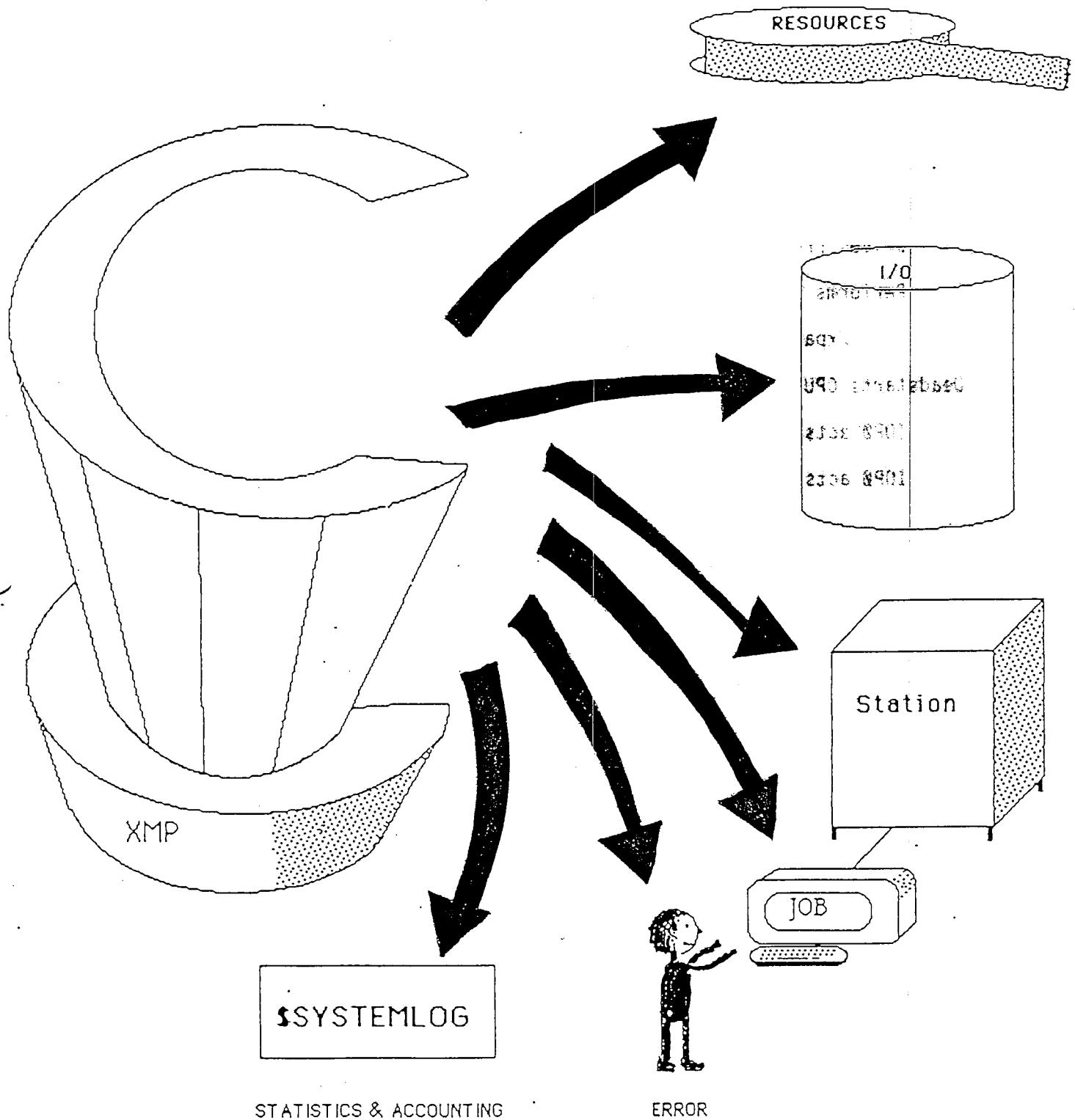
Interactive - Feature of Time Sharing.

 - Every time the computer needs a command, it comes back to the programmer.

 - Program has to wait for a response or action before it can continue.

 - Recognized by an ! prompt.

COS FUNCTIONS



IOS FUNCTIONS

Directs hardware to efficiently perform its function

Acts as a slave to COS

Performs IO between the CPU and peripherals attached to the IOP

Drives the Disk Storage

Drives Tape Storage System

Drives Front End Channels

Performs Master Operator Station functions

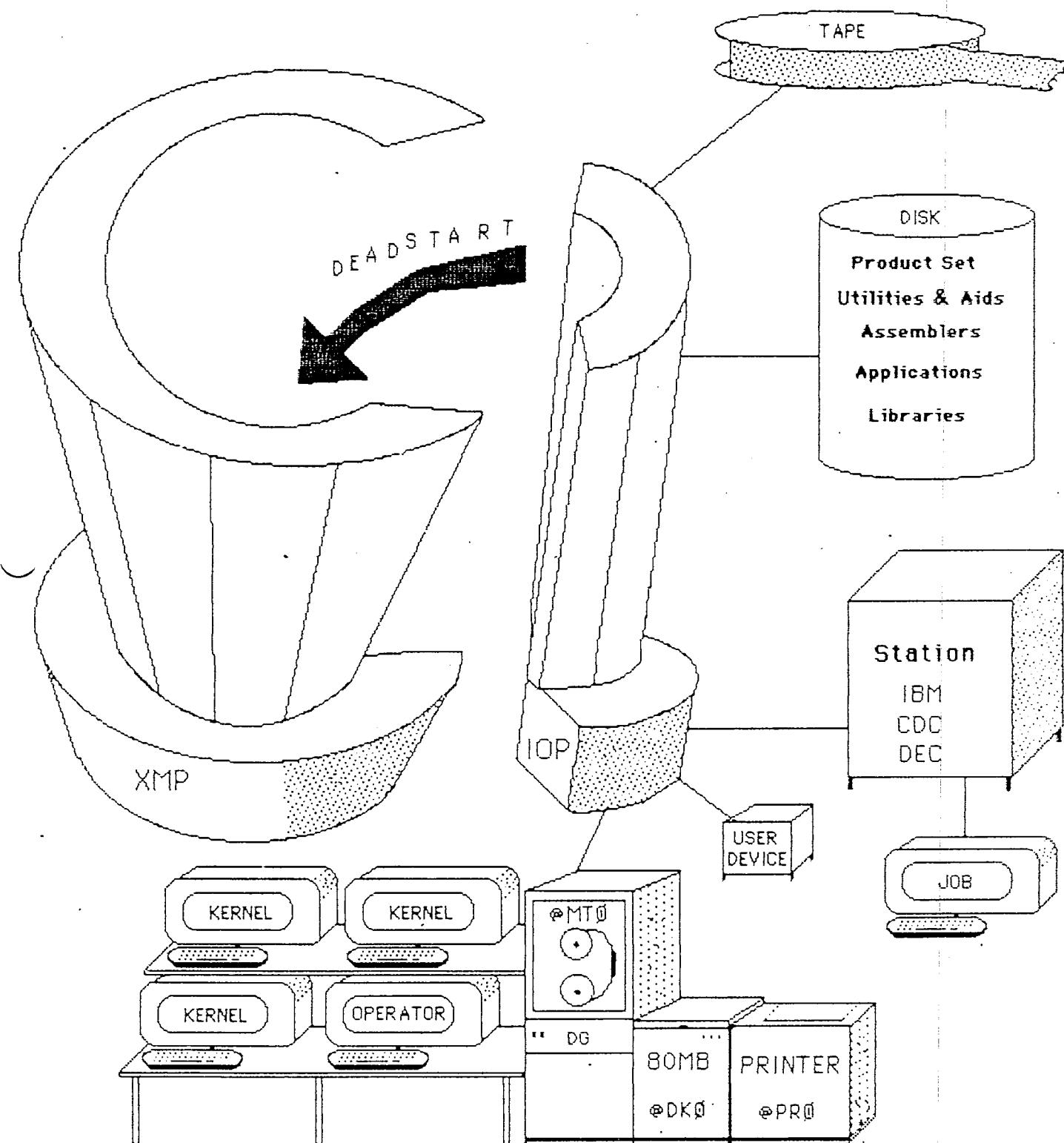
Expander Chassis and IOPØ

Deadstarts CPU

IOPØ acts as the master processor

IOPØ acts as maintenance control unit offline

IOS FUNCTIONS



COS SYSTEM COMPONENTS

Manages resources to provide a multiprogramming environment

Multiprogramming of user applications

Scheduling of applications by priority and job class

Manages disk and tape resources

Manages front-end communications

Manages file and program maintenance

Capable of modification at startup

Interrupt driven

COS is a collection of system programs.

Monitor (EXEC):

Controls system

Schedules next task or job for execution

Performs I/O

Accesses all memory

Manages exchange package area

Runs in non-interrupt mode

System Task Processor (STP):

15 tasks

Processes all user requests

Runs in interrupt mode

Control Statement Processor (CSP):

Interprets all job control statements

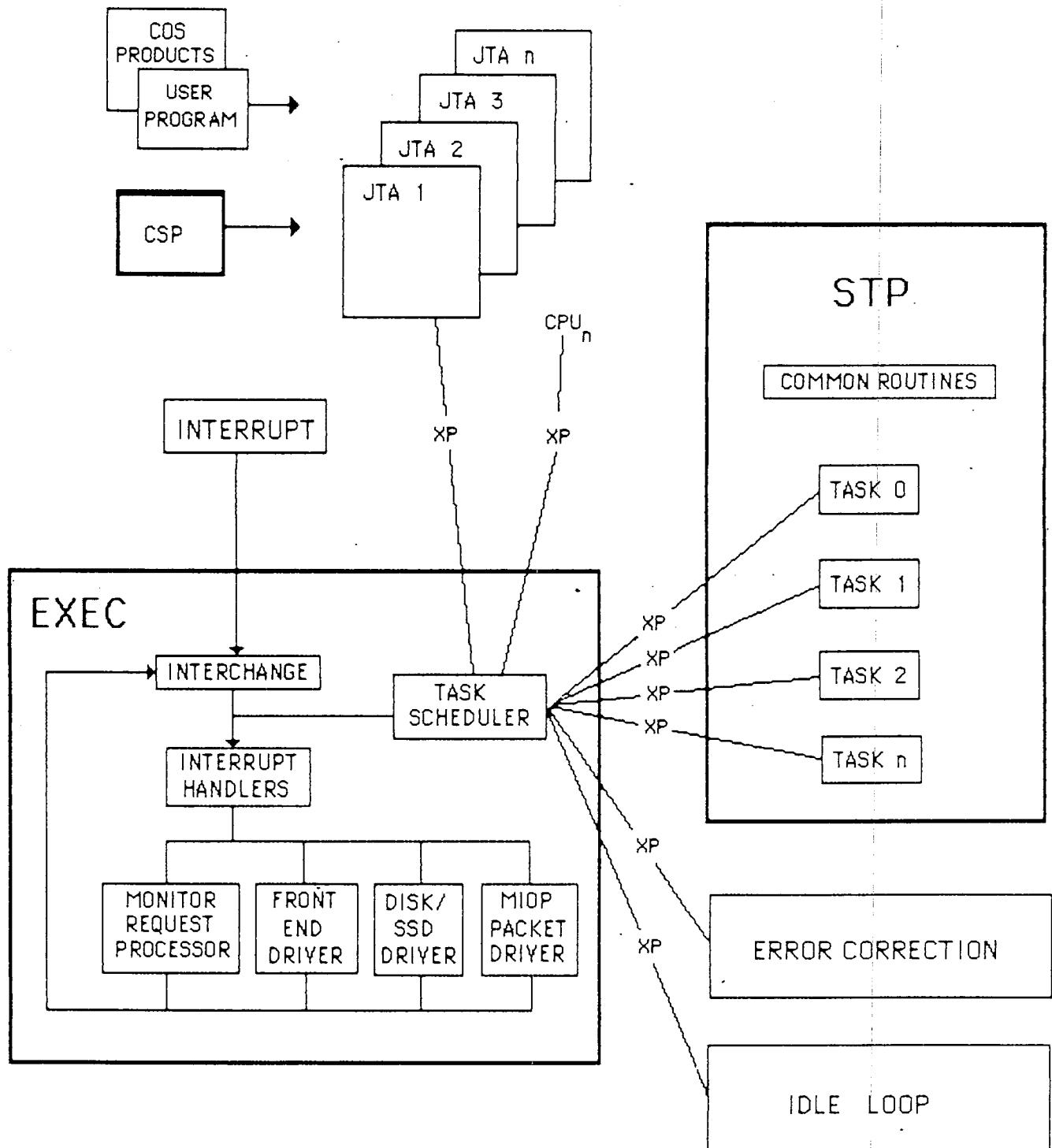
Calls systems tasks

Product Set

Job Control Language

Operational Aids and Utilities

COS COMPONENTS



EXEC

Runs in non-interrupt mode

BA=0

LA=I@MEM

Does privileged instructions

Has IO drivers (physical)

Handles interrupts

Exchange Processor

Entry point EN

Saves current RTC

Updates accrued CPU time

Interchange entry point is ENA

EXEC circles through Interchange until all flags are serviced and
no IO flags or interrupts are pending

Checks for time events

Calls IO handler if IO flag set

Calls appropriate interrupt handler

Entered from the scheduler before exchanging out of EXEC at EX

Interrupt Handlers

Clears flag

Processes channel error

Processes error exit breakpoints

IHT and CHT used for index address

Service Request Processor

Privileged work for system monitor to do

Monitor calls - See SM-40, Page 2-17 for further detail.

Task Scheduler

Entered when all flags are processed

Entered on an Error Exit from a user

EXP will be exchanged to for processing the Error Exit

Schedules tasks by priority first

Schedules user after all tasks have been serviced

Schedules Idle Loop when nothing else is

Idle loop reads EXEC looking for memory errors

Error correction exchanged to on a memory error

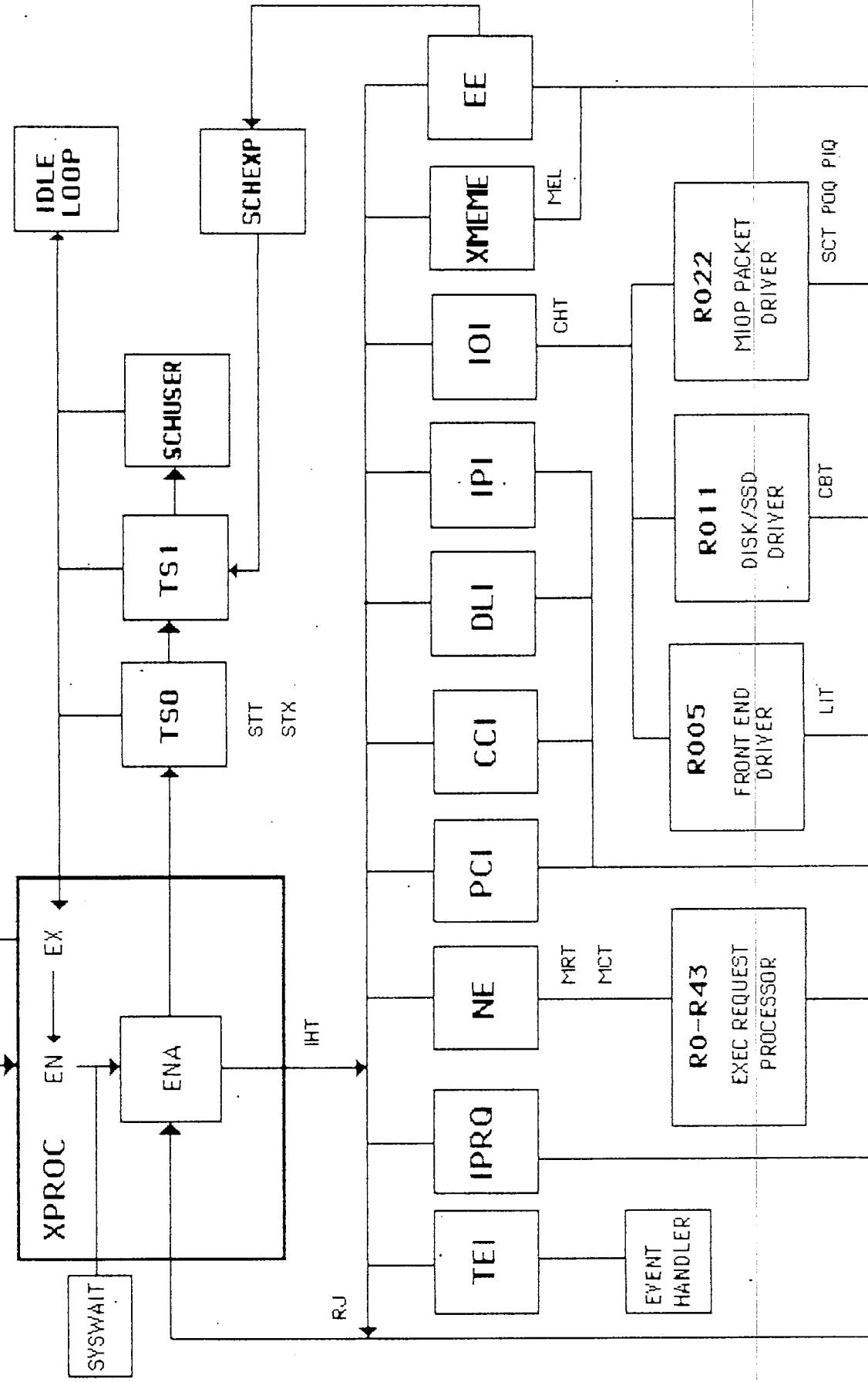
Common Subroutines

Queue Control

Stop-Error Halt routine

INTERRUPT EXIT

EXEC



EXEC REQUEST PROCESSOR

Performs essential services for tasks, in non-interruptable mode.

Process is as follows:

- Task puts function number in S7
- Task puts supplemental information in S6
- Task does a normal exit (004)
- Interchange sees the normal exit flag
- NE clears the flag and uses S7 to index into Monitor Request Table
- MRT contains the processor address

Requests exist to:

- Create, ready or suspend a task
- Enable Front-End Driver request
- Enable Disk I/O request
- Connect and disconnect user to CPU
- Post messages in the history trace table
- Start and stop the operating system
- Display or alter memory
- Display or alter register or exchange packet
- Set or clear system breakpoint
- Report statistics

When an error is made by the processor an error code is in S6

EXEC REQUESTS

R000	null request
R001	create task
R002	ready task
R003	suspend task
R004	system hardware performance
R005	front-end transmission
R006	ready task after time delay
R007	reserved for installations
R010	change CPU status
R011	I/O request
R012	single bit error detection select
R000	unused
R014	ready task, suspend self
R015	initiate STP profile time slice
R016	request CPU
R017	disconnect CPU
R020	post data to circular trace buffer
R021	set memory size
R022	packet I/O request
R023	load and execute new copy of COS
R024	start system
R025	stop system
R026	task already created
R027	source ID mismatched
R030	bad function
R031	task parameter block changed
R032	invalid channel number
R033	clear system breakpoint
R034	CPU utilization report
R035	EXEC request report
R036	EXEC call count report
R040	channel interrupt count report
R041	switch to other CPU
R042	dump X-MP cluster to memory buffer

IOS SYSTEM COMPONENTS

KERNEL

Local memory resident

Executes in each I/O processor with minor modifications

Responsible for:

- Activity management
- Inter-activity communication
- Resource management
- Interrupt handling
- Inter-processor communication

OVERLAYS

Executable Programs or Subroutines

Reside in buffer memory

Read into local memory when needed

Makes up the bulk of the system

Re-entrant

Not all used by any one processor

An overlay table is maintained to provide information about the overlay

Operand Register %B contains the overlay's base address

The DISK input/output software moves data in streams between Central Memory in the mainframe and disks.

The TAPE EXEC software processes requests from the Tape Queue Manager task in the mainframe. The Tape Exec performs functions related to tape I/O such as message routing, data formatting, data movement, and error recovery.

The INTERACTIVE STATION permits interaction with a job running in the mainframe. Interactive commands are entered at a console connected directly to the IOS.

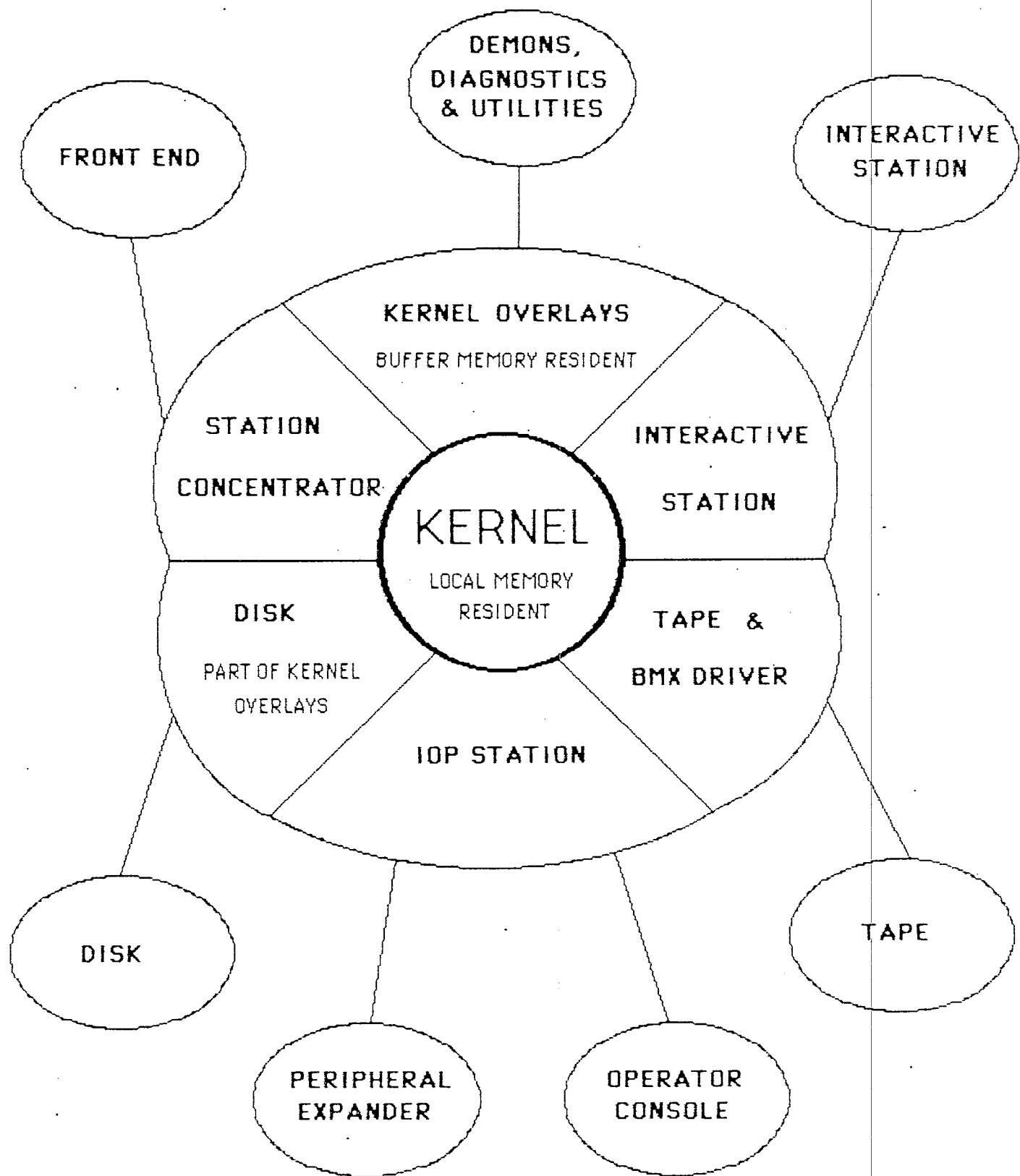
BLOCK MULTIPLEXER channel interface software drives the block multiplexer channel hardware. It contains device-independent command and interrupt code that executes requests from the Tape Exec.

STATION software runs in the Master I/O Processor (MIOP) and supports operator commands, station displays, and dataset staging. All dataset staging is performed without queuing; datasets are transferred directly to the mainframe.

CONCENTRATOR software accepts data from front-end computers into I/O Subsystem Buffer Memory, builds the data into a message, and sends it to the mainframe. The concentrator relieves the mainframe of the burden of handling an interrupt for each subsegment of messages transferred.

USER CHANNEL I/O software runs in the Master I/O Processor (MIOP) and supports access of I/O Subsystem channels by COS tasks. User Channels may be used for connecting new devices or mainframes to the I/O Subsystem.

IOS COMPONENTS



KERNEL

Local Memory Resident

Operand Registers 0-377

Unique to each Processor

Executes in non-interrupt mode

Interrupt Handler

Entered on a done flag set
No handler for high speed and buffer memory

Service Request Processor

Performs essential services for activities in non-interrupt mode
See SM-46, Page 2-11 to 2-38 for more detail

Activity Dispatcher

Transfers control from one activity to another
Swaps software stacks
Maintains overlay memory

Memory Control

Controls three local memory chains
Allocates buffer memory data, Software Stacks and DALs

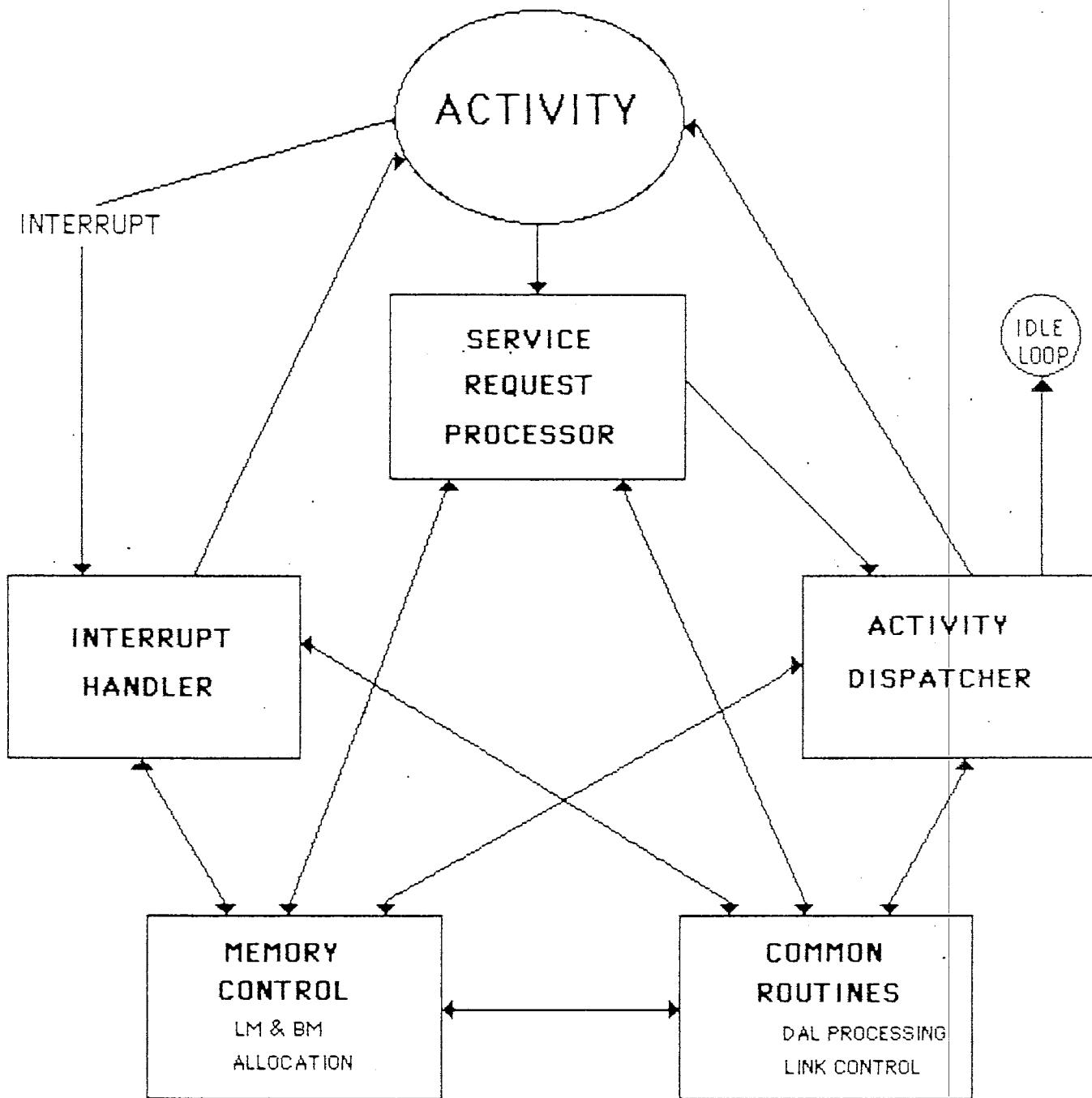
Common Subroutines

Setting up activity descriptor
Maintain queues
Maintain memory chains

Inter Activity Communication and Inter Processor Communication

Popcells, Disk Activity Links, A-A Messages

KERNEL



SERVICE REQUEST PROCESS

Performs essential services for activities, in non-interruptible mode.

Activity calls a macro which passes parameters to another macro, which sets up parameters and does a return jump to service request process.

Service request process is as follows:

1. Lock out interrupts
2. Save A, B, E, and P in SMOD
3. Save specified operand registers in SMOD
Save exit stack in SMOD if any registers saved
4. Get function code from RUNREG
5. Jump to address at FCTABLE + function code

Depending on function, control is passed to requester, kernel, or new overlay on completion.

Service Requests Exist for:

- Creating, rescheduling and terminating activities
- Passing control between overlays
- Locating an overlay in buffer memory
- Controlling push and timer queues
- Sending and receiving messages on CFT channels
- Sending responses to other IOPs
- Requesting another IOP to create or activate an activity
- Initiating front-end and block MUX I/O
- Sending messages to CPU and receiving a response
- Allocating and releasing local and buffer memory
- Moving data between buffer memory and central memory
- Flushing the overlay memory buffers

KERNEL REQUESTS

<u>CODE</u>	<u>NAME</u>	<u>DESCRIPTION</u>
1	PUSH	Put activity on a queue at priority.
2	POP	Remove activity from a queue and place it on CP queue at priority.
3	TERMINATE	Terminate an activity by releasing its AD and SMOD areas.
4	GIVEUP	Reschedule an active task by priority.
5	D4STIO	Initiates a Read or Write to a DD49.
6	D4SEEK	Initiates a Seek on a DD49.
7	PAUSE	Suspend an activity for tenths of a second.
11	TPUSH	Put activity on a queue and on a timer queue for tenths of a second.
12	GMDAL	Allocates MOS DAL.
13	RMDAL	Releases MOS DAL.
14	ASLEEP	Returns next popcell DAL. If none, push activity on popcell.
15	ALERT	Request another IOP to create an activity.
16	AWAKE	Request another IOP to activate an activity.
17	RESPOND	Send response to another IOP.
20	MSG	Send a message to a CRT.
21	MSGR	Send a message to a CRT and wait for response.
22	OUTPUT	Output a message to a CRT (station).
23	STATIO	Initiate I/O between a concentrator and a front-end.
25	RECEIVE	Input one character from a console.
26	GDAL	Allocates Local Memory DAL.
27	RDAL	Release Local Memory DAL.
30	GETMEM	Allocate local memory.
31	RELMEM	Release local memory.
32	BGET	Allocate a 512-word (4000 parcel) local buffer.
33	BRET	Release a 512-word local buffer.
34	SEND	Sends message to mainframe.
35	MGET	Allocate a 512-word MOS buffer.
36	MPUT	Release a 512-word MOS buffer.
37	OUTCALL	Calls an overlay in another IOP to execute once.
42	HSPR	Initiates a read on High Speed Channel.
43	HSPW	Initiates a write on High Speed Channel.
44	POLL	Send a message to the CPU.
45	TRANSFER	Move data between MOS and central memory.
46	MOSR	Read data from MOS to local memory.
47	MOSW	Write data from local to MOS memory.
50	CALL	Pass control to an overlay with return.
51	GOTO	Pass control to an overlay.
52	RETURN	Return control to an overlay.
53	FIND	Find MOS address and word length of an overlay.
54	FLUSH	Reinitialize overlay memory.
55	CREATE	Set up an independent activity and place it on a CPU queue.

GENERAL REFERENCES

NAME	CODE	DESCRIPTION
PNSH	1	High accuracy on a single bit position.
BBB	2	Remote control from a distance and blade at up to 90°.
TERMINATE	3	Termination on selectivity by releasing for AO and SMD.
DIVERS	4	Receives on specific track by position.
DISSTIO	5	Identifies a road or route to a DDA.
DISSEK	6	Identifies a sector to a second.
RAISE	7	Subarea on selectivity for feature of a second.
TRASH	11	Put selectivity on a distance and no a timer done for feature of a second.
OMDCL	12	Transfers of a second.
WMDCL	13	All locates M02 DAL.
APLBB	14	Registers next before DAL. If none, duty selectivity on before).
WILBERT	18	Registers stopper IOB to create an selectivity.
AWAKE	19	Registers stopper IOB to activate an selectivity.
BESBOD	21	Send response to stopper IOB.
MZO	20	Send a message to a CRT.
WDR	21	Send a message to a CRT (register).
OUTLIT	22	Outputs TVO pattern a consequence and a point-end.
STATIO	23	Imports one operation from a configuration.
RECEIVE	24	All locates P02 DAL.
GDBL	25	Registers P02 DAL.
SWBL	26	All locates focus memory DAL.
REFMEM	27	Registers focus memory.
SGET	28	All locate a 315-word (4000 bytes) local pattern.
BBEL	29	Registers a 315-word local pattern.
SEKDO	30	Send a message to memory.
MZBT	31	All locates a 315-word M02 pattern.
WTBT	32	Registers a 315-word M02 pattern.
OUTCALL	33	Call as overflow of selection IOB to execute code.
R2BR	34	Registers a word to HIGI speed channel.
R2BN	35	Registers a word to HIGI speed channel.
POFL	44	Send a message to file CRT.
TRANSIBER	45	Move data bytes between RAM and configuration memory.
WDRX	54	Read data from M02 to focus memory.
WDRW	55	Write data to M02 memory.
CALT	56	Set position of rotation with return.
GOTO	57	Set position of rotation.
RETURX	58	Return control to the previous.
HIND	59	Bring M02 address and word length to an original.
FLUSH	60	Registers current value memory.
CREATE	62	Set up an independent selectivity single to the CRT.

OVERVIEW

Match the following terms and their descriptions.

- | | |
|---------------------------|--|
| 1. STATION | — An interruptable COS program performing system functions |
| 2. EXEC | — 1 XMP CPU in monitor mode and executing EXEC at a time |
| 3. KERNEL | — Log of operating system and user activities |
| 4. \$SYSTEMLOG | — Software that runs on the front end linking COS and the front end operating system |
| 5. SINGLE THREADED | — Privileged work an overlay asks the IOS monitor to do |
| 6. MULTITASKING | — Privileged work an STP Task asks the monitor to perform |
| 7. TASK | — IOS program running under Kernel control |
| 8. KERNEL SERVICE REQUEST | — Several CPU's working on the same job |
| 9. EXECUTIVE REQUEST | — Monitor for each IOP |
| 10. OVERLAY | — Monitor for the Mainframe COS |

10. OVERLAY
Model for the following feature was proposed by GEC
9. EXECUTIVE REQUEST
Model for each of the following requests
8. KERNEE 256A/CE PROGET
for each of the following requests
7. MULTITASKING
same job
6. KERNEE 256A/CE PROGET
same job
5. STACK
1024 bytes memory area
4. SYSTEM LOGIC
and linking CGS and the front end character display
3. KERNEL
use of shared memory areas and shared libraries
2. EXEC
execute GEC at a time
1. STATION
better suited share the interface
0. OVERVIEW
What the following feature was proposed by GEC

System Programs

2

(C)

(C)

(C)

LEARNING OBJECTIVES

With the aid of all furnished reference materials, upon completion of this System Programs module, the learner should be able to:

1. Explain the process of generating COS and IOS.
2. List and explain the functions of 15 system tasks.
3. Define what an IOS overlay is.
4. List and explain the functions of 5 overlay subsystems.
5. Name a common overlay for each subsystem.
6. Locate a specified system program listing and explain its function.

SYSTEM GENERATION PROGRAM LIBRARIES

GENPL

- Gen1 - Generate Procedure Library
- Gen2a - Generate JCL Binaries
- Gen2b - Generate JCL Binaries
- Gen3a - Generate COS Binaries
- Gen3b - Generate IOS Binaries

COSPL

EXEC
TASKS
COSTXT

SN0001 - Configuration Modifications for the Mainframe

IOPPL

KERNEL
OVERLAYS
\$APTEXT

I0001 - Configuration Modifications for the I/O Subsystem

CAL BINARIES

There are some special macros used by CAL.

Macro definitions in program libraries must be assembled first.

\$SYSTXT - User's CAL Macros

COSTXT - contains Global definitions for STP macros.

\$APTEXT - contains Global definitions for IOS macros.

EXEC macros - defined at the beginning of EXEC listing.

Common Decks - Common Code used in Program Library.

- Defined by update directive *COMDECK
- Called from any regular or common deck.

JOB, JN=U1502A, T=800.
ACCOUNT, AC=265124, US=CRT, UPW=CRT.

*
* COS LISTINGS
*
ACCESS, PDN=SN0101, DN=CONF COS, ID=V114BF1, OWN=SYSTEM.
ACCESS, DN=COSPL, ID=V114BF1, OWN=SYSTEM.
UPDATE, I=CONF COS, P=COSPL, C=COSTXT, N=NEWCOS, Q=CT.
CAL, I=COSTXT, LIST, T=\$COSTXT, S=0. L=0.
UPDATE, P=NEWCOS, N=0, I=0, C=LISTE, Q=EXEC.
CAL, I=LISTE, LIST, S=\$COSTXT.
UPDATE, I=0, N=0, C=LISTT, Q=EXP: PDM.
CAL, I=LISTT, LIST, S=\$COSTXT.

*
* IOS LISTINGS GENERATION
*
ACCESS, PDN=100101, DN=CONF IOS, ID=V114BF1, OWN=SYSTEM.
ACCESS, DN=IOPPL, ID=V114BF1, OWN=SYSTEM.
UPDATE, I=CONF IOS, N=NEWIOS, C=APTEXT, Q=AT.
APML, I=APTEXT, T=\$APTEXT, S=0. L=0.
UPDATE, I=0, N=0, C=LISTK, Q=K.
APML, I=LISTK, S=\$APTEXT, LIST.
UPDATE, I=0, N=0, C=LISTO, Q=AMAP:ACOM:AMSG:BCOM:BEGIN:CALL:CDEM:CLI:CLOCK:CONCIO:
CONSL:CRAY:CRAYIO:CRTDEM:DISK:DISPLAY:IAIOP:KEYBD.
APML, I=LISTO, LIST, S=\$APTEXT.

TASK PROCESSOR CHARACTERISTICS

A system program to perform a system function.

Tasks have the following characteristics:

- Memory resident following EXEC
- Each task is a separate program loaded together
- Communicates with EXEC, each other and user jobs
- BA is end of EXEC 32000 (varies with release)
- LA is at the install parameter I@MEM
- Tasks operate in interrupt mode
- Each task has a priority (0-377)
- Each task has an ID (0-35)
- Task can be suspended by EXEC Requests
- A task may not suspend another
- Tasks are pre-emptable
- A task can create another task through an EXEC Request
- The Startup task creates the STP memory area

STP TASKS

Station Call Processor

ID 1 P 1

Tape Queue Manager

ID 13 P 3

STaGer

ID 14 P 6

MESSage Processor

ID 7 P 10

Disk Queue Manager

ID 5 P 2

EXEC

INTERRUPT HANDLERS

CHANNEL DRIVERS

EXEC REQUEST PROCESSOR

log MessAge manager

ID 6 P 5

Permanent Dataset Manager

ID 3 P 14

System Performance
Monitor

ID 10 P 24

Job Class Manager

ID 12 P 12

Flush Volatile Device

ID 15 P 15

Job Scheduler

ID 11 P 13

Disk Error Correction

ID 4 P 20

user EXchange Processor

ID 2 P 11

ISP Queue Manager

ID 16 P 4

startup STP - Z

ID 0 P 77

TASKS

Station Call Processor - SCP

Monitors communication between CRAY and Front-End
Passes staged datasets to stager
Calls STG, DQM, JCM, and JSH

Stager - STG

Manages dataset staging
Moves segment buffer data to disk buffer data
Used to be part of SCP

Disk Queue Manager - DQM

Manages disk resources
Performs IO request processing
Allocation/Deallocation of disk space

Permanent Dataset Manager - PDM

Performs permanent dataset functions
Manages Dataset Catalog DSC

Job Class Manager - JCM

Determines class of user job based on system resource needs
Determines number of execution entries per job class

Job Scheduler - JSH

Schedules user jobs by priority
Performs memory management
Controls rolling and recovery of job

Exchange Processor - EXP or UEP

Processes all user job exits
Receives all CSP user requests
User makes requests to STP through EXP

Tape Queue Manager - TQM

Manages allocation of tape drives
Supplies tape related operator messages
Manages tape IO request processing

Message Processor - MEP

Relays error messages from EXEC to message task

STP ADDRESS POINTERS

DMEM, BIAS=32000, FWA=0, LWA=1000.

STP TABLES

FDUMP 1.14

SYSDUMP

01/04/85

01/04/85

13:58:1

00:44:5

PAGE 36

Log Manager - MSG

Writes messages from system and user to \$SYSTEMLOG or user log
Analyzed by Extract or HERG

System Performance Monitor - SPM

Collects system performance statistics
Records performance statistics in \$SYSTEMLOG
Sends information to Log Manager

Flush Volatile Device - FVD

Backs up volatile storage such as buffer memory and SSD

ISP Queue Monitor - IQM

Gives COS access to datasets or IO devices supported under MVS.
Eliminate the need to stage large front-end datasets to Cray disk.

Disk Error Correction - DEC

Used only on DCU 2/3 Disks
Attempts error correction
Called by DQM
NOT assembled if no DCU 2/3

Startup - Z & STP

Initiates tasks and system tables
Initiates system devices
Controls recovery of jobs and datasets
Relinquishes memory when complete

3 parts

Install - All mass storage is assumed vacant and COS is started as if for the first time.

Deadstart - Permanent datasets are recovered and COS is started as in after a normal shutdown.

Restart - Operator option after a system interruption to recover the IO and jobs.

Z	SCP	STG	DAM	PDM	JCM	JSH	DAM	DEC	EXP	MEP	MSG	SPM	IQM	FYD
AUT	-AUT	POD	-DAT	DAT	-CSD	CSD	(DDL)	EQT	CNT	-AEM	AUT	CSD	ILT	EQT
CNT	-IBT	SDT	OCT	CSD	SDT	-JXT	-(DNT)	-DEX	(DSP)	DCT	VPT	DRT	VCT	DRT
DAT	-LCT	-SST	(DNT)	(DNT)	-MST	-(DSP)	(DNT)	JTA	+MCT	+ICT	+MCT	VCT	TRB	TRB
(DNT)	-LIT	-DRT	DRT	-DRT	-RJI	JXT	(DSP)	JXT	+ICT	+STT	+STT	IST	IST	IST
DRT	-LXT	(OSP)	-DSC	-DSC	SDT	-LFT	-DUX	-DUX	-LGU	-LGU	-LGU	-LGU	-LGU	-LGU
-DSC	POD	-EQT	(DSP)	(DSP)	-TXT	-ODN	-ODN	-FSH	POD	POD	POD	POD	POD	POD
(DSP)	-SDT	GRT	DXT	JTA	(PDD)	GRT	GRT	GRT	SDT	SDT	SDT	SDT	SDT	SDT
-DVL	-STT	JXT	EQT	SDT	SDT	JXT	-SWT	-LOT	-SM	-UPT	-VAX	-VUX	-VUX	-VUX
DXT	-RQT	EQT	JCB	JTA	JTA	JTA	TCB	TCB	-JTA	-JTA	-JTA	-JTA	-JTA	-JTA
-EFT	-EQT	PHR	JXT	PHR	SQP	PDD	-UPT	-UPT	-VAX	-VUX	-VUX	-VUX	-VUX	-VUX
-EQT	GRT	SQP	TXT	TXT	-PDI	-PDS	-QDT	-QDT	IDD	IDD	IRT	IRT	IRT	IRT
JTA	JXT	TXT	-PDI	-PDI	-PDS	-QDT	SDT	SDT	TRB	TRB	TRB	TRB	TRB	TRB
(DNT)	PDI	QDT	-XAT	-XAT	-XAT	-XAT	-XAT	-XAT	-XAT	-XAT	-XAT	-XAT	-XAT	-XAT
-RJI	SDT	TCB	TDT	TDT	TDT	TDT	TDT	TDT	TDT	TDT	TDT	TDT	TDT	TDT

WHAT IS AN ACTIVITY?

Work for IOP to do:

Drive operator console

Pass a dataset

Interruptible program that runs on the IOP

Consists of one or more nested overlays

High priority activities are demons

OVERLAY CHARACTERISTICS

Executable program or subroutine that resides in buffer memory

Read into local memory when needed for activation

Overlay table contains an entry for each defined overlay name

An overlay may be called from the kernel console or another overlay

Overlays are read only and deallocated when no longer being used

Groups of overlay types make up a subsystem

Completely re-entrant

Kernel maintains an operand register (%B) containing the overlay base address

Overlay Format

Field	Parcel	Bits	Description
OV@NAME	0-3	0-15	Overlay name (up to eight ASCII characters)
OV@TYP	4	0	Type of overlay: 0 Executable 1 Data
OV@NUM	4	1-15	Overlay number
OV@PAR	5	0-15	Parameter information:
SM@NUM	-	0-6	Number of registers
SM@FST	-	7-15	First operand register
OV@ENT	6	-	Entry point (first executable statement)

DMEM, TYPE=IOP1, FWA=0, LWA=1777777, FORMAT=PARCEL, R.

FDUMP 1.13
SYS DUMP

07/06/84 13:38:17
05/04/84 16:12:25

```

000024360 102006 020152 022154 024152 027155 071006 020152 022153 $ { $ 
000024370 011177 024154 020151 012037 024001 030001 024152 020152 021502 { 0 { & 4 
000024400 012003 024001 030001 004011 024152 020152 023154 133064 MD) ; 
000024410 000026 001000 000000 000000 046504 024424 035434 000000 - ) MD- ) 
000024420 026654 024424 020003 016000 046504 026654 024414 000002 ) / - HSPTES 
000024430 024414 027634 006264 026423 044123 050124 042523 000000 ) 9 ( ) ) ) ) ) 
000024440 000071 004030 010000 024430 024431 024432 024433 024434 000000 ) )%) ) 0 ) ) 
000024450 024435 024445 024441 024460 014000 005575 024410 010000 010000 ) 9 G ) * ] * 
000024460 034410 020107 102002 070032 010001 024452 020452 013003 020452 013003 ) 9 ) 4 ] * ) 5 
000024470 100003 102002 070023 010015 024464 020452 024465 014000 020452 014000 ) 9 ) 6 4X 9 
000024500 000071 024466 010464 054000 014000 034430 076011 020006 034430 076011 ) 1 - * ) % 
000024510 012013 024410 030410 026452 071026 020445 103033 010030 020445 103033 ) 4 <) 16 ) 7 
000024520 024464 010074 024465 010000 024466 010000 024467 010000 024466 010000 ) 8 % ) 9 4X ) 7 
000024530 024470 010445 024471 010464 054000 010000 076011 103002 010464 076011 ) 4 4X 9 
000024540 070010 010004 024464 010464 054000 014000 034430 076011 034430 076011 ) 9 ) % ) <) 1 
000024550 071033 020445 024416 010074 024415 020415 102006 010000 020415 102006 ) 9 / - ) % ( ) & 
000024560 034416 027415 026416 071006 020445 012050 024446 020162 012050 024446 ) 8 ) M ) S 
000024570 102002 070012 014000 002115 024444 077003 001341 077003 001341 077003 ) 5 4X ) 4 
000024600 001633 075003 001273 020441 103023 010032 024464 010411 024464 010411 ) 4 ] ) 4 
000024610 024465 010464 054000 010000 076011 103002 070010 010004 076011 103002 ) 5 4X 9 ) 10 
000024620 024464 010464 054000 014000 034430 076011 071023 020460 034430 076011 ) 4 4X 9 ) 10 
000024630 102002 070006 020441 024460 010000 024441 071033 077003 024441 071033 ) 1 ) 0 ) ) 
000024640 001633 014000 001730 024444 077003 001341 077003 001633 077003 001633 ) S 
000024650 077003 001633 014000 001744 024444 077003 001341 077003 001341 077003 ) S 
000024660 001633 014000 001757 024444 077003 001341 077003 001633 077003 001633 ) S 
000024670 014000 001767 024444 077003 001341 077003 001633 014000 077003 001633 ) S 
000024700 001774 024444 077003 001341 077003 001633 014000 002001 077003 001633 ) S 
000024710 024444 077003 001341 077003 001633 014000 002001 077003 001633 014000 ) S 
000024720 002006 024444 077003 001341 077003 001646 030446 004010 030446 004010 ) S 
000024730 024423 020423 013061 102002 070010 010001 024430 010001 024430 010001 ) 1 1 2 ) 
000024740 024431 010001 024432 070040 020423 013062 102002 070006 013062 102002 ) 1 ) 1 3 
000024750 010001 024430 010001 024431 070027 020423 013063 102002 013063 102002 ) 1 ) 1 4 
000024760 070004 010001 024430 070020 020423 013064 102002 070004 013064 102002 ) 1 ) 1 5 
000024770 010001 024431 070011 020423 013065 102002 070004 075003 013065 102002 ) 1 ) 1 5 
000025000 001273 070002 071143 014000 002015 024444 077003 001341 077003 001341 ) S 
000025010 077003 001646 010000 024450 077003 001456 107013 030445 001456 107013 ) { 1% 
000025020 024453 020445 012001 024410 030410 024454 020453 013040 030410 024454 ) + ] % ) 1 , 1 , 1 + 1% - 
000025030 100003 102002 071027 077003 001456 107032 030445 024455 001456 107032 030445 ) % ) 1 , 1 , 1 - 
000025040 020445 012001 024410 030410 024456 020455 013040 100003 024456 020455 013040 ) & ] + - , , , 
000025050 102002 071046 020453 023455 102002 070006 020454 023456 102002 070006 020454 ) . , , / - + , , 
000025060 102002 070002 071057 020455 023453 024423 004020 024417 023453 024423 004020 ) . , , / - + , , 
000025070 020456 023454 024424 101002 027423 004020 021417 106074 027423 004020 021417 ) S 
000025100 000703 001633 014000 002034 024444 077003 001341 077003 001341 077003 001341 ) L 
000025110 001646 030446 004010 024423 020423 013114 102002 070004 020423 013114 102002 ) S 
000025120 010001 024433 070006 020423 013123 103002 070002 071027 013123 103002 070002 ) S 
000025130 014000 002051 024444 077003 001341 077003 001646 030446 001341 077003 001646 ) B 
000025140 004010 024423 020423 013102 102002 070004 010001 024434 102002 070004 010001 ) S 
000025150 070006 020423 013127 103002 070002 071025 020432 102027 070002 071025 020432 ) B 
000025160 014000 002076 024444 077003 001341 077003 001646 030446 001341 077003 001646 ) S 
000025170 004010 024423 020423 013131 102002 070004 010001 024435 102002 070004 010001 ) Y 
000025200 070006 020423 013116 103002 070002 071027 020453 024437 070002 071027 020453 ) N 
000025210 020454 024440 077003 001703 020455 023437 024423 004020 020455 023437 024423 ) S 
000025220 024417 020456 023440 024424 101002 027423 004020 021417 101002 027423 004020 ) S 
000025230 020423 103006 020424 017000 001000 101002 070005 014000 001000 101002 070005 ) S 
000025240 001000 024457 070003 020424 024457 020434 103002 070004 024457 020434 103002 ) S 
000025250 020457 024436 070003 010001 024436 020436 005002 024423 024436 020436 005002 ) S 
000025260 020457 005002 024424 020424 023423 024442 020457 023436 024442 020457 023436 ) S 

```

DISK SUBSYSTEM

Moves data between central memory and disk.
Performs disk error recovery.
COS must initiate I/O by making a disk request.
COS is responsible for device assignments and dataset allocations.
Part of the disk code is within the Kernel.

DISK INTERRUPT ANSWERING

Entered when a disk channel interrupts.
Initiates next I/O and schedules DISK demon or disk error recovery.
Executes in non-interruptible mode.
Allocates local memory buffers on reads.
Deallocates local memory buffers on write.

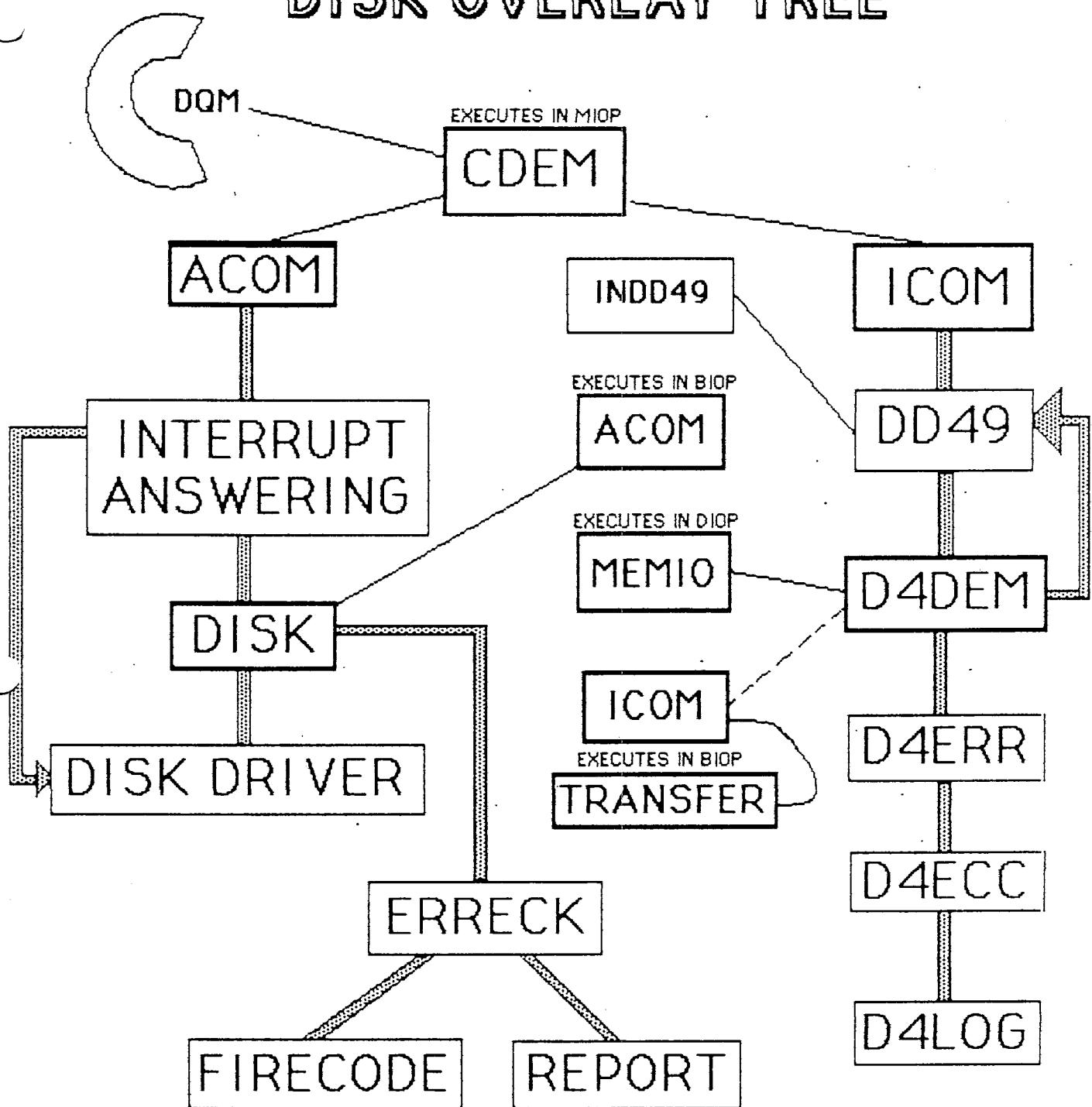
DISK DRIVING ROUTINES

Performs most of the physical I/O.
Used by disk interrupt answering, DISK demon, ACOM demon, and disk error recovery.
Executes in non-interruptible mode.
Routines available for:
Selecting head and cylinder
Setting up disk buffers
Building and sending a DAL
Initiating disk I/O

DISK DEMON ACTIVITY

Nucleus of the disk subsystem.
Evaluates requests pending on a disk channel's done queue.
Builds EDALS as needed.
In DIOP transfers data between local and buffer memory.
In BIOP transfers disk data over high-speed channel.
Creates disk error recovery activity if necessary.
Usually activated by disk interrupt answering.
Executes often in non-interruptible mode.
Resides in buffer memory as disk overlay.
Sends EDAL with done status to MIOP.

DISK OVERLAY TREE



STATION SUBSYSTEM

Provides a means for operator-COS communication.
Controls operator consoles.
May be used as a batch job entry station.
Jobs or datasets staged from tape.
May accept COS output and distribute it to mag tape or printer.
Allows on-line debugging of COS.

CHARACTERISTICS

Executes mostly in MIOP with some high speed transfers through BIOP.
May have more than one station active at a time.
Each station must have a dedicated console.
They must share the expander peripherals.
Two or more consoles may be supported by one station.
Appears to be just another front-end station to CPU.

MAIN COMPONENTS

STATION Overlay:

Initializes a station when 'STATION' is typed in at the MIOP KERNEL console.
Initiates one set of station console handling activities:
KEYBD, CLI, and DISPLAY

KEYBD Overlay:

Receives characters entered at the station console keyboard.
Calls the CONSL overlay to echo the characters.
Activates the CLI activity to process commands.

CLI Overlay:

Interprets and executes the operator commands.
Gets commands from a circular buffer filled by the KEYBD activity.
Validates them, and calls appropriate overlay to process them.

DISPLAY Overlay:

Formats the operator display.
Responds to requests from CLI and calls appropriate display overlay.

PROTOCOL Overlay:

Manages station-COS communications for all active stations.

Initiated by LOGON command.

Terminated by LOGOFF or communication breakdown.

Responsible for:

- Generating message sent to COS.
- Validating COS responses.
- Maintaining stream states.
- Creating activities to manage dataset transfers.
- Scheduling messages to COS.
- Distributing COS responses.

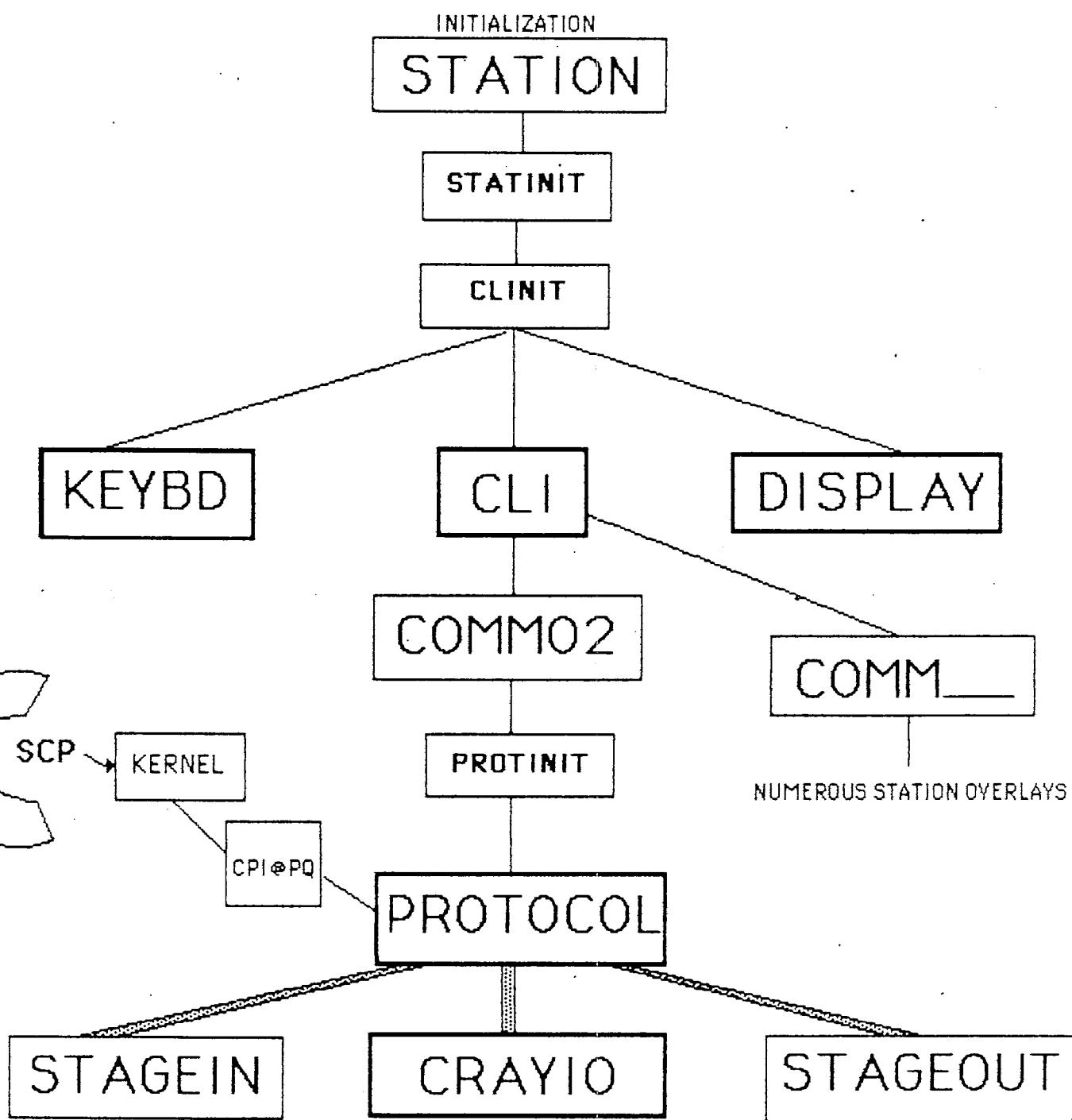
STAGEIN Overlay:

Stages a dataset from the I/O subsystem to COS.
Created by protocol activity.

STAGEOUT Overlay:

Stages a dataset from COS to the I/O subsystem.
Created by the protocol activity when CPU initiates staging on an output.

STATION OVERLAY TREE



CONCENTRATOR SUBSYSTEM

Allows apparent direct communication between the CPU and a front-end.

Looks like a CRAY channel pair to front end.

Thus no changes necessary to existing front-end stations.

May reduce the number of interrupts to the CPU per front-end message.

CHARACTERISTICS

Resides in buffer memory as overlays.

Executes mostly in MIOP with high speed transfers to CPU through BIOP.

One active concentrator for each front-end channel pair.

May have several logical IDs logged on to one concentrator
(through same channel pair).

Each ID may have a different segment size.

Controlled via 'CONC' and 'ENDCONC' kernel console commands.

MAIN COMPONENTS

CONC Overlay:

Initializes concentrator resources.
Creates CONCIO activity.

CONCIO Overlay:

Concentrator IO routines.
Accepts messages from front end and writes to buffer memory.
Sends message to front end reading from buffer memory.

CONCID Overlay:

Locate ID based table entries.
Termination processing.

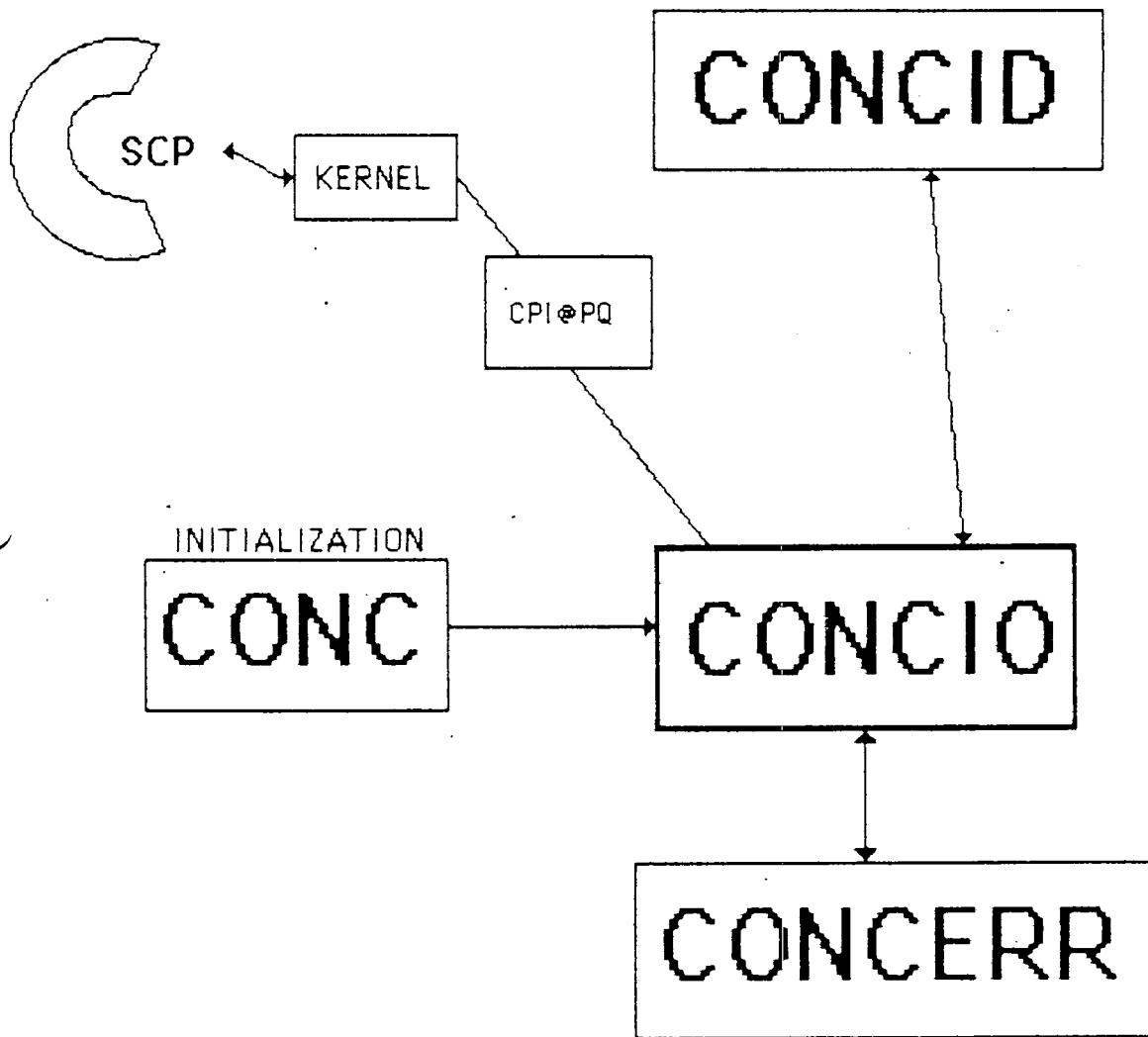
CONCERR Overlay:

Handles concentrator hardware and software errors.

CRAYIO Overlay:

Gets central memory addresses for messages via channel extension table.
Does a STATIO request to BIOP to move LCP or Message to or from CM and BM.

STATION CONCENTRATOR OVERLAYS



NSC HYPERCHANNEL SUBSYSTEM

Links the station concentrator software to the JSC Hyperchannel Adapter.

CHARACTERISTICS

Resides and executes in MIOP.

Uses signal watch macros to talk to CONCIO.

MAIN COMPONENTS

NSC Overlay:
Initializes NSCIO Activity

NSCIO Overlay:
IO Control and Hyperchannel driver

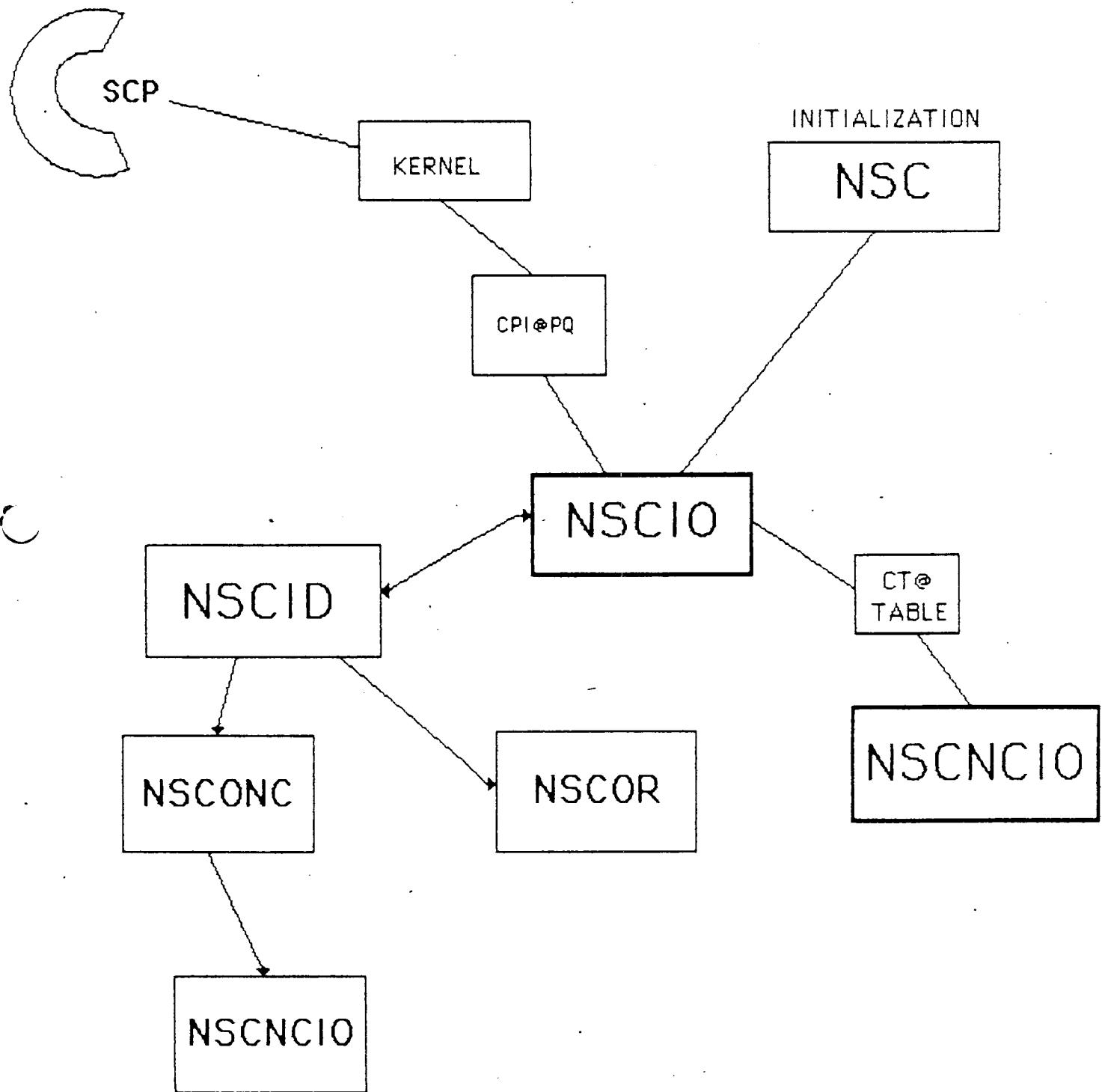
NSCID Overlay:
Logical IO processor for CXT

NSCOR Overlay:
Ordinal assignment

NSCONC Overlay:
NSC concentrator initialization

NSCNCO Overlay:
NSC concentrator IO activity

NSC HYPERCHANNEL OVERLAY TREE



INTERACTIVE STATION SUBSYSTEM

Allows analyst to use COS interactive facility.

Access to TEDI and JCL statement control.

CHARACTERISTICS

Resides in buffer memory as overlays.

Executes mostly in MIOP with high-speed transfers to CPU through BIOP.

May support several consoles.

Consists of two parts:

 Interactive concentrator

 Interactive console

MAIN COMPONENTS

IAIOP Overlay:

 Initializes IOP Station Concentrator and interprets commands.

IACON Overlay:

 Initializes interactive station console Ø.

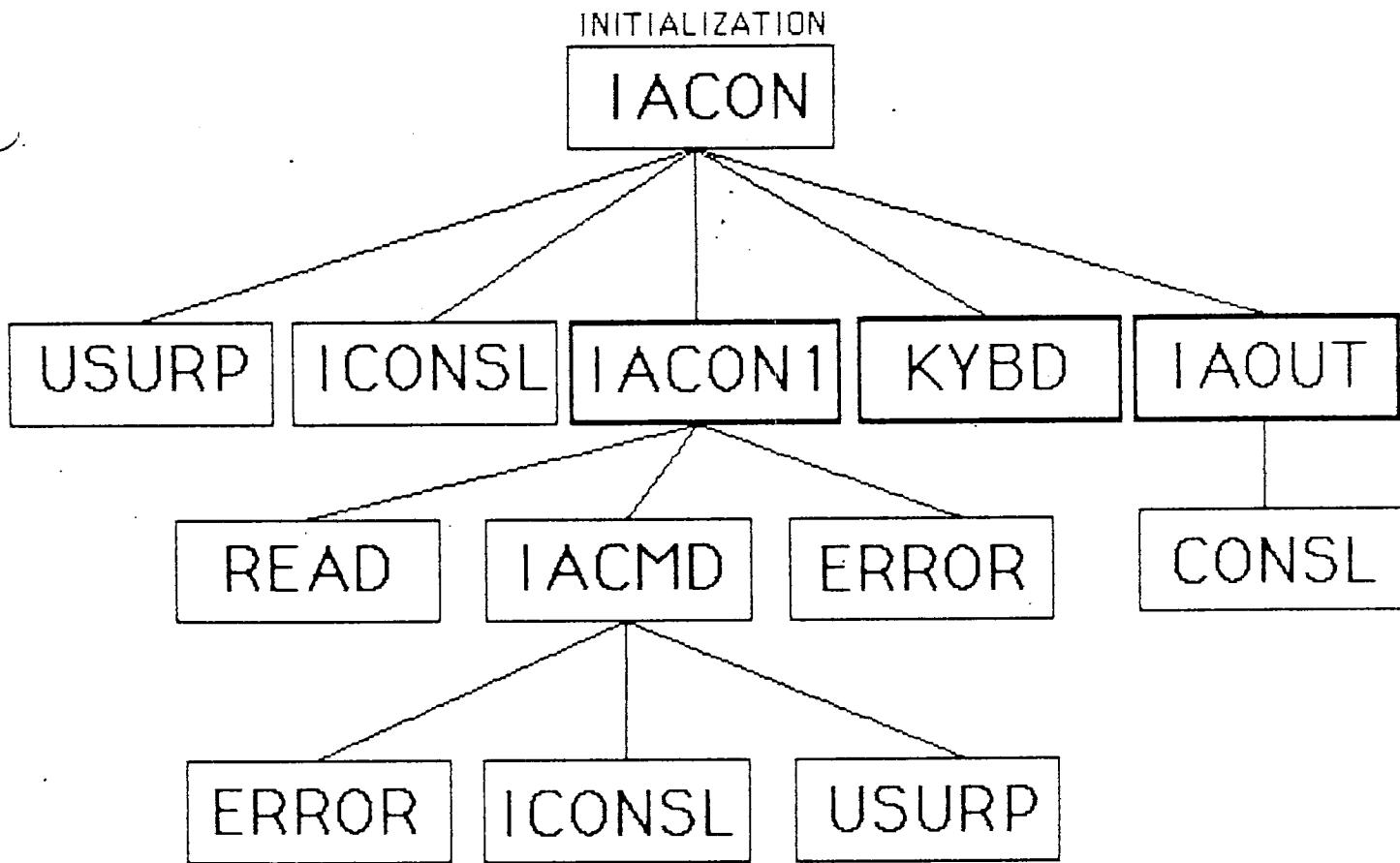
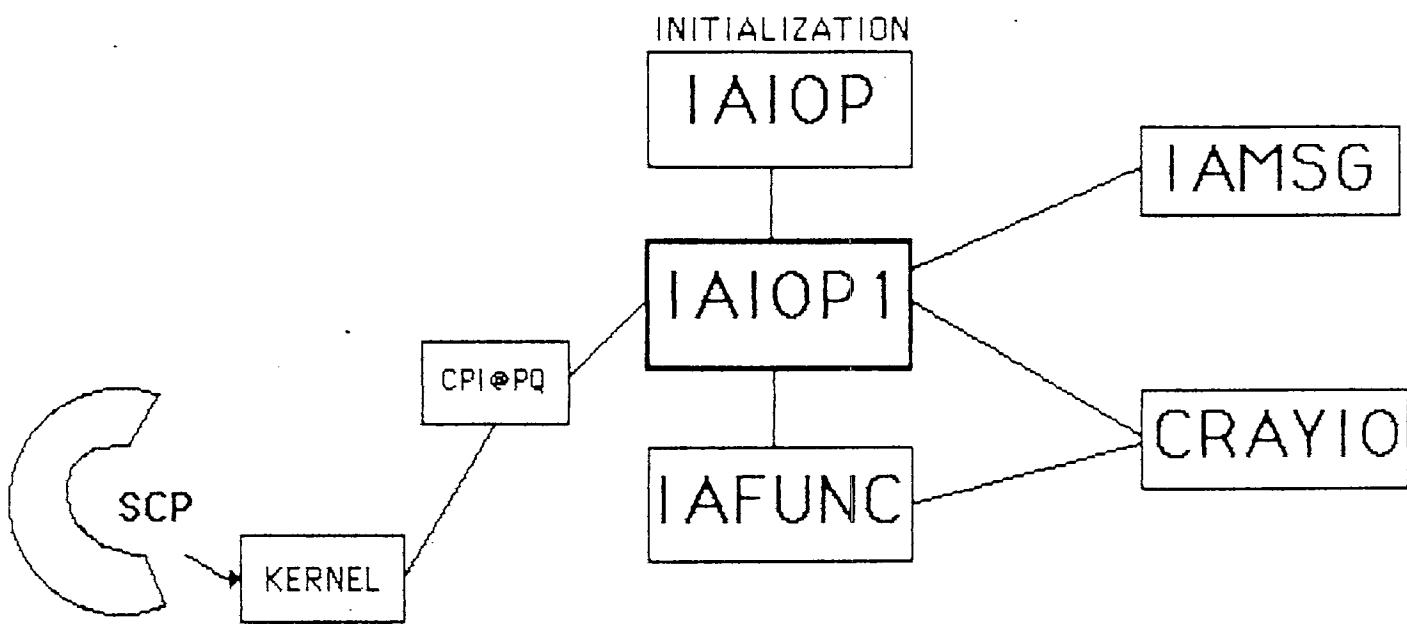
ICONSL Overlay:

 Acquires MOS Buffer and a local table for display screen data.

CRAYIO Overlay:

 Does a STATIO request to BIOP to move LCP or Message to or from CM and BM.

INTERACTIVE OVERLAY TREE



TAPE SUBSYSTEM

Processes requests for tape I/O from TQM in CPU.
Moves data to and from central memory.
Performs error recovery and reporting.

CHARACTERISTICS

Consists of activities and demons in MIOP, BIOP and XIOP.
Processes requests asynchronously.
Data movement to/from tape is independent of data movement to/from
central memory.
Interfaces to block multiplexer driver to perform physical tape I/O.

MAIN COMPONENTS

BCOM Overlay:

Interprocessor Tape Message Handler
Directs DAL to appropriate activity

CONMAN Overlay:

Processes configuration change requests

BYPASS Overlay:

Controls transfer of data between XIOP and BIOP
Sends DALs to BIOP
One activity for all drives

BUFMAN Overlay:

Controls allocation of Buffer Memory buffers for Tape Data

TEX Overlay:

Main control overlay of tape driving
Call TAPEIO and TAPEMOV to do I/O

BMXOPE Overlay:

Processes OPEN and CLOSE requests

TAPEIO Overlay:

Read/write control for TAPE EXEC
Builds CPWs for driver and calls BMXSIO
Calls TERROR on Tape error

TDEM Overlay:

XIOP to/from buffer memory data handler

TDEMO Overlay:

XIOP to buffer memory read reverse data handler

XDEM1 Overlay:

BIOP to/from buffer memory data handler

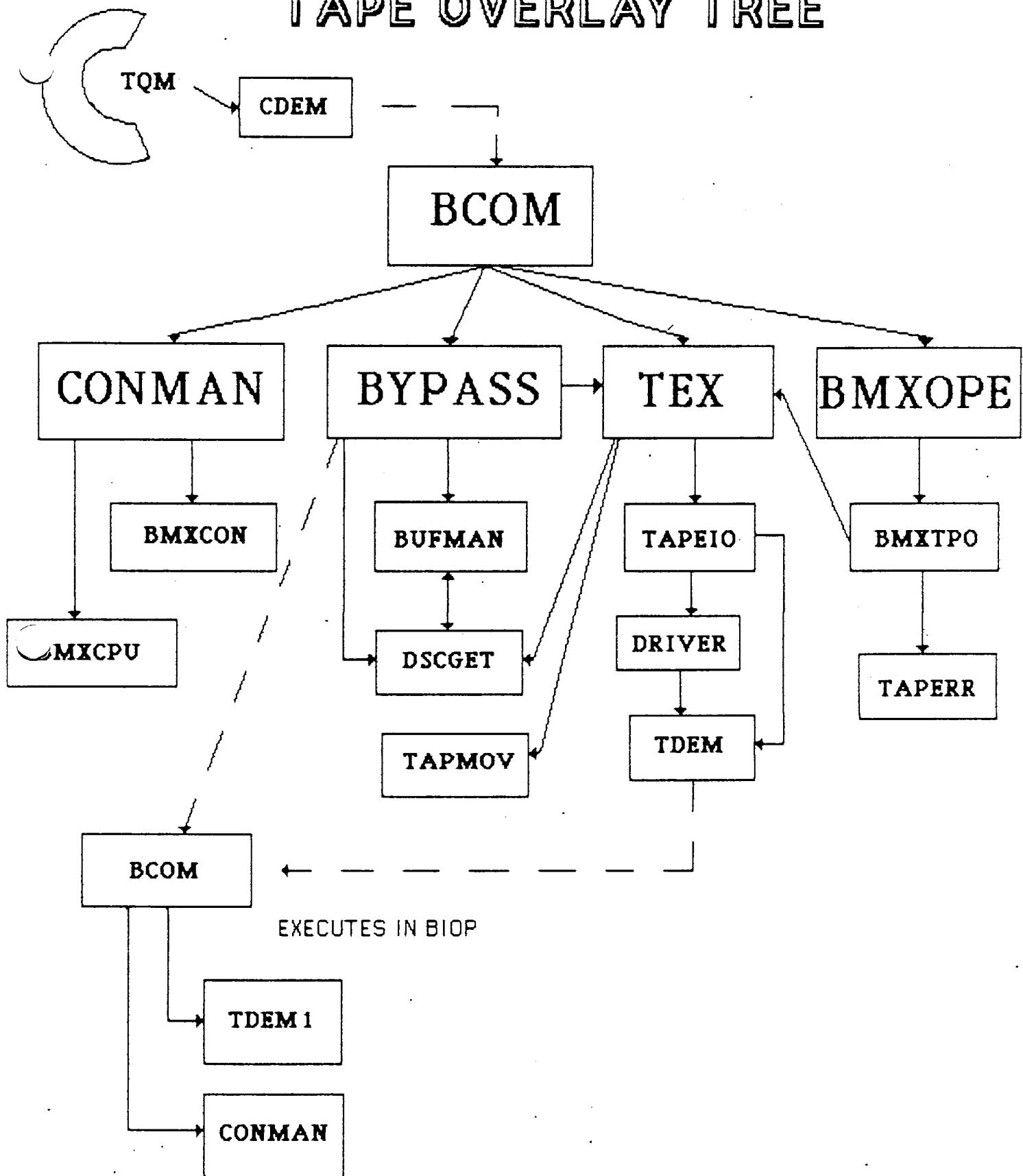
TAPEMOV Overlay:

Handles NONIO requests
Build CPW for driver and calls BMXIO

BLOCK MULTIPLEXER DRIVER

Performs device independent command and interrupt sequences.
Performs channel selection.
Performs channel error reporting.

TAPE OVERLAY TREE



USER CHANNEL SUBSYSTEM

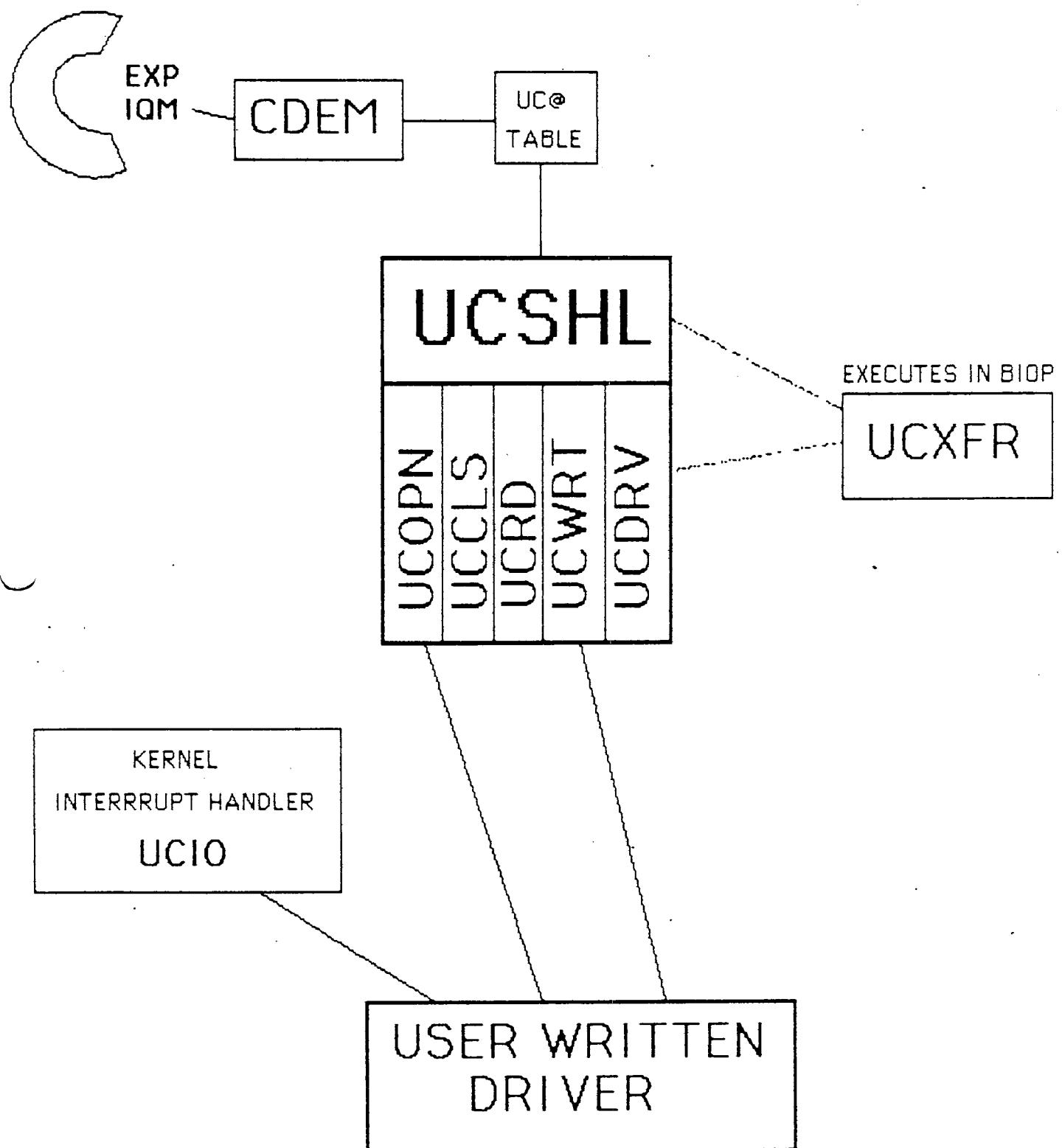
What is the user-channel shell?

- IOS overlays to process F-packets from the CPU. (UCSHL, CDEM, UCOPN, UCCLS, UCR, UCWRT)
- User-channel table (UCT).
- Kernel interrupt handler (UCIO).
- BIOP transfer overlay (UCXFR).
- Driver interface (SIGNAL/WATCH).
- Driver overlay.

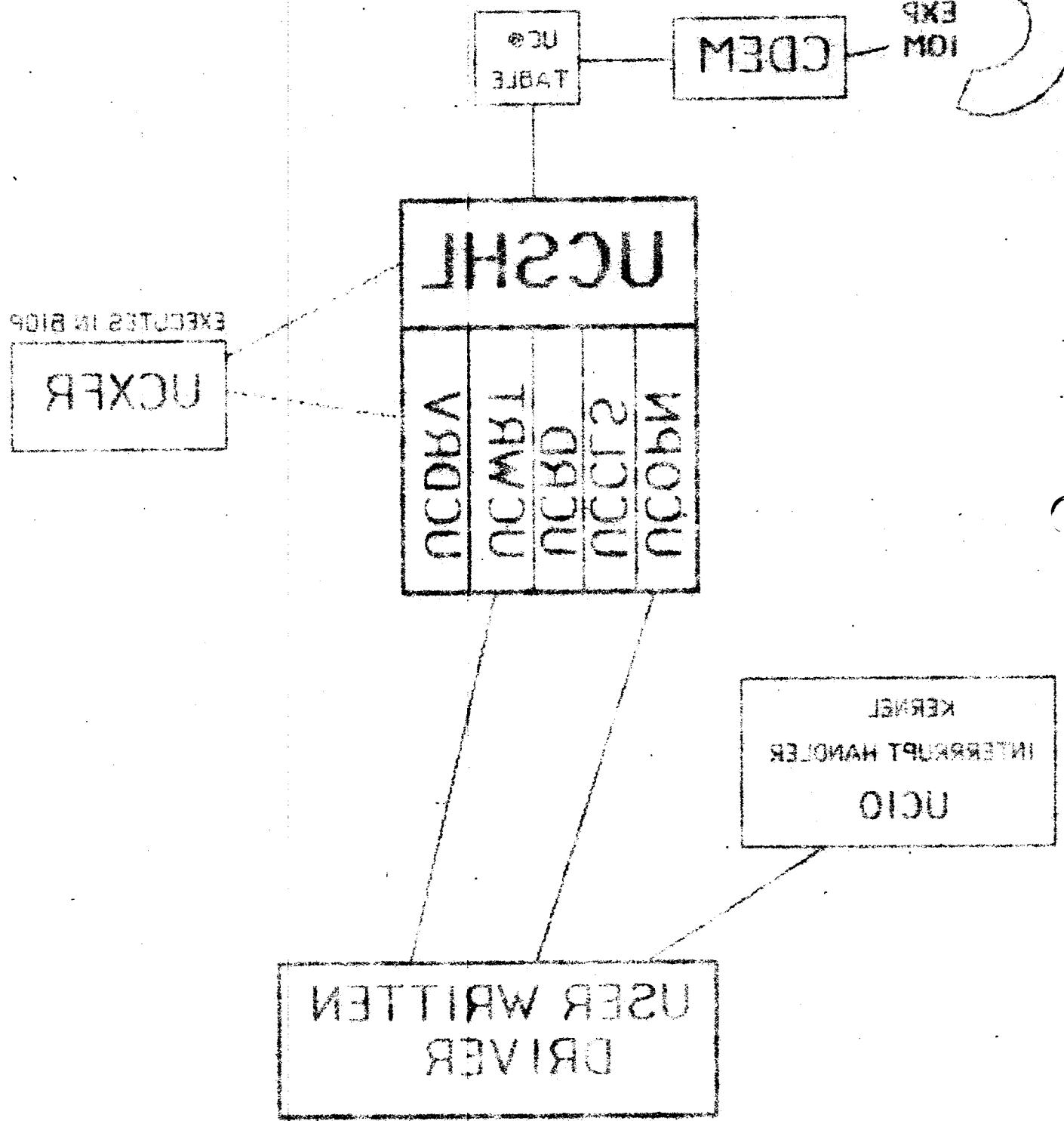
What is the purpose of the shell?

- Allows COS jobs to invoke custom IOS driver overlays.
- Customer can write special-purpose channel drivers.
- Interface to driver overlay is simple, standardized.
- Shell takes care of request queuing, replies to CPU, table creation, memory allocation, creation of driver activity and data transfer.
- Driver only has to be concerned with the channel and device characteristics.
- CPU invokes driver by name, causes shell to link it to the requested channel.
- Accessible via F\$DRIVER request from privileged COS jobs (see SM-0040, ch. 8).
- Accessible via common routine UDCOM for COS system tasks (STP). (See SM-0040, 4.10.)
- Independent shell and driver for input and output.
- Customer can implement new driver functions.

USER SHELL OVERLAY TREE



USER SHEET OF AERIAL



SYSTEM PROGRAMS

Match the following terms and their description

- | | |
|---------------------------|--|
| 1. STATION CALL PROCESSOR | ____ Inter IOP Message Handler for DD-19 and DD-29 disk activities |
| 2. \$APTEXT | ____ Definitions for COS tables and constants |
| 3. CONCIO | ____ Consists of one overlay |
| 4. IOP STATION | ____ Work for IOP to perform |
| 5. DEMON ACTIVITY | ____ Processes all User exchanges using S0 as the function code |
| 6. ACOM | ____ Contains definitions of macros, constants, and tables for IOS |
| 7. COSPL / IOPPL | ____ Considered a front-end to COS |
| 8. EXP | ____ Communicates between the station and COS |
| 9. COSTXT | ____ Source listings for the operating system |
| 10. ACTIVITY | ____ Processes FEI channel activities in the IOP |

SYSTEM B002A

Major goals followed future and parallel development

1. STATION CALL PROCESSOR → Major 108 message history for
B0-18 and DD-36 disk controller

2. 2456XT → Utilities for 108 CDS replace and
controller

3. CONCIO → Changes to the driver

4. 108 STATION → Work for 108 to pull out

5. DEMON ACTIVITY → Processors will use exchange code
number 29 as the function code

6. MODA-B → Controller definitions of words
conversative, string parser for 108

7. C002R \ 108P → Configuration is layout of C008

8. EXP → Communication interface definition
between 108 and B08

9. C002T → Some hardware for the parallel bidirectional
interface

10. ACTIVITOA → Processor for chip level software for 108

Program Switching and Control

3

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LEARNING OBJECTIVES

With the aid of all furnished reference materials, upon completion of the Program Swapping module, the learner should be able to:

1. Explain the mechanism with which COS switches programs.
2. Explain the mechanism with which IOS switches program.
3. Interpret the needed program executing environment.
4. Define an activity.
5. Interpret activity descriptors and storage modules.
6. Interpret Any Packets and DALS.

TASK EXCHANGING

EXEC

- Entered on an Interrupt
- Entered on every other exchange
- Processes reason for exchange
- Determines who to exchange back to

TASK

- Highest priority task exchanged back to first
- Determined from System Task Table
- All flags and channel interrupts are handled

USER

- Exchanged to if no tasks scheduled
- Users compete for CPU connection
- System Dataset Table holds queue
- Selected by class and priority

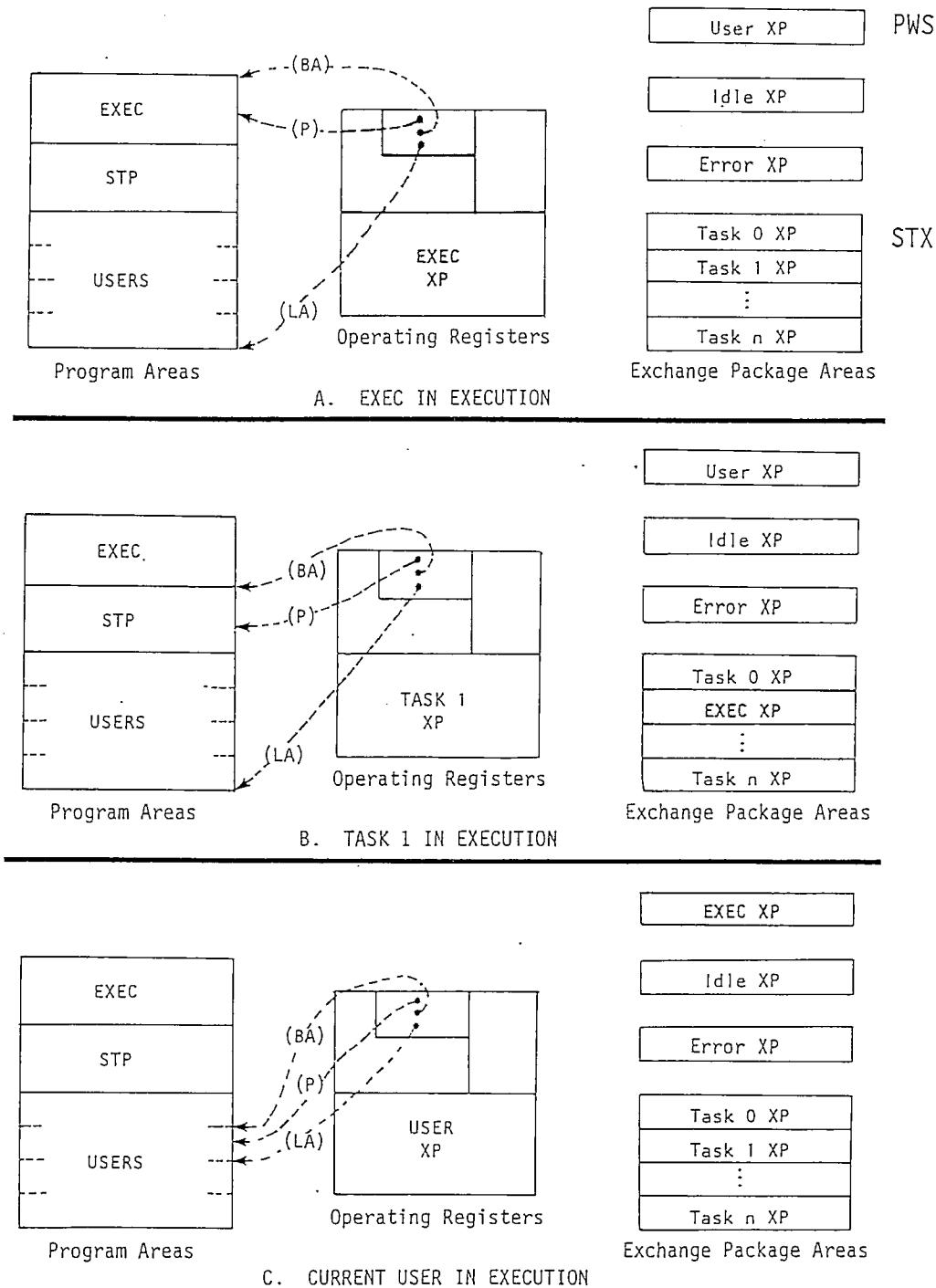
IDLE

- Exchanged to when nothing else is scheduled
- Reads memory looking for memory errors
- Reads Exec only

ERROR CORRECTION

- Logs error in MEL table
- Trys to correct error
- Logs the error in \$SYSTEMLOG through STP

EXEC'S EXCHANGE PACKAGE



TASK XP AREA

Located in Exec Table Area

System Task Table contains Exchange Package Address

Has ASCII Label of Task name in STT

Exchange Packages in same order as task IDs

Exec table pointers contain STX address

SYSTEM TASK TABLE

Function - for scheduling and controlling STP tasks

STT Header

STRTS Bit - request task scheduler flag

Active task ID

Active task exchange package address

Active task parameter block address

STT Part A - TPBs

One entry for each task

Ready bit

Suspend bit

Task Priority

Task ID

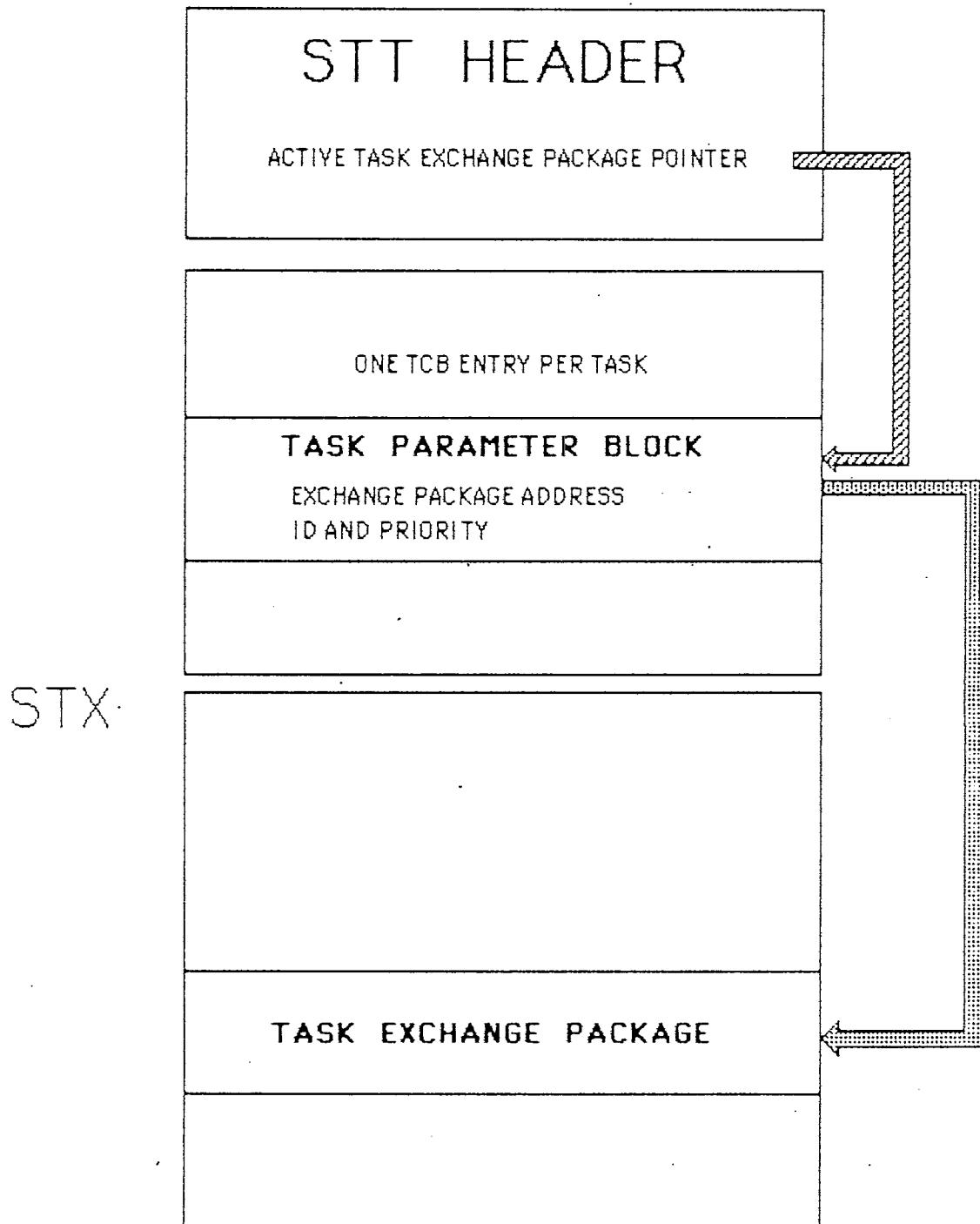
STT Part B - STX

One entry for each task

Contains the exchange package for the task

Located in the low memory XP area

SYSTEM TASK TABLE



ACTIVITY CHARACTERISTICS

Tasks executing in an IOP are called Activities.

An Activity is a routine (or routines) which perform a specific task.

Normally consists of nested Overlay calls.

Kernel maintains activities through the use of:

Activity Descriptors

Storage Modules (SMODs)

Popcells

DEMON ACTIVITIES

Activities may enable or disable interrupts.

A demon consists of one Overlay that performs high Priority Tasks.

Software Stacks (SMODS) are local memory resident.

Created on initialization by BEGIN overlay.

Activity Descriptors are never deallocated.

ACOM
AMSG
CDEM
DISK
BCOM
TDEM
CRTDEM
CLOCK

ACTIVITY DESCRIPTOR

Used by kernel to schedule and activate activities.

One for each activity.

Built by the kernel as a part of CREATE service request.

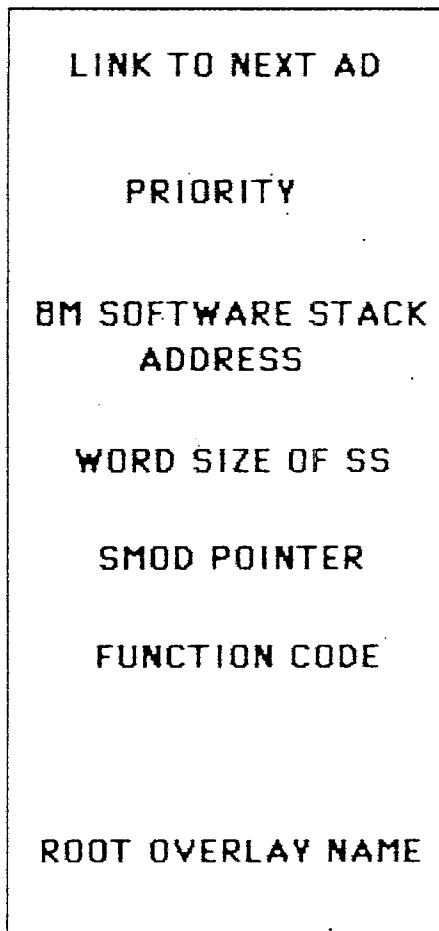
Contains links, addresses and other information necessary to manage an activity.

Local memory resident.

Exists until an activity is terminated except with demons.

ACTIVITY DESCRIPTOR

PARCEL 0



PARCEL 23
8

IOS STORAGE MODULE (SMOD)

Used to save an overlay's executing environment.

One per overlay read into local memory.

Size varies depending on how many registers need to be saved.

Minimally contains:

- Links to activity descriptor and previous SMOD
- Overlay information
- A, B, C, E, and P register contents

May contain:

- Operand register contents essential to its overlay
- Program exit stack entries for its' overlay

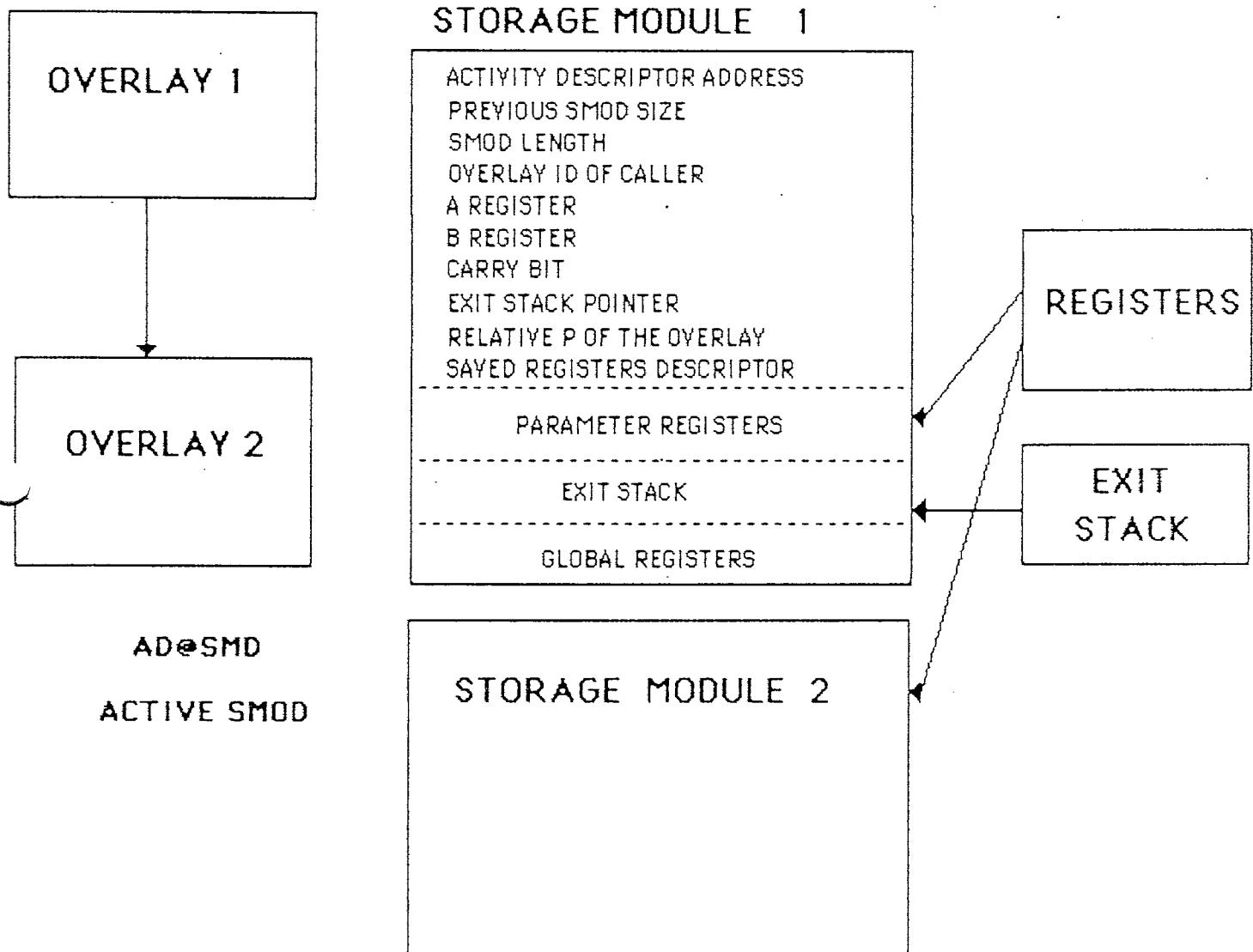
SMOD is partially updated when an overlay does a kernel service request.

If CALL results in loss of control, SMOD is completely updated.

Registers are re-loaded from SMOD when Service Request is completed or when overlay gets control back.

Initial SMOD set up through CREATE service request and is written to buffer memory as a software stack.

STORAGE MODULES



SOFTWARE STACK

There is a fixed stack in local memory where the SMODs for the current activity's overlays reside.

A SMOD is 'PUSHED' onto this stack when an overlay CALLS another overlay.

A 'PUSH' consists of saving an overlay's registers and updating the SMOD pointer in the activity descriptor.

The caller's SMOD is 'popped' off this stack when the called overlay does a RETURN service request.

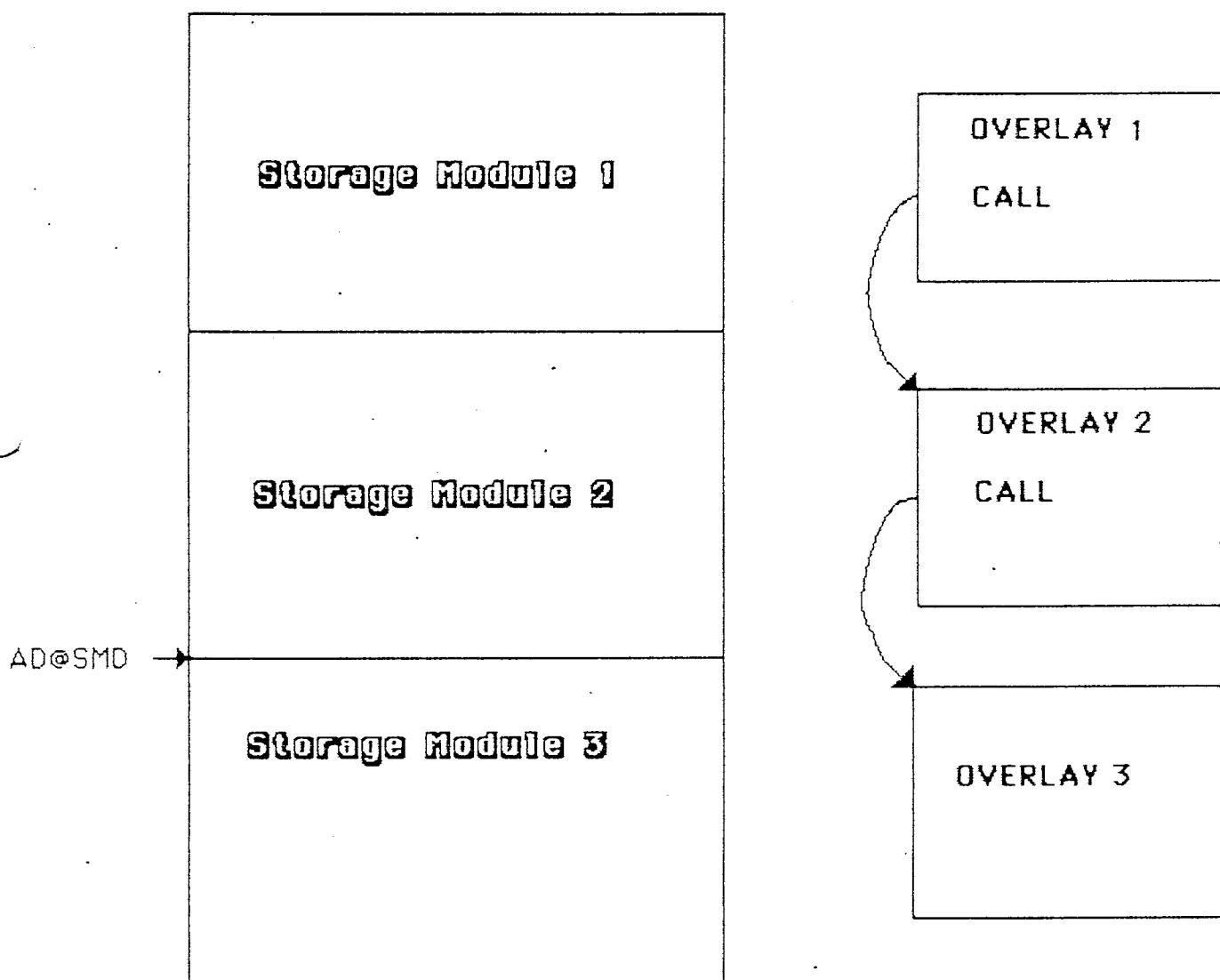
A 'POP' consists of updating the SMOD pointer and restoring the calling overlay's registers.

This software stack is written out to buffer memory when an activity relinquishes control to the kernel and other activities are on the central processor queue.

The local memory software stack is not free for use by another activity.

When an activity regains controls, its software stack will be read into the local software stack from the buffer memory.

SOFTWARE STACK



ANY PACKETS

~~DATA~~

~~RAW TFS~~

Messages between COS and IOS

6 word block of information

C1 Cray Mainframe ID

EX Exec IO

A Disk I/O

B Front End IO

C Error Message

D Tape IO

E Echo

F User Driver

G Tape Configuration

I Initialization I

J Initialization II

K Kernel

S N U L L

S Statistics

See SM-45 for Packet Descriptions

~~E DATA~~

~~1 elated opente~~

~~2 elated opente~~

~~3 elated opente~~

012004

ANY PACKETS

0	DID SID	
1		PACKET ID
2		
3	DATA BUFFER ADDRESS	PN CHN*
4	DISK LOCATION INFORMATION	
5		CYLINDER, SECTOR, HEAD, OFFSET

A PACKET - DISK REQUEST

0	DID SID	CHANNEL ORDINAL
1		
2		LOGICAL ID
3	LCP ADDRESS - IN and OUT	
4	SEGMENT BUFFER ADDRESS - IN and OUT	
5	LTP ADDRESS - IN and OUT	

B PACKET - FRONT END REQUEST

DISK ACTIVITY LINK

Communication packet between IOPs

40₈ parcels long

Any packet contained within the DAL

Sending IOP initiates by a Kernel service request in CDEM

Passed through buffer memory

Pointed to by inter A-A message (see SM-46 pp. 2-43)

Receiving IOP reads in with ACOM, BCOM or ICOM

Master DAL

Message packet created by a request from the mainframe

40₈ parcels in length

Built by MIOP from the CPU B packet

One for each I/O request

Passed to disk subsystem in appropriate IOP

Disk subsystem returns master DAL to MIOP when I/O request is completed.

Disk subsystem uses master DAL as a template for building executable DALS.

Executable DAL

Built from MDAL

Controls the transfer of one sector between disk and central memory

40₈ parcels in length

Built by disk subsystem from a master DAL

One for each sector of disk requested

Used by disk subsystem to keep track of where each sector of data is

Usually passed by DIOP to BIOP for high speed transfer requests

Returned when transfer complete

Also referred to as slave DAL or EDAL

DISK ACTIVITY LINK

PARCEL 0

LINK TO QUEUES
FUNCTION CODE
DAL ADDRESS
ACTIVITY DESCRIPTOR
INTER A-A MESSAGE
DESTINATION ID
SOURCE ID
REQUESTOR ID
COMMON MEMORY ADDRESS
PROCESSOR NUMBER
CHANNEL NUMBER
CYLINDER
HEAD GROUP
BLOCK LENGTH
BM BUFFER ADDRESS
LM BUFFER ADDRESS

PARCEL 37
8

DIG ACTIVITY LINE

LINK TO ONESES	PAGE 0
SUMCITION CODE	
DAI 400622	
ACTIVITY DESCRIPTION	
INTER A-A MEMBER	
DECOMMISSION ID	
SOURCE ID	
BUSINESS ID	
COMMON MEMBER ADDRESS	
PROCESSOR NUMBER	
CHANNEL NUMBER	
CHINIDER	
HEAD 48008	
BLACK ITEMID	
BN 80110 400622	
BN 80110 400622	PAGE 0

PROGRAM SWITCHING

Match the following terms and their descriptions

- | | |
|--|---|
| 1. EXCHANGE PACKAGE | _____ Defines an Activity in IOS |
| 2. DISK ACTIVITY LINK | _____ 6 words of information passed between COS and IOS |
| 3. SYSTEM TASK TABLE | _____ Activates ACOM, BCOM, ICOM giving Buffer Memory address of DAL being passed |
| 4. ACTIVITY DESCRIPTOR | _____ All SMOD's of an activity |
| 5. STORAGE MODULE | _____ Central Memory address for all COS Task exchange packages |
| 6. ANY PACKET | _____ Controls COS STP Tasks |
| 7. SOFTWARE STACK | _____ Central Memory location for user exchange packages |
| 8. STX | _____ Information passed through buffer memory to another IOP |
| 9. PROCESSOR WORKING STORAGE | _____ An Overlay program's executing environment |
| 10. INTER A-A MESSAGE
Interrupt Handler | _____ A COS mainframe programs executing environment |

- MASTER SITE FOLLOWING TERMS AND THEIR DEADLINES**
- 1. EXCHANGE PACKAGE**
T. Get base au ACCIAI A 102
- 2. DISK ACTIVITE LINK**
E. Works of telecommunication based
per new COB since 102
- 3. SYSTEM TASK TABLE**
A. Activities ACCIAI, BCOM, ICOM
during BATTel International activities
of DAF period based
- 4. ACTIVITY DESCRIPTOR**
A. All SWCO's to be suitable
- 5. STORAGE MODULE**
C. Cuts! International access 102 till
COB Task exchange based
- 6. AWA PACKAGE**
C. Cutleaf COB 216 tasks
- 7. SOFTWARE STOCK**
C. Cuts! International focus to 102
new exchanges based
- 8. STX**
T. Inclusion based already
partial members CO SWCO, ICOM
- 9. PROCESSOR WORKING STORAGE**
T. You can't just blocking a secondary
environment
- 10. INTER-A-A MESSAGE**
T. Full support for message
- 11. A MESSAGE**
A. COB multistation dialogue
exchange for message

System Resources

4

()

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LEARNING OBJECTIVES

With the aid of all furnished reference materials, upon completion of this System Resources module, the learner should be able to:

1. Draw out a Cray computer system.
2. Explain the main function of each hardware component.
3. Label all mass storage device areas.
4. Map central memory, common memory, and local memory.

HARDWARE CONFIGURATION

MIOP responsibilities are as follows:

- The MIOP is the first I/O Processor in the I/O Subsystem to be deadstarted. The MIOP initializes the contents of Buffer Memory and deadstarts the other processors in the configuration using Buffer Memory and accumulator channels to the other processors.
- The MIOP, in concert with the BIOP, deadstarts the mainframe.
- The MIOP handles all communication with the mainframe over the 6 Mbyte channel. This traffic includes disk and tape requests and station communications.
- The MIOP performs front-end and station software support.
- The MIOP handles input and output operations on the expander channel.
- The MIOP accepts information from the error channel and transmits it to the mainframe for inclusion in the system error log.
- The MIOP is the operator interface to the I/O Subsystem editor, which maintains deadstart and restart parameter files.
- The MIOP handles input and output operations on user channels for COS tasks.

BIOP responsibilities are as follows:

- The BIOP transfers data between Central Memory and Buffer Memory and vice versa across a 100 Mbyte channel.
- The BIOP performs disk I/O to and from disk units attached to its channels. (IOS software supports the DD-19, DD-29, and DD-49 Disk Storage Units.) It performs error recovery when errors are detected on data transfers.

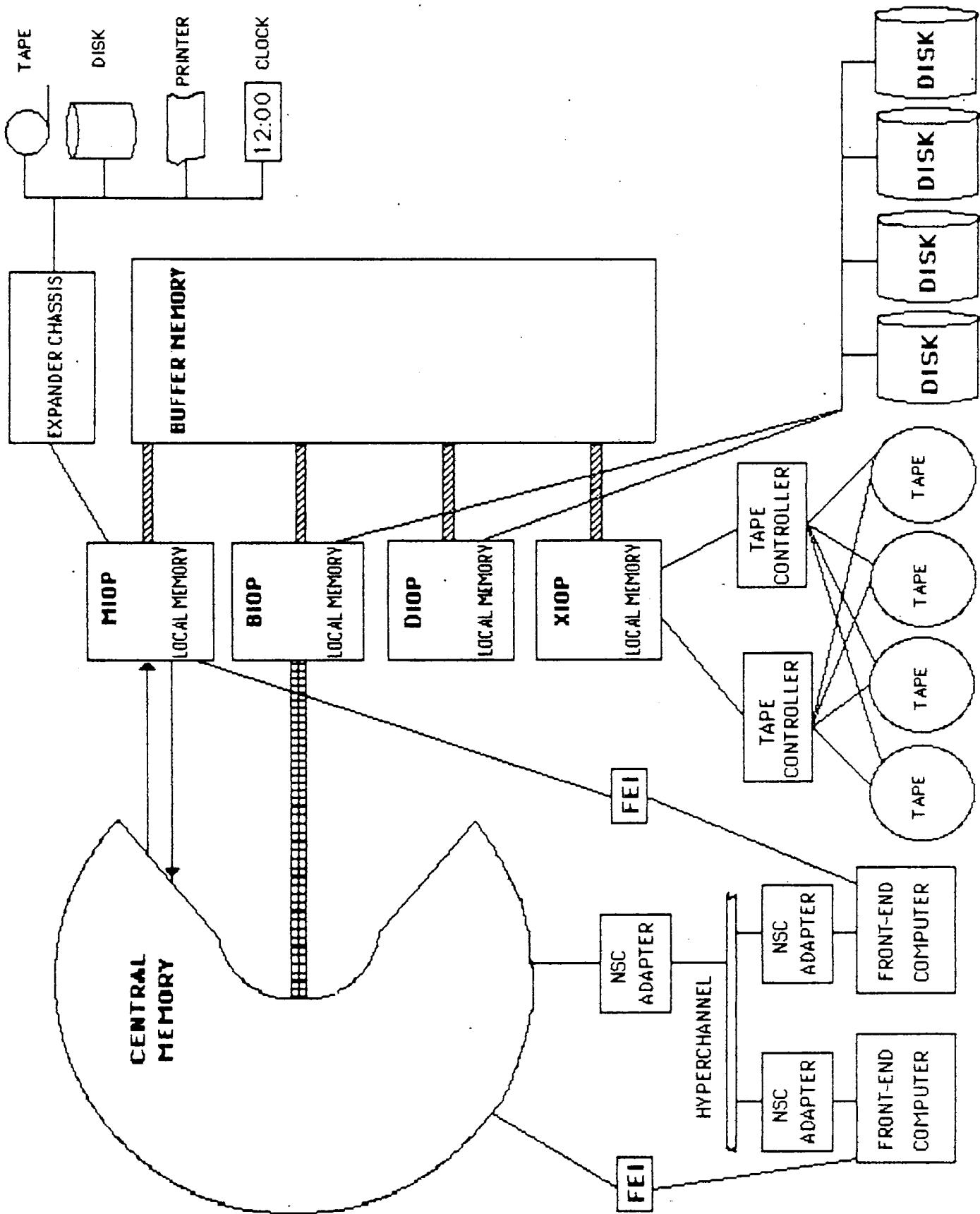
DIOP responsibilities are as follows:

- The DIOP moves data from Buffer Memory to disk and vice versa at the request of packets from the mainframe via the MIOP. These packets also return status to the requester.
- When errors are detected in data transfers to or from disk, DIOP software attempts error recovery.
- If the optional second 100 Mbyte channel is present, the DIOP transfers data between Central Memory and Local Memory and vice versa.

XIOP responsibilities are as follows:

- The XIOP handles data from IBM-compatible tape drives and buffers the data to Buffer Memory at the request of packets from the mainframe.
- When errors are detected while transferring data to or from tape, the XIOP performs error recovery procedures.

HARDWARE



MASS STORAGE

FEATURES	DD-19	DD-29	DD-49
Word Capacity / Drive	3.723×10^7	7.483×10^7	15.6×10^7
Word Capacity / Cylinder	92,160	92,160	175,000
Bit Capacity / Drive	2.424×10^9	4.789×10^9	20×10^9
Cylinders / Drive	411	823	891
Sectors / Track	18	18	44
Bits / Sector	32,768	32,768	32,768
Number of Heads	40	40	32
Latency	16.7ms	16.7ms	16.7ms
Access Time	15-80ms	15-80ms	2.5-32ms
Data Transfer Rate	35.4×10^6	35.4×10^6	200×10^6

Device Label Table - DVL

A device label table exists on the first usable track of a mass storage device and is created by STARTUP during an install. The device label table contains a bad track label for the device. The device label for the master device also contains pointers to the Dataset Catalog.

Engineering Flaw Table - EFT

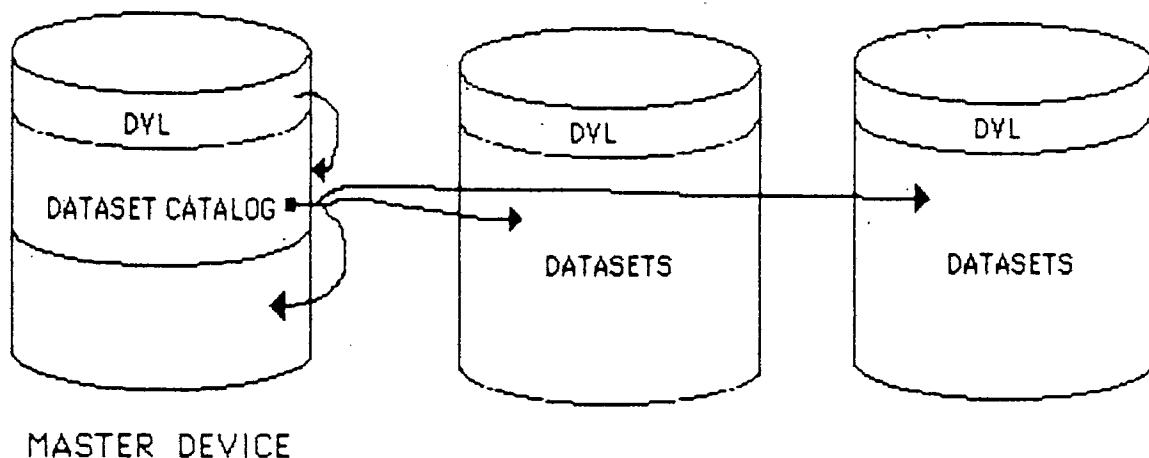
Engineering diagnostics use this disk-resident table to record flaws encountered during a surface analysis of a disk pack. STARTUP uses the table to make flawed tracks unavailable to COS.

The EFT is written to Sector 20 octal of the first track that can be successfully reread on the device (no more than 10 tracks are tried). No EFT is written if no track in the first 10 tracks can be written and reread successfully. Install reads back each DVL after writing it to verify the integrity of the DVL. If the Device Label cannot be read back perfectly, then the track is overwritten with a test pattern and a different track is tried.

Formatting

Before a unit can be introduced into the system, it must be formatted. Formatting is the process of writing cylinder, head, and sector addresses onto the disk drive. This process is performed off-line by field engineers, and unless addressing information has been inadvertently destroyed, formatting is performed only once.

DISK STORAGE



MASTER DEVICE

DEVICE LABEL

DLB - DEVICE LABEL		MASTER DEVEICE FLAG
LOGICAL DEVICE ID	49-A1-20 39A-A2-27	
MASTER DEVICE DATASET CATALOG (DSC)		

GRAY STATION. VERSION X.15, IOS L S R M 01/08/85 15:47:41

MASS STORAGE STATUS

FRAME 0

FLAGS	DEVICE	SPACE		ERRORS		LAST CYL	ERROR HD	SC
		FREE	PERM	RECOV	UNREC			
M P	49-A1-20	47%	53%	0	0	0	0	0
DP	49-A1-21	100%	0%	0	0	0	0	0
S	39-1-22A	74%	0%	2	0	3	4	0
S	39-1-22B	99%	0%	0	0	0	0	0
S	39-1-22C	99%	0%	8	0	12	3	0
S	39-1-23A	76%	0%	23	0	52	3	0
S	39-1-23B	99%	0%	7	0	14	3	0
S	39-1-23C	99%	0%	0	0	0	0	0
	49-H1-24	28%	67%	0	0	0	0	0
	49-A1-25	32%	64%	0	2	0	0	0
P	49-A1-26	100%	0%	0	0	0	0	0
	49-A1-27	38%	60%	5	0	633	3	45
	49-A1-30	28%	69%	0	0	0	0	0

>STO
>SNA

LOCAL MEMORY

The Kernel resides in local memory in each IOP.

The Kernel maintains four local memory chains.

Overlay Memory Chain:

- Located after Kernel
- Allocated in multiples of four parcels
- Implemented as a doubly linked list
- Used to hold executing overlays
- Operand register %MEMORY points to the beginning of the list
- Operand register %B points to the active overlay

Disk Activity Link Chain:

- Located after overlay memory
- Allocated in multiples of 40_8 parcels
- Implemented as a forward linked list
- Used for message space
- Memory location EDES points to the beginning of the list

Free Memory Chain:

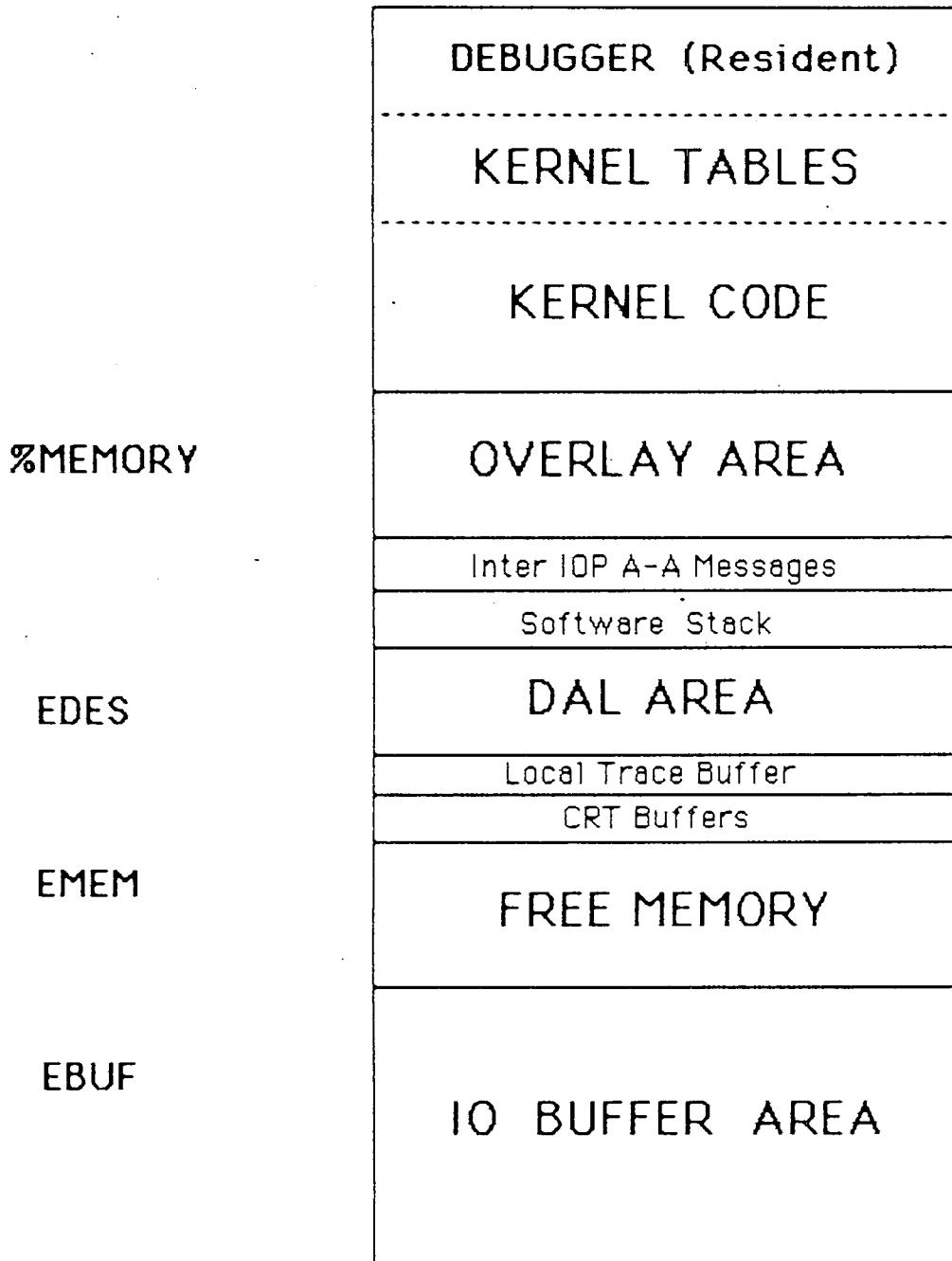
- Located after DALS
- Allocated in multiples of four parcels
- Implemented as a forward linked list
- Used for tables, activity descriptors and popcells
- Memory location EMEM points to the beginning of the list

Local I/O Buffer Chain:

- Located in upper memory
- Allocated in multiples of 4000_8 parcels
- Implemented as a forward linked list
- Used mainly for I/O buffers
- Memory Location EBUF points to the beginning of the list

These pointers are built on initialization in the Kernel SIN routine or in the SYSS.

LOCAL MEMORY



BUFFER MEMORY

Deadstart Buffer

Image of the Kernel for initialization

System Directory

Pointers to Buffer Memory Address boundaries

Disk Activity Link Area

Interprocessor DAL area

Size of area set in AMAP for each IOP

Each message area is 40₈ parcels

AMAP

Configuration of IOP channels

Text Overlay

Overlays

IOS System Programs

Read only Programs

LIST0 will give BM address of each overlay

Kernel Storage

Tables and Queues

Software Stack Area

An overlay's execution environment is saved here when it loses control of the CPU

Allocated in 400₈ blocks

I/O Buffer

IO dataset area

Trace Buffer

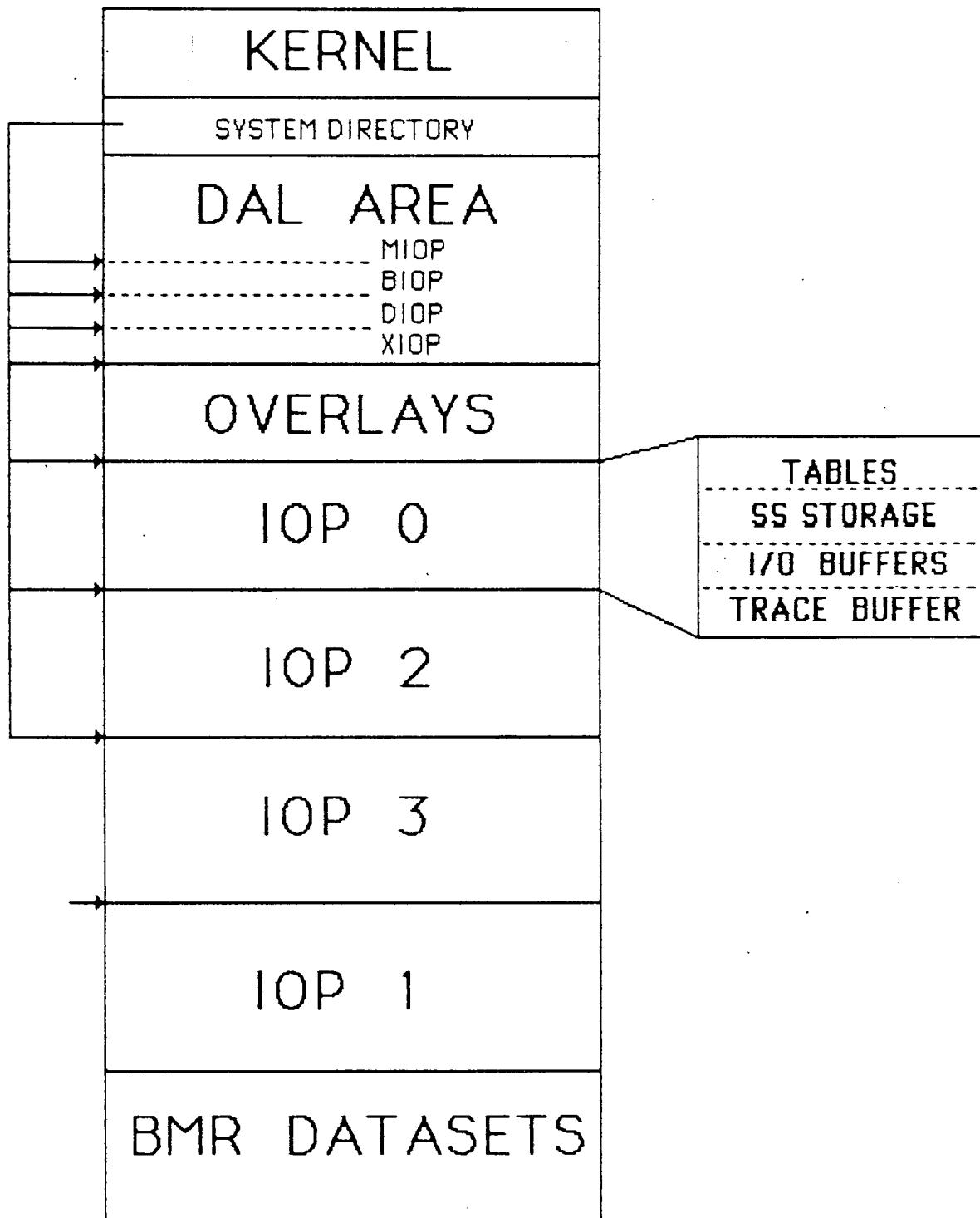
Circular history trace for debugging

Buffer Memory Resident Datasets

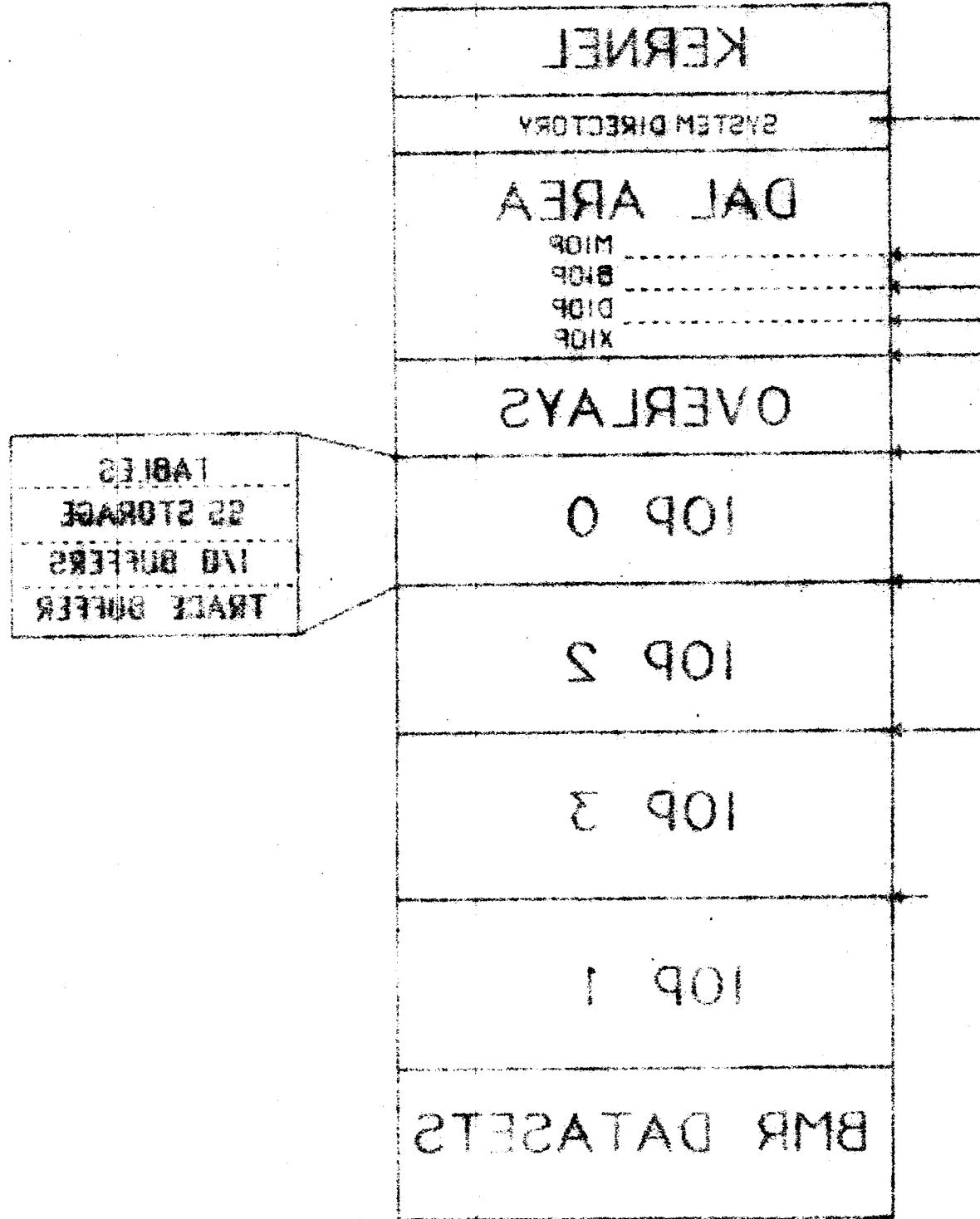
Mass storage area usable from JCL

Logical Device name is BMR-00

BUFFER MEMORY



BUFFER MEMORY



SYSTEM DIRECTORY

Parcel	Bits	Description
0-1	0-15	MIOP message area
2-3	0-15	BIOP message area
4-5	0-15	IOP-2 message area
6-7	0-15	IOP-3 message area
10-13	0-15	Reserved
14-15	0-15	First overlay (AMAP) address
16-17	0-15	Unused
20-21	0-15	MIOP Kernel storage area
22-23	0-15	Size of MIOP storage area in 512-word blocks
24-25	0-15	BIOP Kernel storage area
26-27	0-15	Size of BIOP storage area in 512-word blocks
30-31	0-15	IOP-2 Kernel storage area
32-33	0-15	Size of IOP-2 storage area in 512-word blocks
34-35	0-15	IOP-3 Kernel storage area
36-37	0-15	Size of IOP-3 storage area in 512-word blocks

CENTRAL MEMORY

EXEC

- Exchange Package Area
- Table Area
- Monitor Instructions
- Interrupt Handlers
- Exec Request Processor
- Channel Drivers

System Task Processors

- 15 programs loaded after EXEC Handles Resource Needs and Scheduling
- Table Area within Task Area Loader Map Gives Relative Address

Job Table Area

- Job Related Information
- User Log
- User JCL
- Exchange Package
- B,T,V Registers
- Dataset Name Tables
- Not Accessible to User
- Can be Dumped

User Area

- Job Communication Block
- User Program/Data
 - Control Statement Processor
 - Product Set
 - User Program
- Logical File Tables
- Dataset Parameter Area
- I/O Buffers

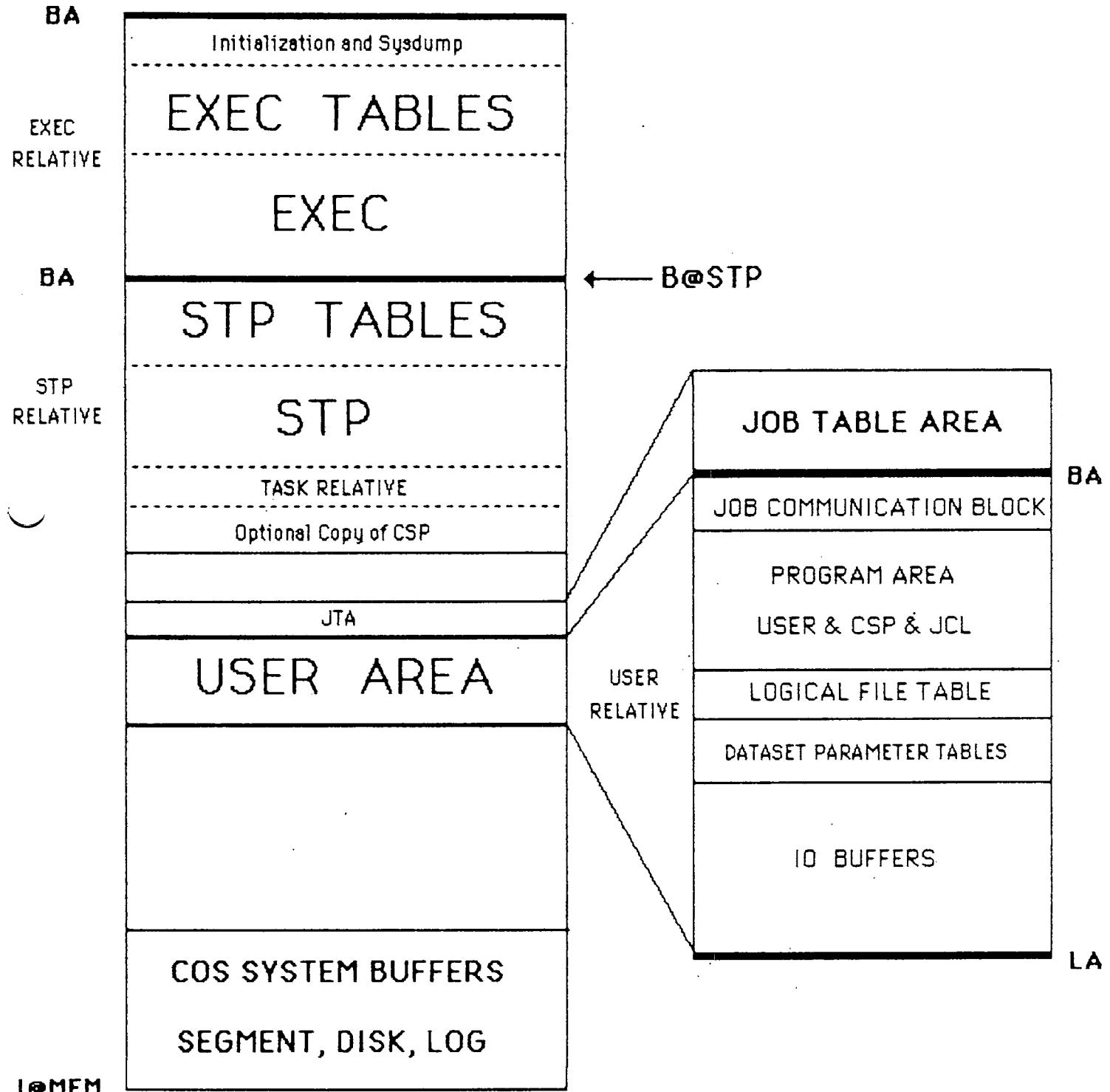
System Log Buffer

- Used by MSG and MEP

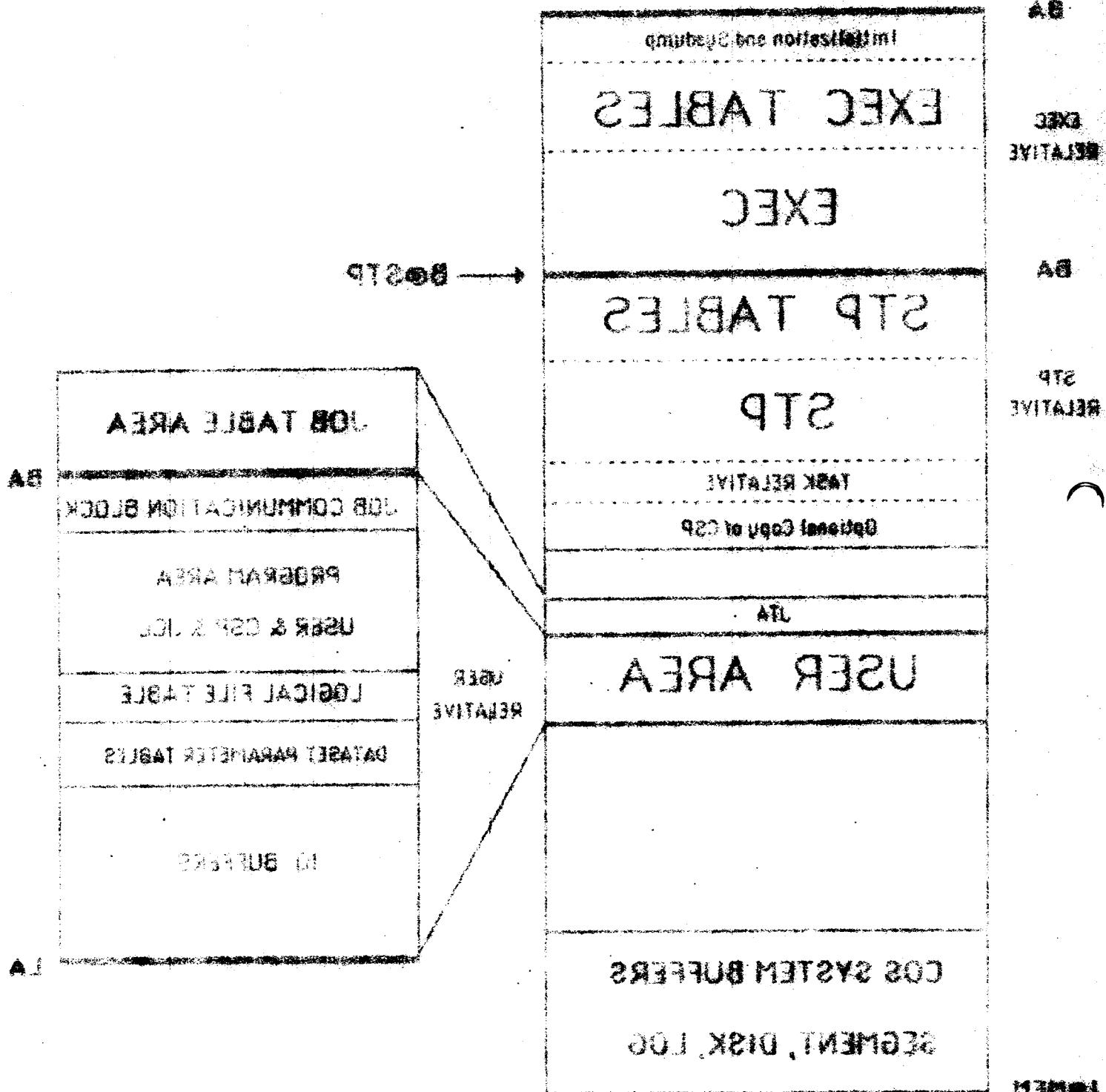
System IO Buffers

- Disk.
- Segment

CENTRAL MEMORY



CENTRAL MEMORY



SYSTEM RESOURCES

Match these terms and their description:

- | | |
|---------------------|--|
| 1. MASTER DEVICE | <input type="checkbox"/> Considered part of the IOP station |
| 2. MIOP | <input type="checkbox"/> Logically considered a fast disk storage device |
| 3. BIOP | <input type="checkbox"/> Main responsibility is handling the NSC hyperchannel and Front End channels |
| 4. DIOP | <input type="checkbox"/> Main Responsibility is to drive IBM type Block mux devices and tape units |
| 5. XIOP | <input type="checkbox"/> Contains the Dataset Catalogue |
| 6. SSD | <input type="checkbox"/> Contains a High Speed channel for use by MIOP for front-end activities accessed by STATIO |
| 7. BUFFER MEMORY | <input type="checkbox"/> Main responsibility is to drive Disk Storage devices |
| 8. EXPANDER CHASSIS | <input type="checkbox"/> Memory used by each IOP for monitor and executing IOS activities |
| 9. CENTRAL MEMORY | <input type="checkbox"/> Working storage for IOP activities |
| 10. LOCAL MEMORY | <input type="checkbox"/> Memory where COS resides |

SYSTEM REQUIREMENTS

Major system features and their description

1. **MASTER DEVICE**
 - Configuration detail of the I/OB
 - Selection
 - Loadability considerable & fast
 - Quick start mode devices
2. **WORK**
 - Main responsibility is handling
 - CPU HPC processing one float and character
3. **I/OB**
 - Main responsibility is to drive
 - IBM type B disk with devices and fast access
4. **X/I/OB**
 - Contains the greatest complexity
5. **SCD**
 - Contains a High speed counter
 - Use by I/OB for time keeping
 - Suitable successor of S/TATIO
6. **RAM/ROM MEMORY**
 - Main responsibility is to store
 - Quick start mode devices
7. **PROGRAMMING**
 - Memory area of about 100 Kbytes
 - Monitor and supervisor I/O
 - Diagnostic
8. **CENTRAL MEMORY**
 - Workload storage for OB
 - Diagnostic
9. **LOCAL MEMORY**
 - Memory areas C001-C010

Station Protocol

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LEARNING OBJECTIVES

With the aid of all furnished reference materials, upon the completion of this Station Protocol module, the learner should be able to:

1. List the difference between networking and stations.
2. Explain what a station is.
3. List some functions of a station.
4. Break down and interpret a station message Link control package.
5. Explain the sequence a station goes through for a log-on.

WHAT IS A STATION?

The hardware and software connecting a front-end computer to the Cray.

Functions:

Dataset Transfer

Input and Output datasets

Acquire, Fetch and Dispose datasets

Operator Commands

Control

Status

Interactive

Maintenance Control Unit (MCU)

Deadstarting

Dumping

NETWORK VS. STATION

Network

Generalized

Hosts are peers

Common protocol

Symmetric functions

Telecommunication

Error recovery important

Station

Serves the CRAY

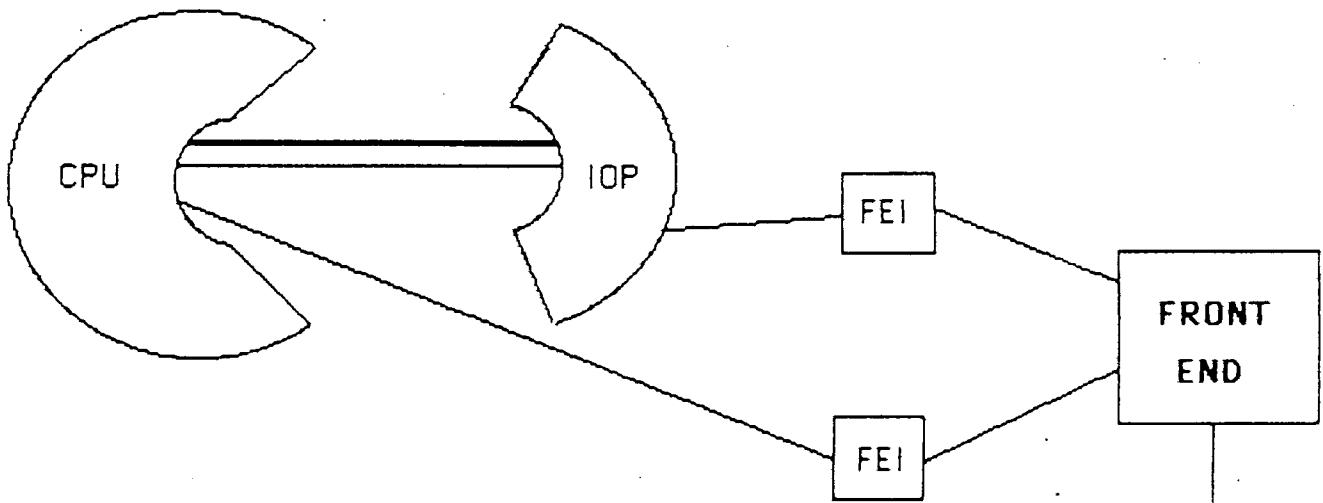
CRAY is the focus

CRAY-oriented protocol

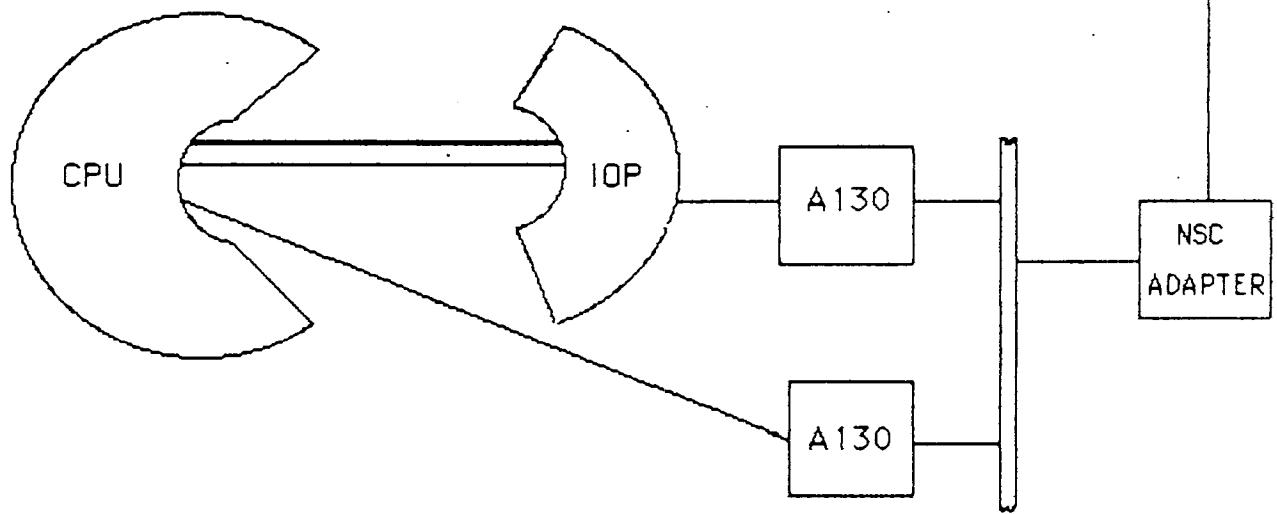
CRAY-oriented functions

Channel to channel

Error rate insignificant



FRONT END STATION



NSC HYPERCHANNEL

NSC HYPERchannel

High-speed digital communications.

Interconnect computers and/or computer peripheral subsystems at full channel rates over long distances.

Maximum rate 50 million bits per second.

Coaxial cable data trunks.

Equipment attached to trunk with adapters.

NSC HYPERchannel ADAPTERS

Function:

Operate coaxial data trunks (up to 4).

Transfer data to and from the coaxial data trunk.

Interface to different computers or peripherals (signal level, protocol).

Processor Adapter - Interfaces a processor to the HYPERchannel network.

A110 - CDC 6000

A120 - CDC 7600

A130 - Cray Low Speed

A140 - Univac 1100

A220 - IBM 370

A410 - DEC

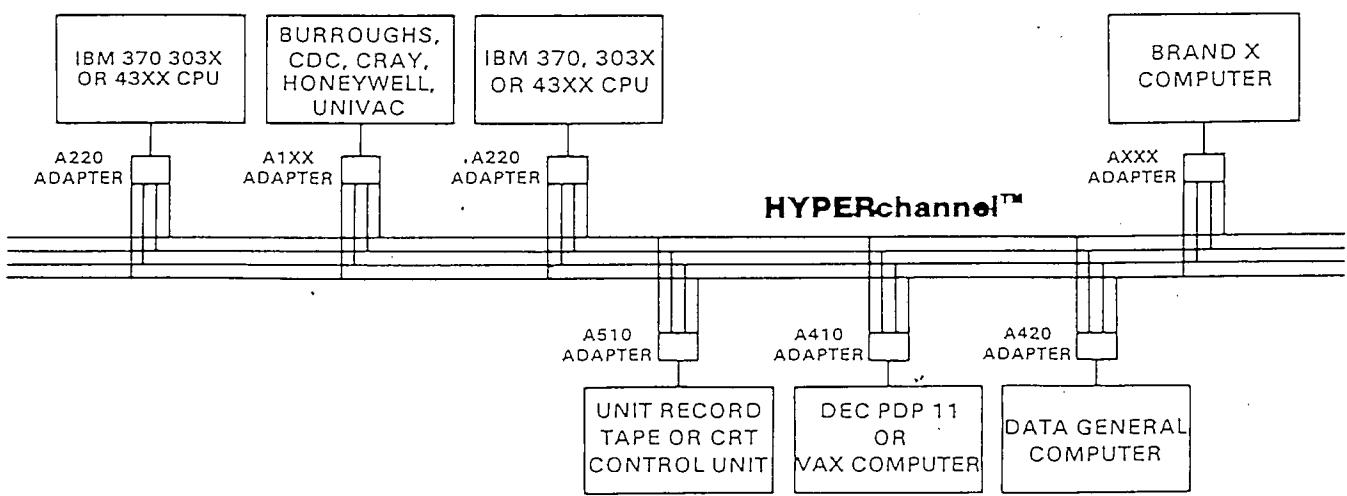
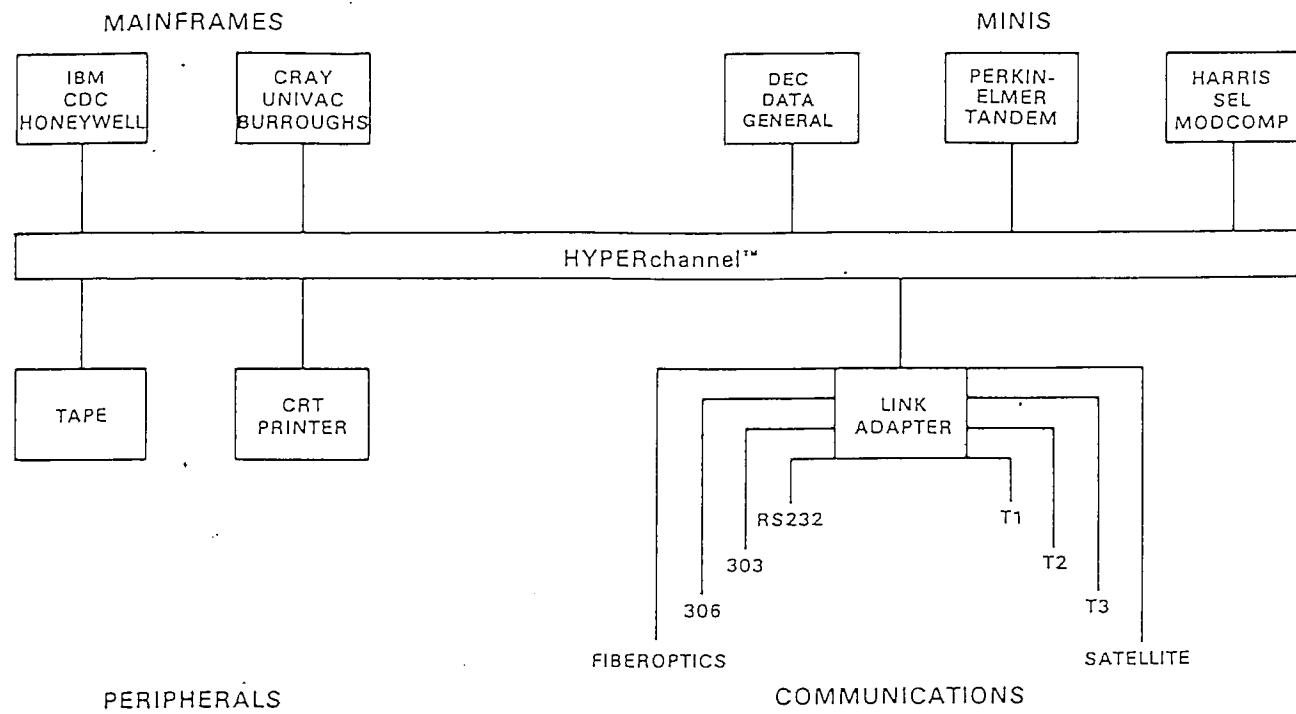
A420 - DG

Device Adaptor - Attaches a peripheral subsystem to the HYPERchannel.

Link Adaptor - Allows two HYPERchannel Networks to communicate over
Microwave or Bell T1 links (A710).

Satellite Link Adaptor - Allows communication between HYPERchannel networks
over a satellite link (A720).

HYPERCHANNEL NETWORK



MESSAGE

A group of related transmissions in the same direction is called a message.

A message consists of:

LCP

0-N Subsegments

LTP (optional)

An entire message is sent in one direction. The computer receiving the message waits for the complete message before it sends a message of its own in the opposite direction.

SEGMENT

Datasets are transmitted in segments.

A segment is always preceded by an LCP.

A segment may be made up of one or more subsegments.

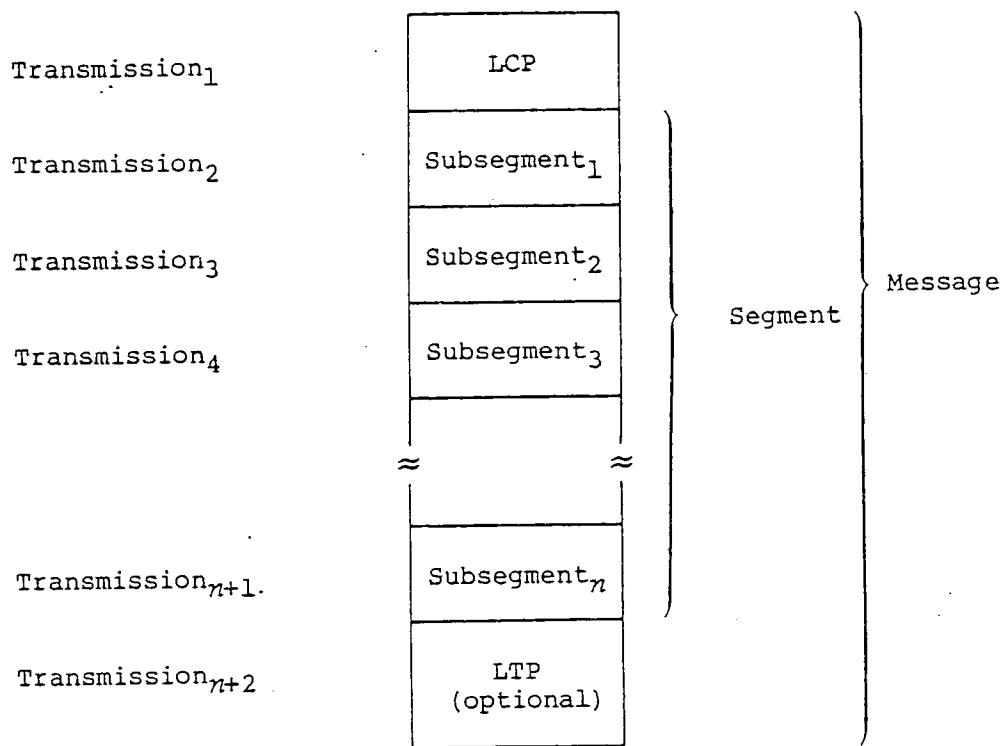
SUBSEGMENT

A block of data that is part of a segment.

It is the largest amount of data that is transmitted across the interface at the driver level.

Multiple of word size on CRAY and front-end machine.

A full subsegment is always transmitted.



Transmission units

CRAY STATION. VERSION X.15, IOS. L S R 10/31/84 00:50:52

LINK STATUS							FRAME 0	
ID	QUEUE COUNT	ACTIVE I O	MAXIMUMS I O AC	SUBSEGMENT #	SIZE	CH, ORD		
AP	0	0 0	1 2 2	1	512	4, 1		
LL	0	0 0	8 8 16	1	512	4, 11		
UX	0	0 0	1 2 3	1	512	4, 12		
M4	0	0 0	4 4 5	1	512	4, 3		
DG	0	0 0	1 1 1	1	128	5		
V3	0	0 0	8 8 16	1	1024	4, 10		
M6	0	0 0	2 2 4	1	512	6		
IC	0	0 0	0 0 0	1	512	6		
UB	0	0 0	0 0 0	1	32	4, 14		
DX	0	0 0	2 2 4	1	512	4, 15		
UA	0	0 0	0 0 0	1	32	4, 13		
II	0	0 0	0 0 0	1	512	4, 8		
Z3	0	1 0	2 2 4	1	3800	4, 16		

>-
>SNAP

LINK CONTROL PACKAGE (LCP)

Six 64-bit words.

LCP fields:

DID - destination mainframe identifier; 2 ASCII alphanumeric characters.

SID - source mainframe identifier; 2 ASCII alphanumeric characters.

NSSG - number of subsegments in a segment.

MN - message number.

MC - message code.

MSC - message subcode.

RQP - request pending flag.

STN - stream number.

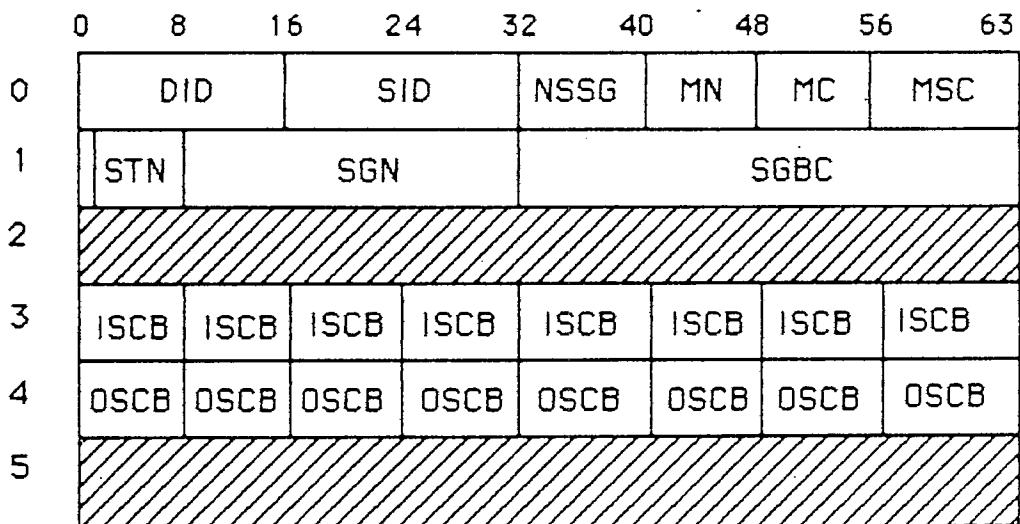
SGN - segment number.

SGBC - number of data bits in segment.

ISCBn - input stream control byte.

OSCBn - output stream control byte.

LINK CONTROL PACKET



<u>Field</u>	<u>Word</u>	<u>Bits</u>	<u>Description</u>
DID	0	0-15	Destination mainframe identifier; 2 ASCII alphanumeric characters.
SID	0	16-31	Source mainframe identifier; 2 ASCII alphanumeric characters.
NSSG	0	32-39	Number of subsegments in segment (0-n)
MN	0	40-47	Message number (0-255)
MC	0	48-55	Message code
MSC	0	56-63	Message subcode
RQP	1	0	Request Pending flag. Station messages are queued by COS, and are sent on receipt of any message not requiring a response, such as a control message. See preceding text.
STN	1	4-7	Stream number (1-8)
SGN	1	8-31	Segment number (0-total number of segments)
SGBC	1	32-63	Number of data bits in segment
ISCB _m	3	0-7, 8-15,etc.	Input stream <i>m</i> control byte [†] (stream ordinal <i>m</i> +8)
OSCB _n	4	0-7, 8-15,etc.	Output stream <i>n</i> control byte [†] (stream ordinal <i>n</i> +8)

STREAM

Software convention for multiplexing dataset transfer.

All messages related to a single dataset transfer including status.

Identified by a stream number.

Number of streams set via front-end parameters.

Maximum of 16 streams.

8 maximum input streams.

8 maximum output streams.

STREAM CONTROL BYTES (SCB)

Types of stream control bytes:

Input stream control bytes (ISCB).

Output stream control bytes (OSCB).

Function of SCBs:

Indicates status of a stream.

Request to take action on the stream.

Response to a request on a stream.

Included for all defined streams; ignored for undefined streams.

Checked for each message unless there is an error.

I/O relative to the LCP sender.

STREAM CONTROL BYTES

Octal Code	Mnemonic	Request/response	Sender	Receiver
00	IDL	Idle	X	X
01	RTS	Request to send	X	
02	PTR	Preparing to receive		X
03	SND	Sending	X	
04	RCV	Receiving		X
05	SUS	Suspend		X
06	END	End of dataset	X	
07	SVG	Saving dataset		X
10	SVD	Dataset saved		X
11	PPN	Postpone	X	X
12	CAN	Cancel	X	X
13	MCL	Master clear	X	X

STREAM CONTROL BYTE FLOW

Front-end is logged on.

Communications in an idle state.

Front-end sends RTS to the CRAY.

CRAY sends RCV to the front-end.

Front-end sends SND to the CRAY along with the job dataset.

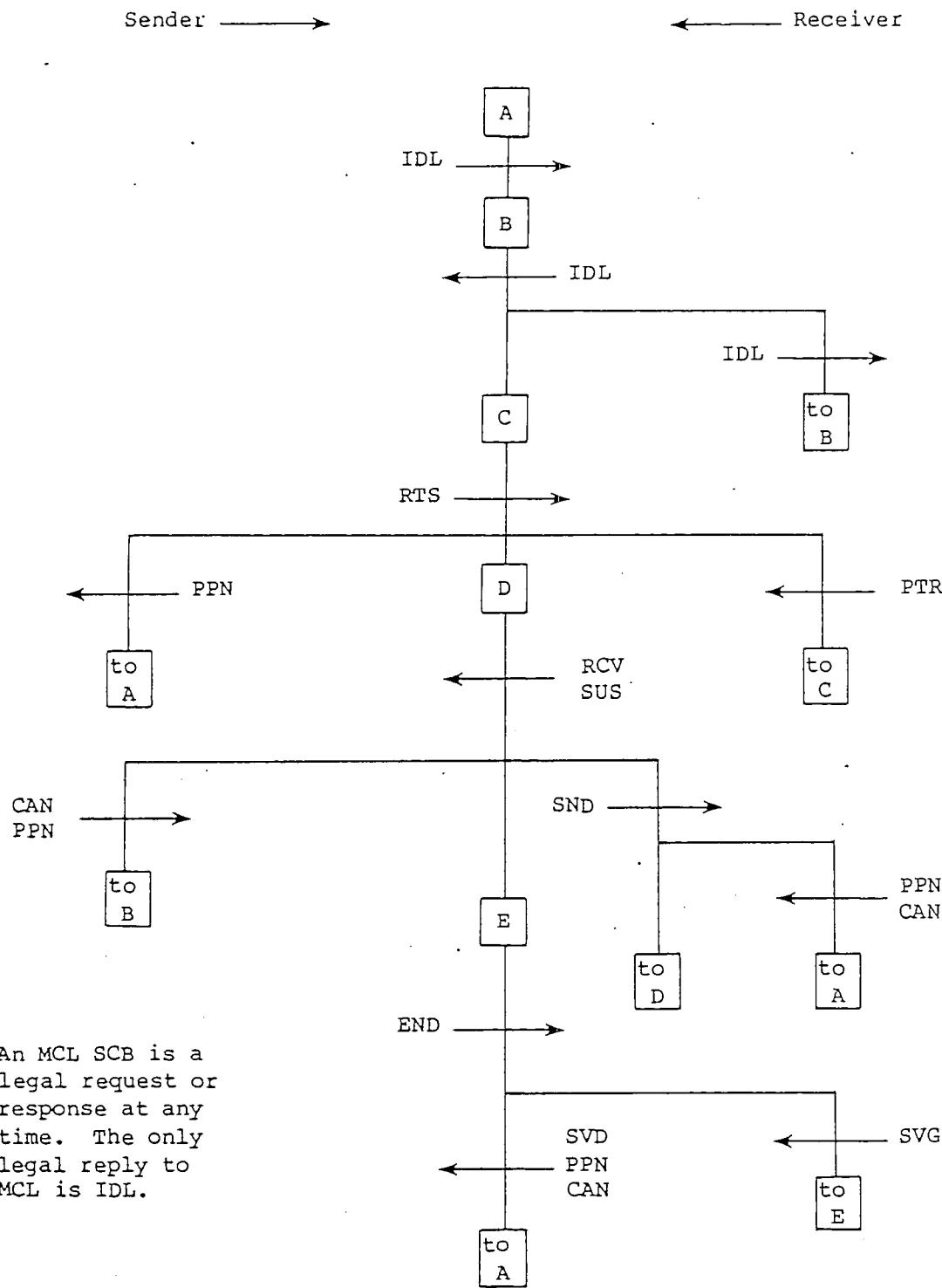
CRAY sends RCV to the front-end while decoding the message and saving the job dataset.

Front-end sends end to the CRAY until the CRAY has saved the dataset.

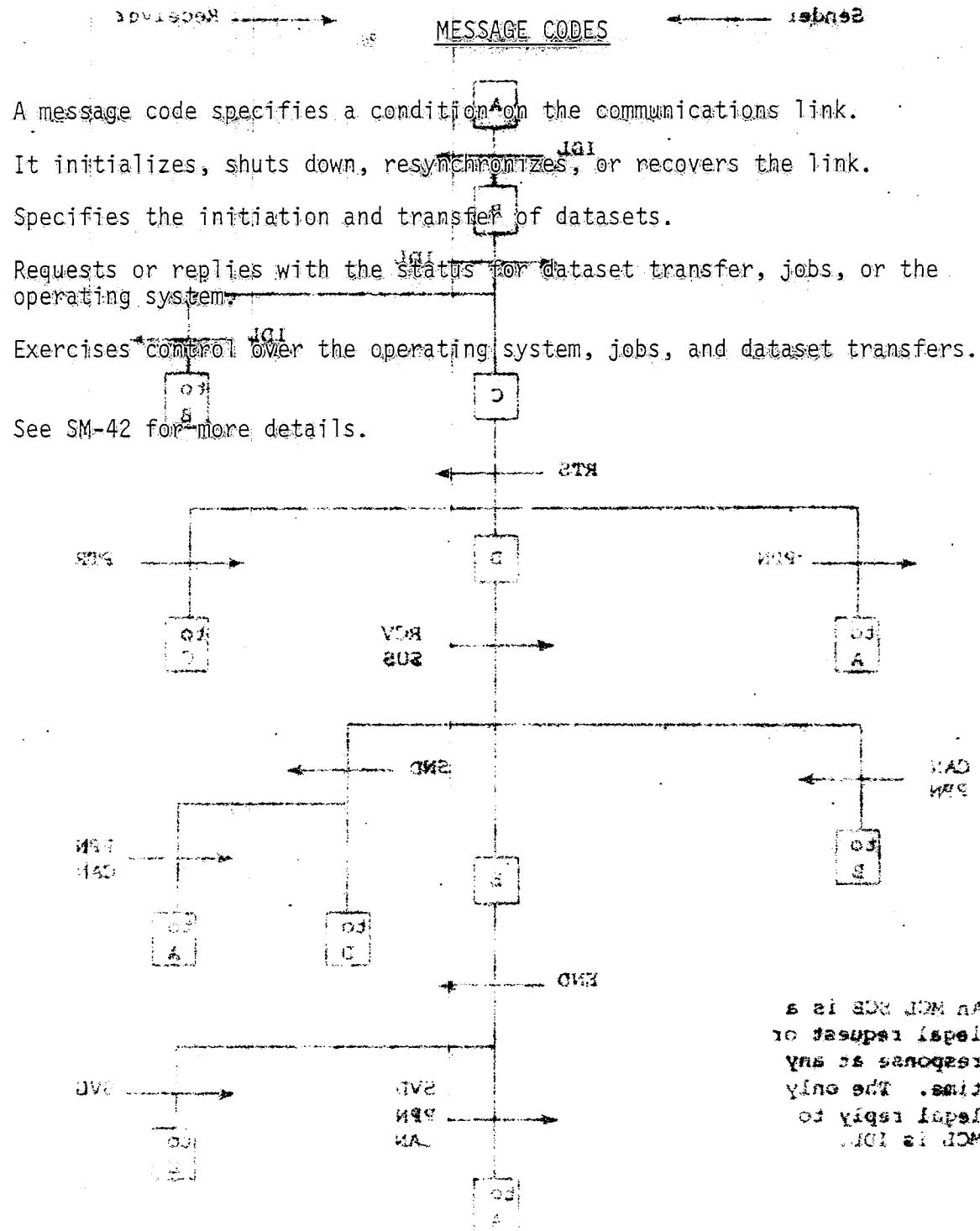
CRAY sends SVD to the front-end once the dataset has been saved.

The front-end and the CRAY then keep communications open by alternately sending and receiving IDL.

STREAM CONTROL BYTE FLOW



STREAM CONTROL BYE FLOW



MESSAGE CODES

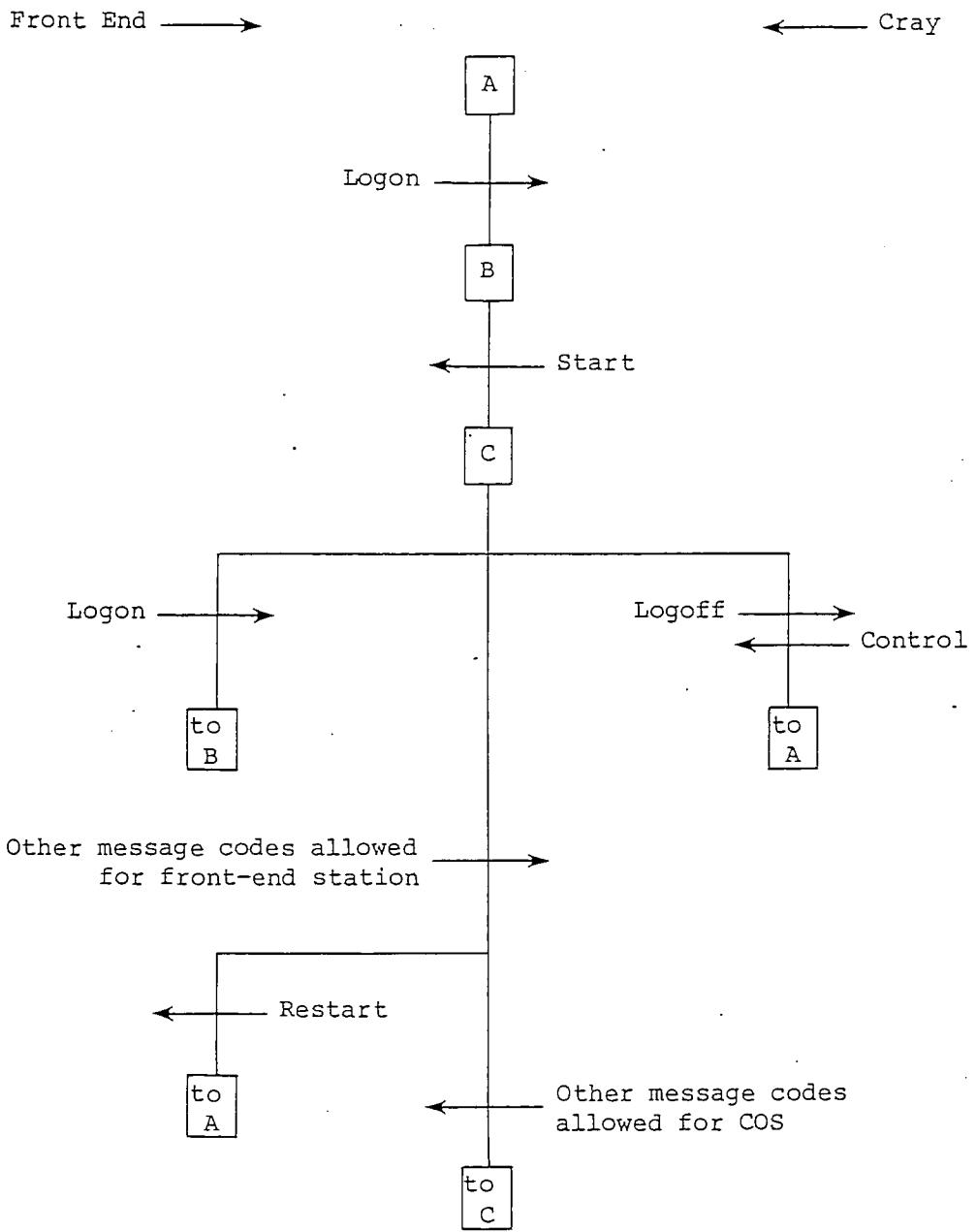
Code	Function	Sender		Segment	Stream Required
		Station	COS		
001	Logon	X		X	
003	Logoff	X		X	
004	Start		X	X	
005	Restart		X	X	
006	Dataset header	X	X	X	
007	Dataset segment	X	X	X	X
011	Control	X	X		
012	Message error	X	X		
013	Dataset transfer request		X	X	
014	Dataset transfer reply	X		X	
015	Enter logfile request	X ^t		X	
016	Enter logfile reply		X	X	
021	Job status request	X ^t		X	
022	System status request	X ^t		X	
023	Dataset status request ^S	X ^t		X	
024	Link status request	X ^t		X	
025	Mass storage status request	X ^t		X	
026	Operator function request	X ^t		X	
027	Debug function request	X ^t		X	
031	Job status reply			X	X
032	System status reply			X	X
033	Dataset status reply ^S			X	X
034	Link status reply			X	X
035	Mass storage status reply			X	X
036	Operator function reply			X	X
037	Debug function reply			X	X
040	Diagnostic echo request	X ^t		X	
041	Diagnostic echo reply	X ^t		X	
042	Interactive request	X ^t		X	
043	Interactive reply		X	X	
044	Statclass request	X ^t			
045	Statclass reply		X		
046	Station message ^{tt}		X		
047	Station reply	X ^t			
050	Tape configuration request	X ^t			
051	Tape configuration reply	X ^t		X	
052	Tape job status request	X ^t			
053	Tape job status reply		X		
054	Configure request	X ^t			
055	Configure reply		X		
056	Dataset status request (ownership) ^S	X ^t	X	X	
057	Dataset status reply (ownership) ^S	X ^t	X	X	
070- 077	Reserved for site use ^{ttt}	X	X	X	X

MESSAGE CODE: M222M

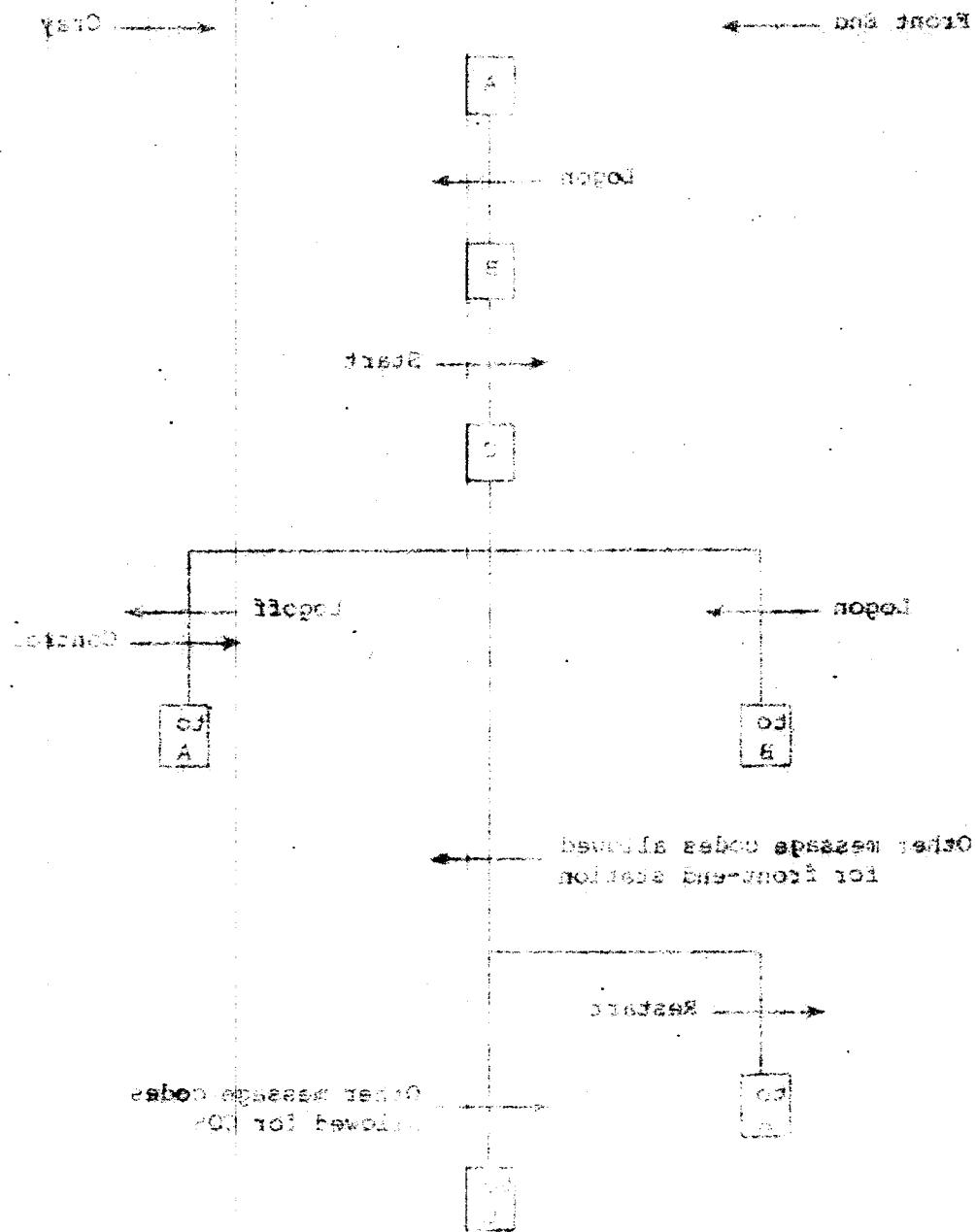
MESSAGE FLOW

Message sequence for		Sequence		Function		Code
		Sequence	Sequence	Sequence	Sequence	
Logon -	x			x	x	001
Start -	x	x	x	x	x	003
x Restart -	x	x	x	x	x	009
Control -	x	x	x	x	x	020
Logoff -	x	x	x	x	x	021
Control message used as Idle	x	x	x	x	x	013
Poll rate determines period of Idle message	x	x	x	x	x	014
	x	x	x	x	x	015
	x	x	x	x	x	022
	x	x	x	x	x	023
	x	x	x	x	x	024
	x	x	x	x	x	025
	x	x	x	x	x	026
	x	x	x	x	x	027
	x	x	x	x	x	028
	x	x	x	x	x	029
	x	x	x	x	x	030
	x	x	x	x	x	031
	x	x	x	x	x	032
	x	x	x	x	x	033
	x	x	x	x	x	034
	x	x	x	x	x	035
	x	x	x	x	x	036
	x	x	x	x	x	041
	x	x	x	x	x	043
	x	x	x	x	x	045
	x	x	x	x	x	046
	x	x	x	x	x	047
	x	x	x	x	x	048
	x	x	x	x	x	049
	x	x	x	x	x	050
	x	x	x	x	x	051
	x	x	x	x	x	052
	x	x	x	x	x	053
	x	x	x	x	x	054
	x	x	x	x	x	055
	x	x	x	x	x	056
	x	x	x	x	x	057
	x	x	x	x	x	058
	x	x	x	x	x	059
	x	x	x	x	x	060
	x	x	x	x	x	061
	x	x	x	x	x	062
	x	x	x	x	x	063
	x	x	x	x	x	064
	x	x	x	x	x	065
	x	x	x	x	x	066
	x	x	x	x	x	067
	x	x	x	x	x	068
	x	x	x	x	x	069
	x	x	x	x	x	070
	x	x	x	x	x	071
	x	x	x	x	x	072
	x	x	x	x	x	073
	x	x	x	x	x	074
	x	x	x	x	x	075
	x	x	x	x	x	076
	x	x	x	x	x	077
	x	x	x	x	x	078
	x	x	x	x	x	079
	x	x	x	x	x	080
	x	x	x	x	x	081
	x	x	x	x	x	082
	x	x	x	x	x	083
	x	x	x	x	x	084
	x	x	x	x	x	085
	x	x	x	x	x	086
	x	x	x	x	x	087
	x	x	x	x	x	088
	x	x	x	x	x	089
	x	x	x	x	x	090
	x	x	x	x	x	091
	x	x	x	x	x	092
	x	x	x	x	x	093
	x	x	x	x	x	094
	x	x	x	x	x	095
	x	x	x	x	x	096
	x	x	x	x	x	097
	x	x	x	x	x	098
	x	x	x	x	x	099
	x	x	x	x	x	100

MESSAGE CODE FLOW



MESSAGE FLOW



STATION PROTOCOL

Match the following terms and their definitions:

- | | |
|----------------------------|--|
| 1. LINK CONTROL PACKAGE | ____ Does the word size, logic level and hardware protocol conversion between the Cray and front-end |
| 2. MESSAGE CODE | ____ Provides longer distance links between the Cray and a front end |
| 3. DATASET HEADER | ____ Links the Station communication to the COS tasks |
| 4. STREAM CONTROL BYTE | ____ Field in an LCP containing error messages |
| 5. STATION | ____ 6 word message header sent between the station and SCP |
| 6. HYPERCHANNEL | ____ Segment zero of a dataset transfer that precedes the dataset |
| 7. FRONT-END INTERFACE | ____ Data transferred across the station Cray link with the LCP |
| 8. SEGMENT | ____ Defines what the segment attached to the LCP contains |
| 9. MESSAGE SUB CODE | ____ Defines the status of a dataset transfer from the request to the transfer completion |
| 10. STATION CALL PROCESSOR | ____ Software that runs on the front-end under the control of the front-end's operating system |

	<u>STATION REPORT</u>	
	MATCH THE FOLLOWING TERMS AND CODES WITH THE INFORMATION	
1. LINK CONTROL PACKAGE	DEFINE THE WORD SIZE, JOLDE LEVEL AND PERFORMANCE PROFILE CONTINUATION BETWEEN THE GATE AND LEAF-OUT	
2. MESSAGE CODE	PROVIDES LOWER LATENCIES INSTEAD OF PERIODIC POLLING AND IS ROUTED AND	
3. DATASET HEADER	USES THE SERIAL COMMUNICATIONS TO THE CO2 FEAKS	
4. STREAM CONTROL BYTE	FEED IN AS PCB CONTINUOUSLY ALL MESSAGES	
5. HYBRID CHANNEL	USES BASED APPROXIMATELY ONE WORD PERIODIC POLLING AND 3CB	
6. FORWARD-END INTERFACE	USES PREDICTED ADDRESSING FOR CO2	
7. AGREEMENT	GETS INFORMATION FROM THE CO2 CONTROLLER REGARDING THE SET OF CO2 CONTROLS	
8. MESSAGE END CODE	DEFINITION OF A CONTROLLED SEQUENCE OF 0 TO 6 BYTES	
9. STREAM CALL NUMBER	DEFINITION OF A CONTROLLED SEQUENCE OF 0 TO 6 BYTES	
10. STATION CALL NUMBER	ROUTE-IN AS DESCRIBED ABOVE	

Inter-Task Communication

6

()

()

()

OBJECTIVES

With the aid of all furnished reference materials, following the completion of this Intertask Communication module, the learner should be able to:

1. Describe how a task passes information to another task.
2. Describe the routines used for Synchronous Communication.
3. Describe the routines for Asynchronous Communication.

Impact in COS Rev. 1.14.

TASK TO TASK COMMUNICATION

Synchronous

User will not gain control of CPU until transfer is complete.

TSKREQ

Sends a request and suspends the task until reply is received.

Asynchronous

If a request is made, the user can be reconnected before the data transfer is finished.

PUTREQ

Sends a request to another task.

GETREQ

Gets a request from another task.

PUTREPLY

Sends a reply to a task.

GETREPLY

Gets a reply from a task.

Task puts function code in S1 and S2.

Format is defined by the called task.

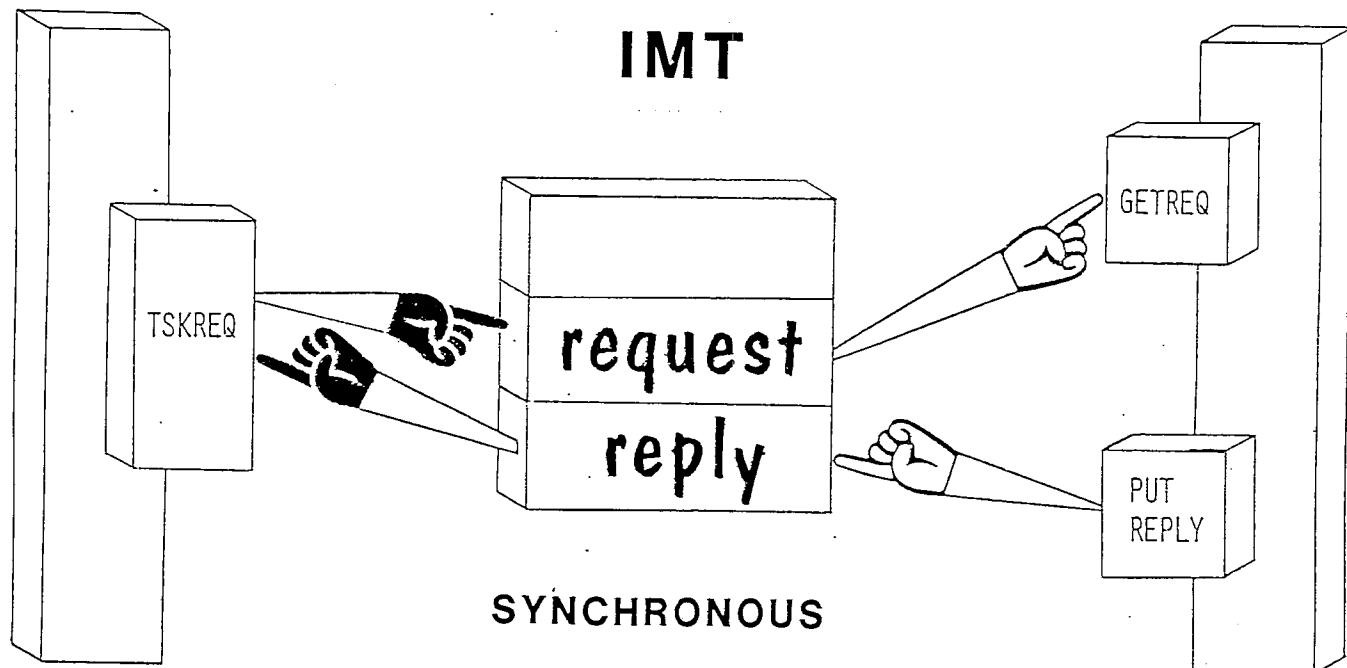
Task calls EXEC to activate the task.

Task scheduler in EXEC examines STT to determine highest priority task ready to execute.

task A

task B

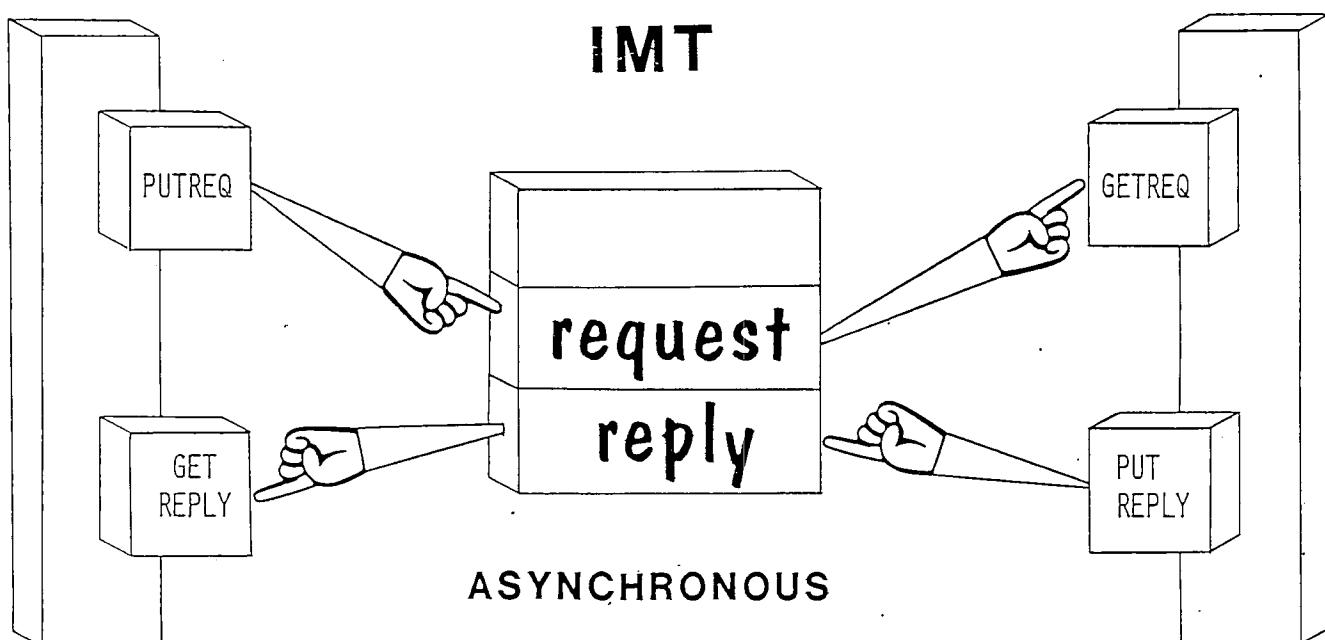
IMT



task A

task B

IMT



INTER-TASK CALLS - SCP CALLS

SCP/Any Task

- via RTSK:

1. INIT n operator command (n=task id).

SCP/DQM

- via PUTREQ:

Transfer:

1. Write RCV datasets to mass storage.
2. Read SND datasets from mass storage.

*** SCP does not use CIO for DQM transfer requests ***

Deallocate:

1. Cancel SND or RCV datasets.
2. Deallocate job input dataset.
 - job termination
 - operator KILL
3. Idle active LXT entry.
 - release all RCV dataset space

Allocate:

1. Interactive job input dataset.
 - allocate a dummy dataset

SCP/JSH

- via RTSK:

1. Notify JSH that a new job is on input Q.
2. Change PRI of executing job.
3. Rewrite CSD dataset when job class is turned ON/OFF.
4. Alter number of JXTs available with LIMIT.

- via TSKREQ:

1. Job debugging from operator console.
2. Operator control (DROP, KILL, RESUME, etc.)
3. Interactive job control.
 - attention
 - abort
4. ACQUIRE, DISPOSE failures (abort job).

SCP/JCM

- via TSKREQ:

1. Assign job to a class.
 - job input dataset has arrived
2. Reassign job class; operator changed ...
 - front-end ID & TID (ENTER or ROUTE command)
 - priority, time limit (ENTER command)
3. Assign class to a job.
 - ENTER CLASS command

*** JCM is called ONLY for jobs on the input Q ***

SCP/MSG

- via TSKREQ:

1. Record operator type-ins (system and user logs).
2. Error message for DISPOSE disk read failure.
3. Log dataset transmission and reception messages.

SCP/PDM

- via PUTREQ:

1. See if dataset to ACQUIRE is on Cray.
2. Operator DATASET command processing.
3. Save spooled input datasets.
4. Delete spooled output datasets.

SCP/TQM

- via PUTREQ:

1. Process operator CONFIG command for XIOP tapes.

INTER-TASK CALLS - JCM CALLS

SCB\902

JCM/MSG

:038M2T sv -

- via TSKREQ:

1. Job class assignment message to system log.
2. Fatal job class definition structure error message.

1. Assign job to a class.

2. Release job class definition structure error message.

3. Assign class to a job.

4. EMTER CLASS COMMAND

*** 9 tasks end no task left ONLY 2nd call for MSG ***

INTER-TASK CALLS - MEP CALLS

SCB\902

MEP/MSG

:038M2T sv -

- via PUTREQ:

1. Forward EXEC messages to MSG.
2. Error message for DISPOSE disk read failure.
3. End dataset finalization and read/write messages.

1. Record debugger tape-use (save and read jobs).

2. Error message for DISPOSE disk read failure.

3. End dataset finalization and read/write messages.

SCB\902

:038M2T sv -

1. See if dataset to VOLUME is on GROW.
2. Release DATASET command blocksize.
3. Save blocksize input dataset.
4. Define blocksize output dataset.

1. See if dataset to VOLUME is on GROW.

2. Release DATASET command blocksize.

3. Save blocksize input dataset.

4. Define blocksize output dataset.

SCB\902

:038M2T sv -

1. Process a debugger COMPILE command for XJOB loader.

INTER-TASK CALLS - JSH CALLS

JSH/DQM

- via PUTREQ:

Allocate:

1. Allocate job roll dataset.

Deallocate:

1. Release roll dataset space:

- job terminates
- larger DAT needed (job expands)

Transfer:

1. Write \$ROLL.
2. Write CSD dataset.
3. Job roll in/out.

JSH/MSG

- via TSKREQ:

1. Write messages to user and system logs.

JSH/JCM

via TSKREQ:

1. Classify job which is to be rerun.
2. Re-order input Q when new job class structure invoked.

JSH/EXP

- via JTEP word:

1. Abort request (A\$xxx) passed to JSH from another task.
2. Bad job class structure invoke (A\$BINV).
3. Initialize job (JXEPC=0).
4. Time limit expired (A\$TLE).
5. Too much memory requested (A\$GSY).

INTER-TASK COMMUNICATION TABLES

STP contains three areas used for intertask communication.

The Intertask Communication Table (ICT)

The request queue table has one entry per task.

Each entry defines the address and size of the associated IMT.

All requests of a particular task are placed on the queue pointed to by its ICT request entry.

The reply queue table has one entry per task

Each entry defines the address and size of the associated IMT.

All replies to a particular task are placed on a queue pointed to by its ICT reply entry.

The Intertask Queuing Table (IQT)

A bit map which defines available and active entries in the IMT.

Initialized to a given size at assembly time.

Number of bits at beginning of bit map is set to zero.

The rest of the bit map is set active.

The Intertask Message Table (IMT)

The IMT immediately follows the IQT and is the message's.

An IMT entry is three words:

Two words from S1 and S2 of the calling task.

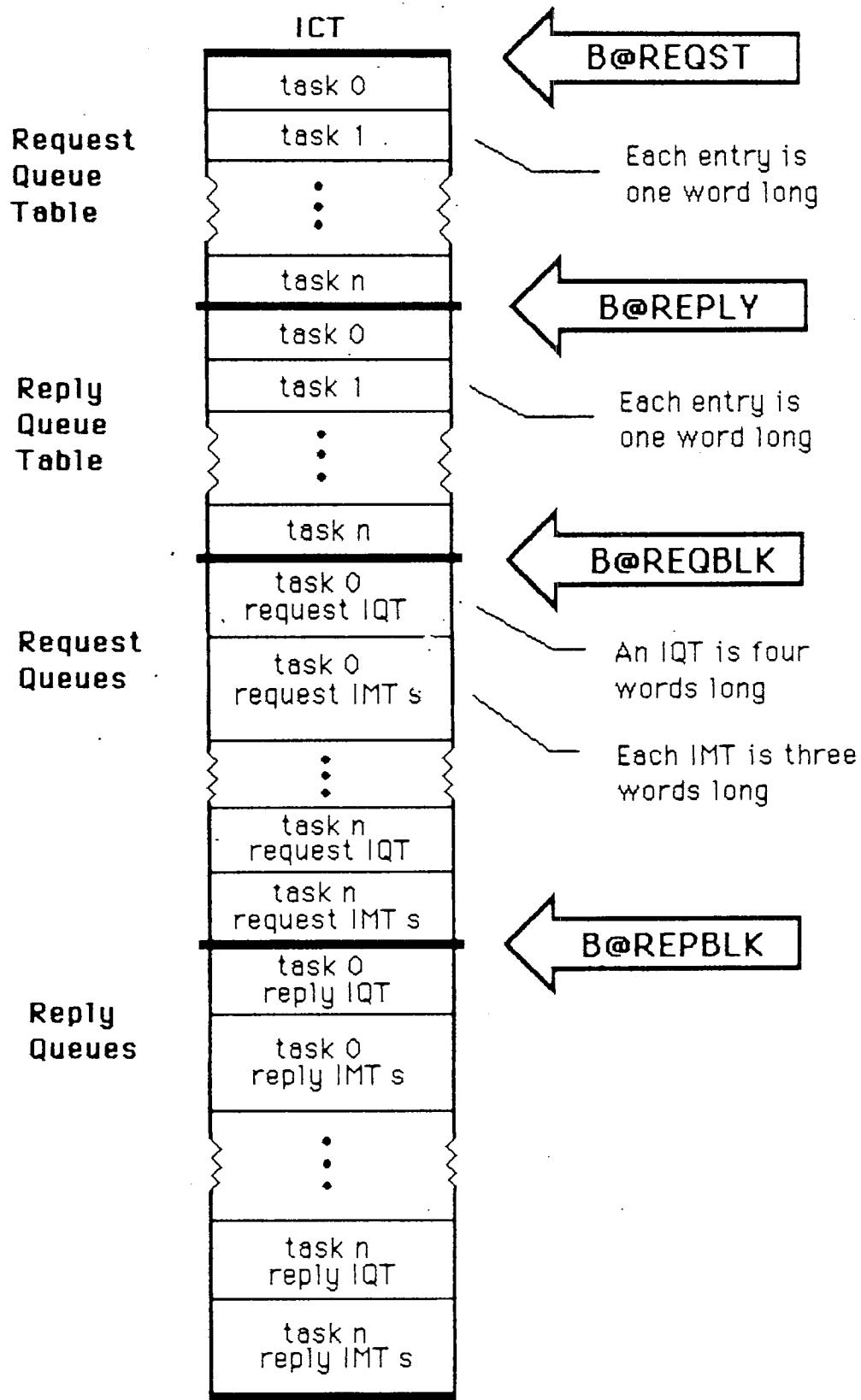
A word with the calling task ID.

One IMT entry for each possible request/reply in the IQT.

One task requests action of another by queuing a message on the request queue of the receiving task.

The second task replies by queueing a message on the reply queue of the first task.

Intertask Communication Table Details



INTER-TASK CALL ROUTINES

PUTREQ:

Sets STPLCK
Allocates first available IMT in the called task's request queue
Puts the calling task's S1, S2 and task ID in the IMT
Determines whether to make an explicit or implicit EXEC request to ready
the called task using the following logic:
IF Callee's priority > caller's priority and Callee suspended < threshold
CALL EXEC TASK-READY
ELSE make an explicit EXEC TASK-READY REQUEST
An explicit request is made by setting up S6 and S7 and doing EX
An implicit request is made setting the bit corresponding to the task ID
in the System Task Activation word (STA)
On an exchange to EXEC (interrupt, etc.) - any task whose bit is set in
the STA will be readied

GETREQ:

Sets STPLCK
Finds first IMT with the corresponding IQT bit set
Sets the IQT bit to zero
Gives the intertask message to the receiving task in S1 and S2
A2 contains the requesting task ID
Unlocks STPLCK

PUTREPLY:

Sets STPLCK
Allocates the first available IMT in the receiving task request Queue
Puts the replying task's S1 and S2 and task ID in the IMT
Determines whether to make an explicit or implicit EXEC request to ready
the receiving task
Unlocks STPLCK

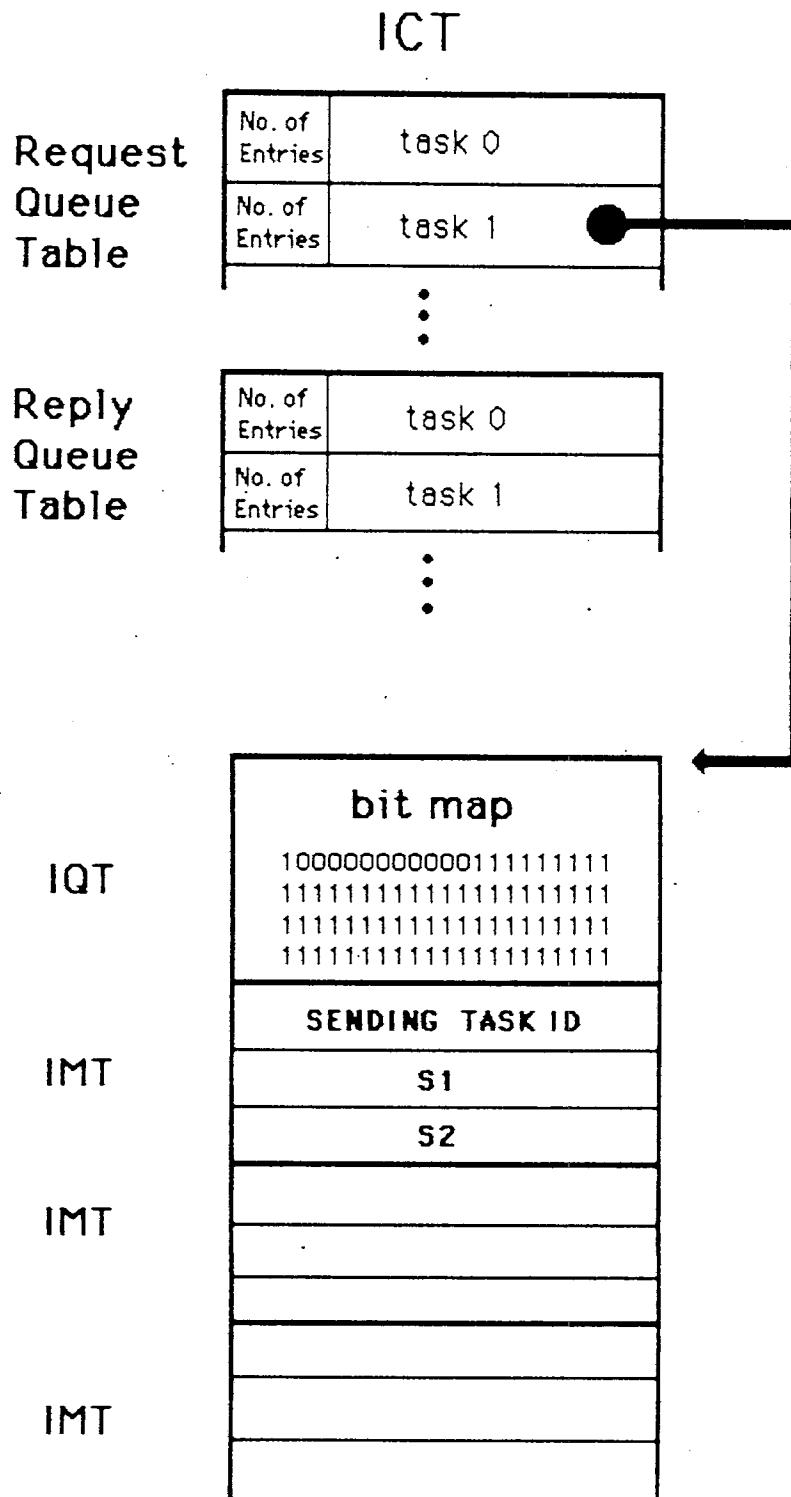
GETREPLY:

Sets STPLCK
Finds the first available IMT with a corresponding IQT bit set
Zeros the IQT bit
Gives the receiving task the intertask reply in S1 and S2
A2 contains the sending task ID
Unlocks STPLCK

TSKREQ:

Used for asynchronous intertask communication
First it does the equivalent of a PUTREQ
Then the task is suspended until a reply is received
Finally it does the equivalent of a GETREPLY
A called task still uses GETREQ and PUTREPLY to respond to TSKREQ

Intertask Communication Tables



INTER-TASK HISTORY TRACE TABLE

Contained in STPTAB as symbol ITCT

Each entry is eight words

Routine name

Elapsed clock periods since last entry

Real time clock

Task ID of current STP task (task making request or reply)

S1 contents

S2 contents

Task ID of requested task (task receiving request or reply)

Next entry pointed to by header field

For S register breakdown, see SM-40

EXP	SM-40, page 8-3 to 8-35
JSH	SM-40, page 9-36 to 9-61
PDM	SM-40, page 10-2 to 10-13
MSG	SM-40, page 11-4 to 11-6
MEP	SM-40, page 12-2
DEC	SM-40, page 13-1
JCM	SM-40, page 15-2 to 15-5
TQM	SM-40, page 17-13 to 17-20
IQM	SM-40, page 16-26 to 16-27
FVD	SM-40, page 19-1
DQM	SM-40, Page 6-1

DQM TRACE

DQMT points to start of table

100 octal words long

TQM TRACE

TQMT points to start of table

21000 octal words long

Intertask Communications

History Trace Message Format

Routine Name	Elapsed Clock Periods Since Last Entry	Real Time Clock	Task ID of Current STP Task
S1	S2	Task ID of Requested/Requesting Task	

Highway Traffic Control
Highway Traffic Control

to GI level Carrying load ST2	from GI level Carrying load ST2	baseball GI level Carrying load ST2	positive slope
	to GI level Carrying load ST2	ST2	12

INTERTASK COMMUNICATION

Match the following terms and their description:

- | | |
|------------|---|
| 1. IMT | _____ Used to make an executive request |
| 2. PUTREQ | _____ Routine to initiate a synchronous request from another COS Task |
| 3. TASKREQ | _____ Used by the user to make system action requests of EXP |
| 4. ITCT | _____ Request/Reply packet between STP tasks |
| 5. S0 & S1 | _____ Registers used for task to task requests/replies |
| 6. S1 & S2 | _____ Trace table for all task to task requests/replies |
| 7. S6 & S7 | _____ Asynchronous Request to another Task |

METHODS OF COMMUNICATION

Match the following code names and their descriptions

Urged to make an executive decision	TMI-1
Routine to initiate a communication leaderless from supporters CDTI ask	S. PATUREO
Urged by the need to make a decision action leaderless of EXP	E. LASKREO
Referred(Help) a leaderless position S1B take	TOTIA
Referred need for CDTI to speak leaderless/take	E. S2 & S1
Urged to speak to all speak to speak leaderless/take	E. S1 & S2
Vocalization leaderless to make a decision ask	E. S2 & S3

Inter-IOP Communication

7

()

()

()

Inter-IOP Communication

7

at this stage necessary to monitor and support the vendor

at each year-end fiscal year budgeting

baseline and base

borrowing and the development of ROI ratios of 10%

target of 25% G

DAU

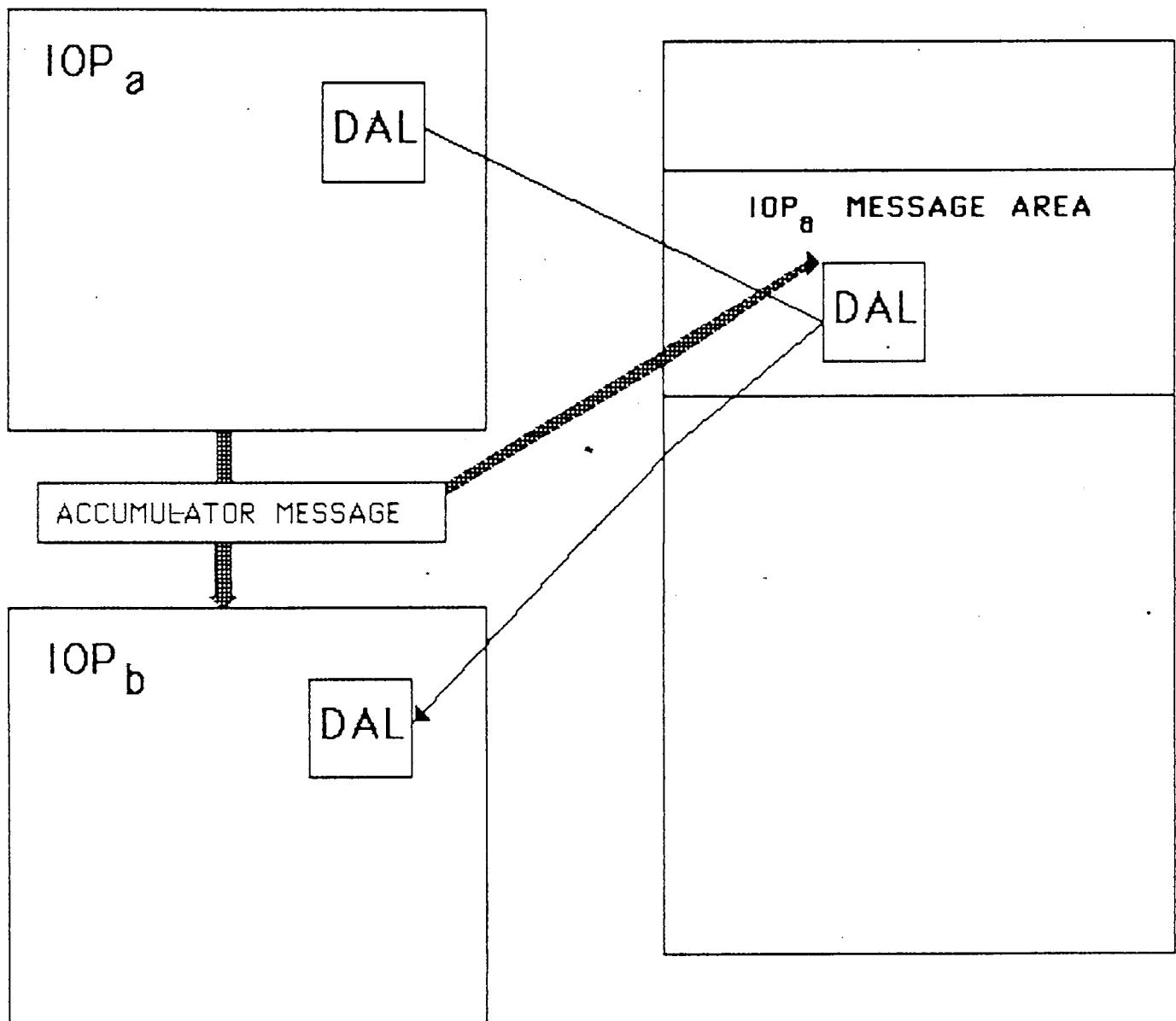
Appendix 10: Summary of Findings

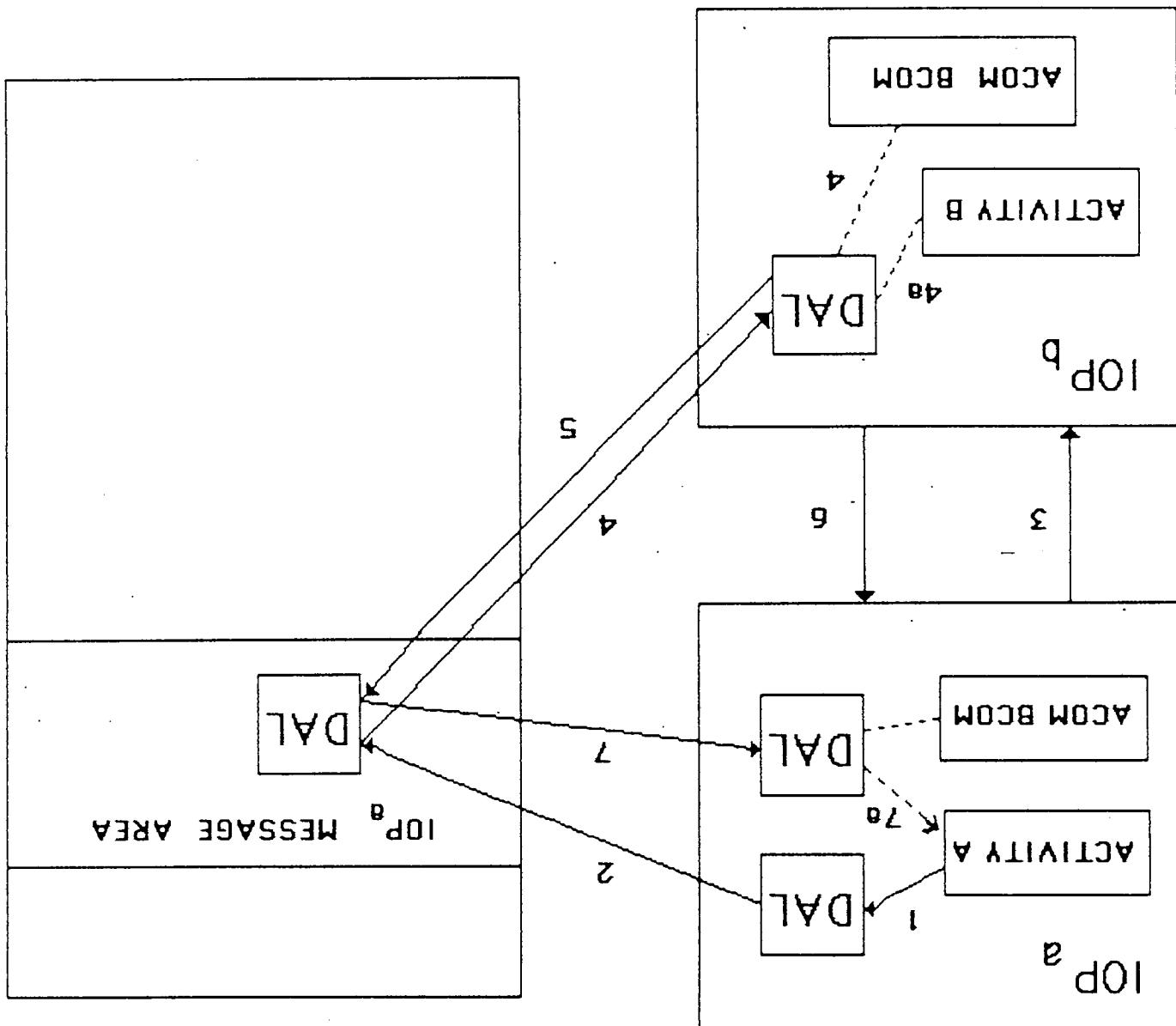
MOTION NUMBER	OWNER/COMMITTEE NO. 22990A
4. Explain the control of one activity over almost everything	III. 57.8
3. Describe how an activity is created.	(JAC) cannot yet identify what
2. Locate the associated D&L. Given a problem related should be seen people see	multiple steps to address the problem
1. Breakdown a A-A message.	multiple steps to address the problem

With the aid of a flip chart she referred materials, upon completion of this module the learner should be able to:

Module 5 will be able to answer the following questions about communication skills:

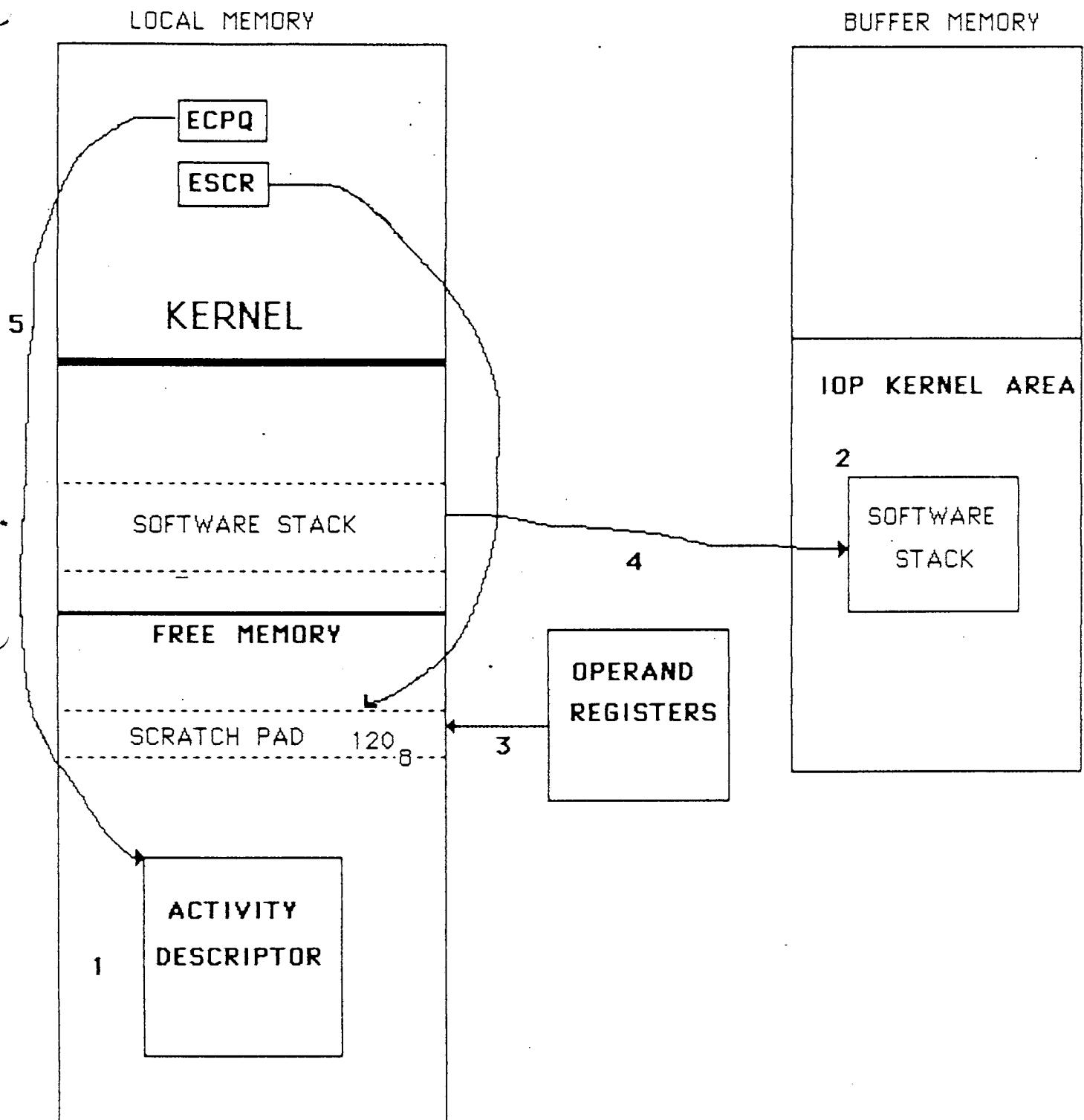
INTER-IOP COMMUNICATION

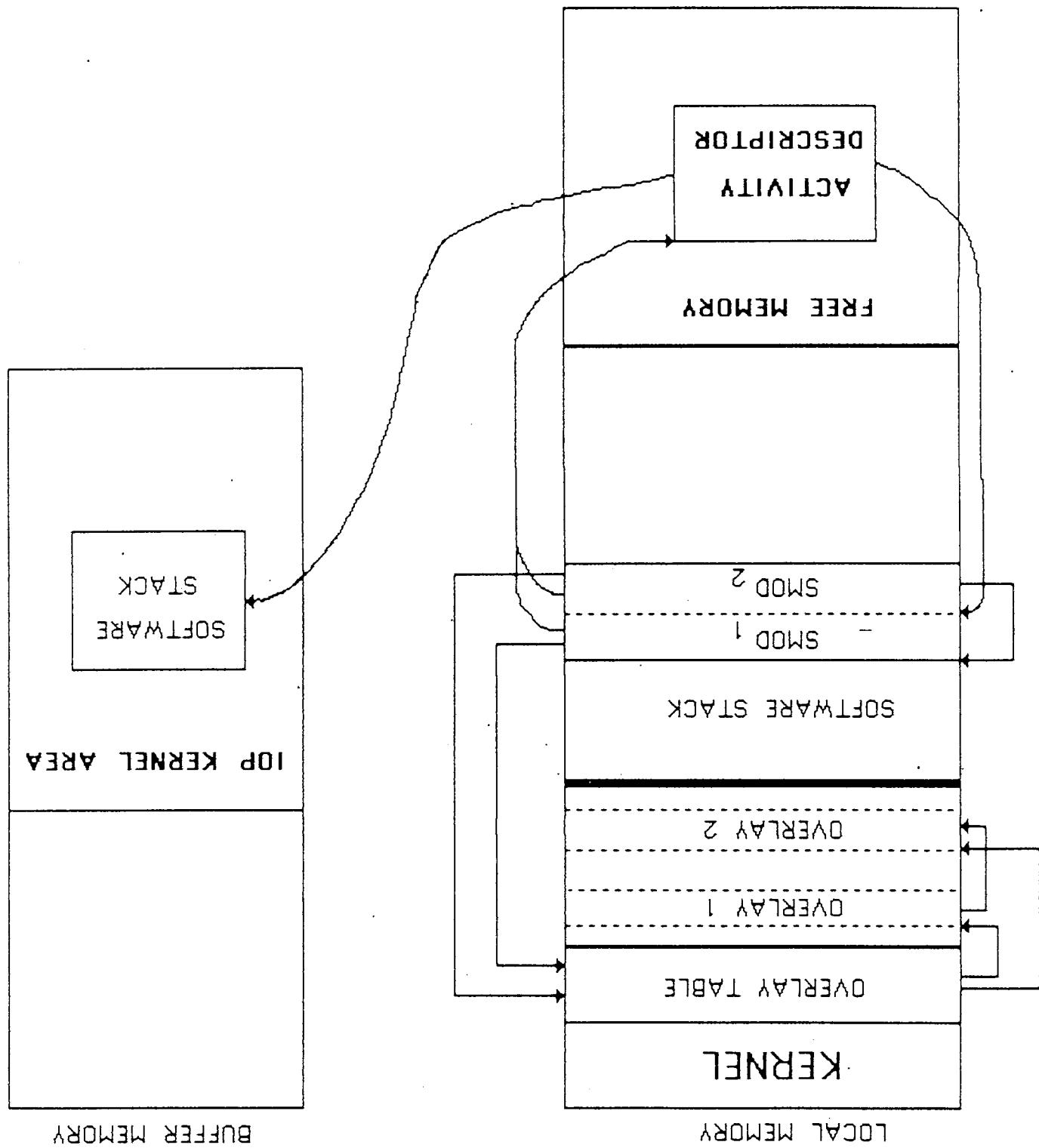




DISK ACTIVITY LINK FLOW

ACTIVITY CREATION





ACTIVITY TABLE LINKAGE

INTER-IOP COMMUNICATION

- | | |
|--|---|
| 1. AMSG | — Activates a suspended activity in another IOP |
| 2. POPCELL | — Processes Interprocessor DALs that are requesting TAPE |
| 3. DISK ACTIVITY LINK | — Kernel service request to activate another overlay |
| 4. AWAKE | — 40 octal parcel message passed between IOPs through Buffer memory |
| 5. CALL | — Processes DAL requests from MIOP for DD-49 activities |
| 6. ICOM | — Processes the ALERT service request creating a POPCELL |
| 7. BCOM | — Processes DD-29 DALs from MIOP, BIOP or DIOP |
| 8. ACOM | — Points to the Interprocessor DAL in Buffer Memory |
| 9. INTER-IOP A-A MESSAGE Interrupt Handler | — Kernel Service request used by MIOP to initiate a Buffer Memory to/from Central Memory for the Concentrator |
| 10. STATIO | — QUEUE and Activity descriptor information for the ALERT, AWAKE, ASLEEP, RESPOND mechanism |

COS Dump Analysis

8

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~~TMUO MTT212 A 10 JULIA 200~~
LEARNING OBJECTIVES

With the aid of all furnished reference materials, upon completion of the COS Dump Analysis module, the learner should be able to:

1. Explain how the CRAY1SYSTEMDUMP is created.
2. Determine the EXEC Hang Address.
3. Determine the Hang Address Macro.
4. Locate the Hang Macro in the Listings.
5. Locate and analyze the History Trace Buffer.
6. Locate and analyze a COS Table.
7. Locate and analyze Memory Error Log.
8. Find the EXEC or Task execution environment.

CREATION OF A SYSTEM DUMP

Why is a dump taken:

1. To determine how system operates before it is to be run with
GRAY HALT appears on Kernel Console

2. IOP HALT appears on the Kernel Console

3. Consoles fail to respond to operator commands

4. Cray system appears hung or dead

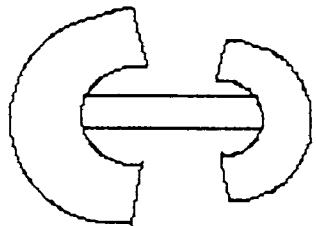
5. Cntl-D was pushed

To get a memory copy, SYSDUMP must be executed

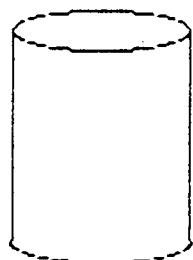
SYSDUMP runs from MIOP and copies system memories to the master device

STARTUP processes buffer and creates permanent dataset

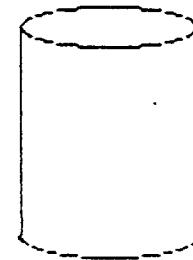
FORMATTED SYSTEM DUMPS



CRAY SYSTEM CONTENTS



DISK SCRATCH BUFFER



CRAY1SYSTEMDUMP

PDN=

OWN=

ED=

R=

FORMATTED
JOB
OUTPUT

FORMATTED DUMP
IN \$OUT

JOB with FDUMP directives

~~DUMP~~ FORMATTING OF A SYSTEM DUMP

Requires a working Cray

If no Cray is available then the dump should have been a raw dump.
(File 2 of tape or disk)

Directives tell the program FDUMP what to dump and allow comments.

Different directives for the mainframe and the IOP.

Initially dump Exec STOP Buffer and STP Hang Message area.

CTRL-D initiates SYSDUMP

DISK SCRATCH BUFFER

STRTUP processes buffer

CRAY1SYSTEMDUMP

=PDN=

=MWN=

=ED=

=R=

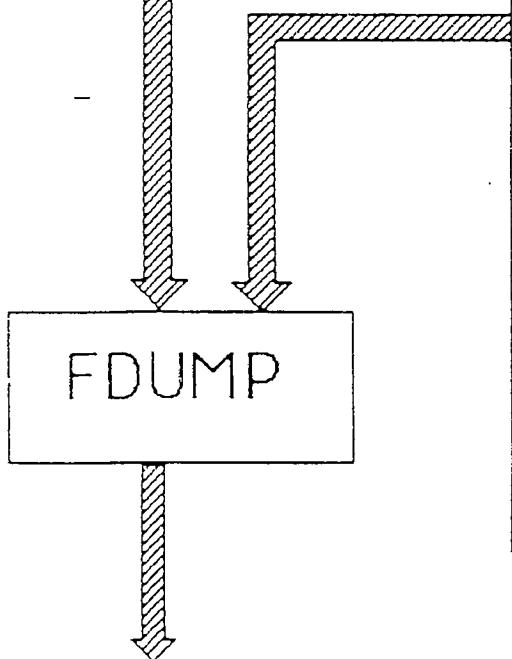
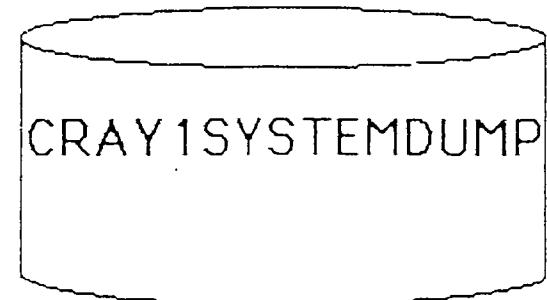
FORMATTED DUMP

IN *OUT

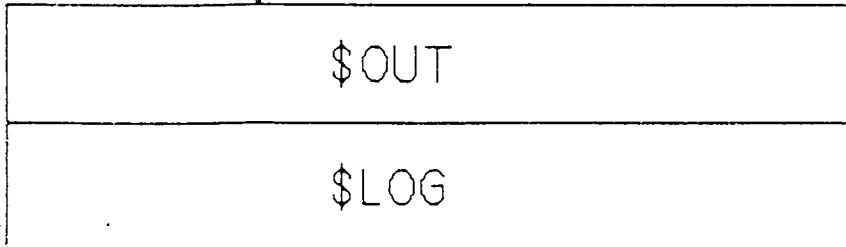
JOB WITH FDUMP directives

FORMATTE
80L
OUTPUT

FDUMP



```
JOB,JN=TNG00A.  
ACCOUNT,AC=,US=,UPW=  
ACCESS,DN=A,PDN=,ED=,M=  
FDUMP.  
/EOF  
FILES,DDS=A.  
DMEM,FWA= 0,LWA=16000.      EXEC  
DMEM,XP,FWA=100,LWA=200.    EXEC XP  
DMEM,XP,FWA=4740,LWA=5320.  TASK XP  
DMEM,XP,FWA=4100,LWA=4160.  PWS 0  
DMEM,XP,FWA=4220,LWA=4270.  PWS 1  
DMEM,FWA= 0,LWA=1000,BIAS=32000. STP  
DMEM,FWA=265540,LWA=267000 ,^  
BIAS=32000.                  ITCT  
DMEM,TYPE=1OPO,FWA= 4000,LWA=12000,R.  
DXTR,TYPE=1OPO.
```



REASONS FOR A HANG

The operating system detects an inconsistency.

Could be hardware or software detected

XP flag or DN flag could have set

A program is too large

Could be hardware or software caused

A channel error

A register overwritten

DUMP ANALYSIS PROCEDURE

Locate and read the Hang Messages

Locate the Hang Macro in the Listing

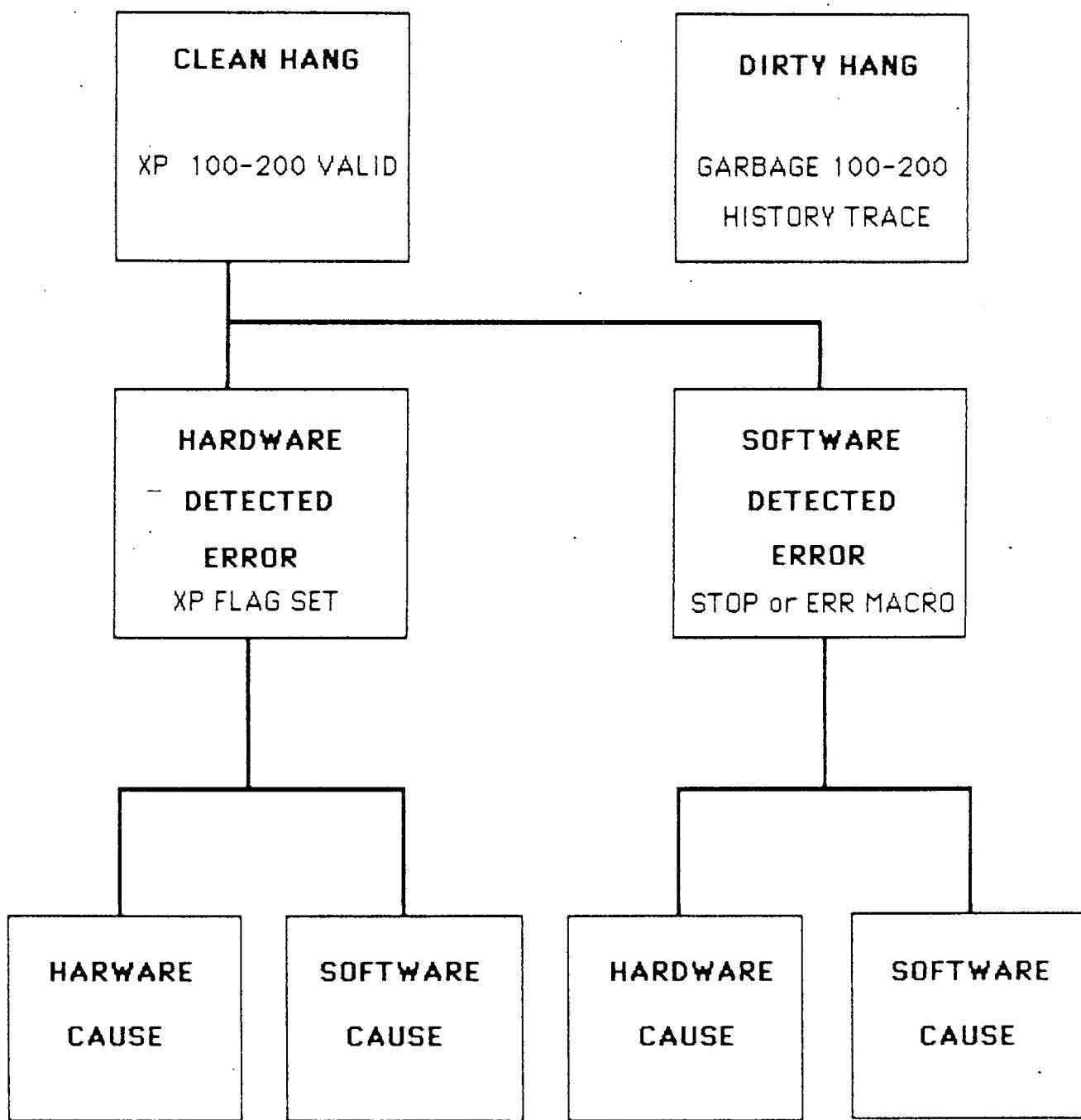
Analyze the Histrace Buffer

Decide on the program to begin with

Locate the necessary Program Executing Environment

Determine the reason for the error hang conditions

COS HANG



This is a list intended to serve as a guide to analyzing CRAY1SYSTEMDUMP'S.

Record any operator interactions with COS or IOS at the time of the crash, record date and time in the crash log.

Examine KERNEL HALT MESSAGE to determine if COS halted IOS .

Take a dump according to CRAY RESEARCH System's analyst needs.

Run FDUMP to format important areas of the dump.

Dump the EXEC table area, STP table pointer area.

Make sure listing matches dump assembly date.

Examine exchange package area 100-200 of central memory.

Determine interrupted exchange package from A1 of hang XP.

If a channel error examine A2 and A3 of hang XP.

Determine if hang looks like a valid EXEC hang.

Examine the Stop Buffer around 1400 of central memory.

Check assembly date and revision against listings.

Determine hang message and number.

Read P and Locate the \$STOP macro in the listing.

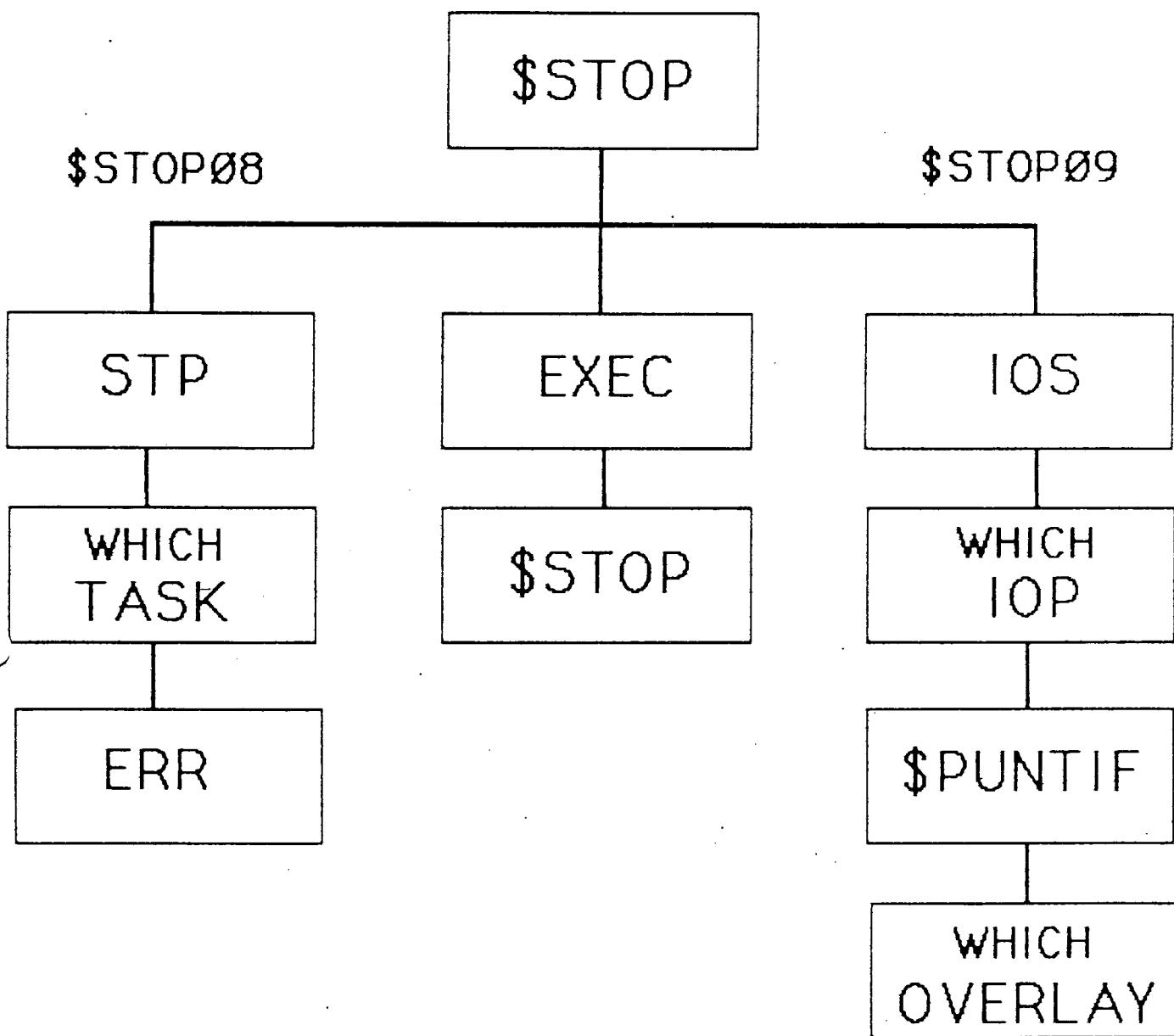
If it's a \$STOP08 then the ERR hang macro was in STP code.

If it's a \$STOP09 then the \$PUNTIF hang macro was in IOS code.

If it's a \$STOP00-03,STOP37-42 then it's likely hardware.

If it's any \$STOP other than 08 or 09 then it's an EXEC initiated hang.

WHERE WAS THE HALT?



LOCATE THE HANG MESSAGE

LOCATE THE HANG MACRO IN LISTINGS

ANALYZE THE HISTORY TRACE

LOCATE NECESSARY POINTERS, REGISTERS, TABLES

```

=====
! S T O P   B U F F E R !
=====
EXEC STOPPED AT LABEL: $STOP006
W.P =           W.B0 =
-----
message
-----END BUFFER -----

```

The stop label is used in EXEC with the STOP macro. The STOP macro does not convert the values in P and B0 to ASCII characters, so their values appear in the dump. The value of P is in the word after the word containing W.P and the value of B0 is in the word after the word containing W.B0. These two values have been truncated to words.

The following convention is used for STOP labels and messages: the label has the form \$STOPec, where ec is a unique decimal number for each error condition. The stop message contains the routine name where the stop occurred and a short, descriptive error message.

(See SM-40, page 2-9U for more detail)

EXEC stop messages

Label	Code	Significance
\$STOP000	EEF	Unknown error
\$STOP001	EX	(A1) does not equal XP exchange address
\$STOP002	APOIP	IOP channel error
\$STOP003	APIIP	IOP channel error
\$STOP004	EE	Program address range error
\$STOP005	EEF	Floating-point error
\$STOP006	EEF	Operand range error
\$STOP007	EEF	Program range error
\$STOP008	EEF	STP error exit (usually accompanied by SY006 message in STP memory)
\$STOP009	EN	CPU halt requested by IOS
\$STOP010	TECAN	Invalid event number
\$STOP011	TS0	The STP Lock flag STPLK is set, but no task is marked as active.
\$STOP012	R041	A PSWITCH request was received from a task other than the job scheduler (JSH).

STOP BUFFER

DMEM, FWA=1400, LWA=16000.

EXEC TABLES

FDUMP 1.14
SYSDUMP

01/04/85
01/04/85

13:58:19
00:44:57

PAGE 2

0001400	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000	LOCATIONS	0001404	THROUGH	0001417	CONTAIN	00000000000000000000000000000000	CPTYPE=CRAY XMP CPQUAN=
0001420	0001032405213124042475	0415222025444026046520	0001032405052520247075	00000000000000000000000000000002	00000000000000000000000000000002	CLMAX=		NCPLUS=				
0001424	0000002064611520254075	000000000000000000000005	0000002344152025251475	00000000000000000000000000000002	00000000000000000000000000000002	NCLS=		COS REV=COS 1.14				
0001430	000000004710323051475	000000000000000000000003	0415172462012221253075	0415172462006113430464	0415172462006113430464	EXECDATE01/03/8501:10:55CAT						c
0001434	0425302124150420252105	0300611363006131364065	03006113643046016432465	041501250000000000002143	041501250000000000002143	CBT	sCHT	CIT	CLT			
0001440	041502250000000000002163	041510250000000000002234	041511250000000000002256	041514250000000000002277	041514250000000000002277	ICT	IHT	IPRQ				
0001444	041530250000000000002317	044503250000000000002615	044510250000000000002631	044520244504000000002645	044520244504000000002645	MCT	MEL	MRT	PCT			
0001450	046503250000000000002650	046505230000000000002714	046522250000000000002746	050103250000000000003012	050103250000000000003012	PIQ	ePOQ	PRT	PWS			
0001454	050111242000000000003145	050111724200000000003330	050122250000000000003673	050127246000000000004026	050127246000000000004026	RIT	RMS	SCT	STT			
0001460	051111250000000000004307	051111254620000000004300	051503250000000000004313	051524250000000000004323	051524250000000000004323	STPRL	uSTX	TBT	TET			
0001464	0515242405111400005565	0515242600000000004740	052102250000000000005320	052105250000000000005322	052105250000000000005322	TPT	4XFT	VXTT				
0001470	052120250000000000005464	054106250000000000005566	054124250000000000005646	00000000000000000000000000000000	00000000000000000000000000000000							
0001474	0364751723647517236475	0364751723647517236475	0364751723647517236475	0364751723647517236475	0364751723647517236475							
0001500	0204401002004010051440	0520402362012010020040	04104002522010610043040	0424402442004010020041	0424402442004010020041	I	S T O P	B U F F E R	I			
0001504	0364751723647517236475	0364751723647517236475	0364751723647517236475	0364751723647517236475	0364751723647517236475							
0001510	042530212414024652117	0501202124204020252040	0461012042451410020040	0221232504752014030070	0221232504752014030070	EXEC STOPPED AT LABEL	SSTOP008					
0001514	053456240364000000000000	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000	W.P=	XH.WOO=					
0001520	0264551322545513226455	0264551004015524442507	04452325042522246020055	0264551322645513226455	0264551322645513226455	A-REGISTERS						
0001524	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000							
0001530	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000							
0001534	0264551322645513226455	0264551005115524442507	04452325042522246020055	0264551322645513226455	0264551322645513226455	S-REGISTERS						
0001540	04000000000000000000000000000000	00000005010000201264000	074676363774000004111	00000000000000000000000000000000	00000000000000000000000000000000	@	h y y	HI				
0001544	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000	00000000000000000000000000000000							
0001550	0264551322645513226455	0264551322645513226455	0264551322645513226455	0264551322645513226455	0264551322645513226455							
0001554	0425052142005510051524	0500402125112223651040	0425302225205410051505	0424402465212010044101	0424402465212010044101	EEF - STP ERROR EXIT, SEE STP HA						
0001560	0471071040650524651501	0435051004002004010020040	02040401002004010020040	02040401002004010020040	02040401002004010020040	NG MESSAGE						
0001564	0364751723647517236475	0201052344204023643040	0411252144310524420075	0364751723647517236475	0364751723647517236475	===== END OF BUFFER =====						
0001570	041514252515242125113	041514252515242125113	0415142525152421251061	04300000000000000000000000000000	00000000000000000000000000000000	CLUSTERS	CLUSTER1F					

0000244	00000000000000000302740	053101210000000000000000	00000000000000000304540	0521012464544014020040	VAD	TASK 0
0000250	05152424000000000304600	05500000000000000512440	05511423643400000051000	0521012464544014220040	STP Z	ZLOG R TASK 1
0000254	05150324000000000305100	052101246454014020040	05250524000000000323540	04151123600000000360100	SCP @TASK 2	UEP CIO @
0000260	0521112360000000363575	052101246454014020040	05010423200000000465040	0521012464544015020040	TIO !TASK 3	PDM j TASK 4
0000264	0421052060000000464740	052101246454015220040	04212123200000000457040	0521012464544015420040	DEC i TASK 5	DQM TASK 6
0000270	046523216000000040100	052101246454015620040	0465052400000000500140	052101246454016020040	MSG @TASK 7	MEP TASK 8
0000274	05152023200000000500500	052101246454016220040	04512322000000000441600	0521012464544014230040	SPM @TASK 9	JSH C TASK 10
0000300	0451032320000000453366	052101246454014023040	05212123200000000364640	0521012464544014231040	JCM V TASK 11	TQM TASK 12
0000304	05152421600000000501100	052101246454014231400	04312621000000000502740	05210124645440142302040	STG @TASK 13	FVD TASK 14
0000310	04452123200000000503640	0250402465212010042516	04212310040524000555467	0515311403006610026440	IOM *	STP ENDS AT 7SY006
0000314	052101246454010031040	0441253234434020252040	03146311463006015461040	0240401003006015420060	TASK 2	HUNG AT 333007B (006 0
0000320	030063144324621443052	020040100304601403040	0300601103046614230461	020040100425222440516	0325221B	1000 000161111 ERRAN
0000324	0211152024152223624400	0410601003644010020040	00000000000000001554035	0521012464544022242123	MACRO) BO =	TASK IDS
0000330	000000000000000000000000	000000000000000000000001	0000000000000000000002	0000000000000000000003		
0000334	0000000000000000000004	0000000000000000000005	0000000000000000000006	0000000000000000000007		
0000340	00000000000000000010	00000000000000000011	00000000000000000012	00000000000000000013		

LOCATING THE MACRO

P or BØ in the STOP Buffer word address with no parcel

Match to Exec listing

If STP halted then locate STP hang message in STP table area

Will contain task ERR address and instructions surrounding macro to ease matching to the listing

Use STP map to determine task address and ID

Convert STP absolute address to the listing relative address

When a task detects a fatal condition, it will notify EXEC to stop the system through one of the following error macros:

<u>Macro</u>	<u>Condition</u>
ERRSZ	S0 = zero
ERRSN	S0 # zero
ERRSM	S0 negative
ERRSP	S0 positive
ERRAZ	A0 = 0
ERRAN	A0 # 0
ERRAM	A0 negative
ERRAP	A0 positive
ERRU	Unconditional

The error macros generate a conditional or unconditional call to an STP common routine (ERROR1) that will format the STP hang message (SY006), and execute an ERR exchange to EXEC. An ERR exchange from a system task will cause EXEC to stop the system with a \$STOP008 code in the EXEC stop buffer.

Hang Macro In The Listings

EXEC --CRAY OPERATING SYSTEM EXECUTIVE--
XPROC/EE

CRAY XMP CAL 1.14(12/11/84) 12/13/84 14:50:14 Page 164
(164)

		* STOP ON AN ERROR EXCHANGE FROM STP	
		E.2774	
		E.2775	
		E.2776	
20110c	<macro>	20110c EEF = * GETPW A6,A7,S1	LC5921HA.229
20113a	1261 00000010	S1 W@PWSAEF,A6	LC5921HA.230
c	052141	S0 S1<S@XPFE Position floating point error flag	M06586DA.1184
d	<macro>	SM,(EEF - FLOATING POINT ERROR)	E.2779
20117a	052142	S0 S1<S@XPORE Position operand range error flag	M06586DA.1185
b	<macro>	SM,(EEF - OPERAND RANGE ERROR)	E.2781
20123a	052143	S0 S1<S@XPRE Position program range error flag	M06586DA.1186
b	<macro>	SM,(EEF - PROGRAM RANGE ERROR)	E.2783
20127a	052146	S0 S1<S@XPEE Position error exit flag	M06586DA.1187
b	<macro>	SM,(EEF - STP ERROR EXIT, SEE STP HANG MESSAGE)	E.2787
20133a	<macro>	SSTOP000 STOP UC,(EEF - UNKNOWN ERROR)	E.2788

```
*****
** NAME CRASH
* PURPOSE TO ALLOW A USER TO HALT COS (TO BE USED FOR DEBUGGING
* PURPOSES).
* ENTRY A1 == TCB ADDR
* A2 == JTA ADDR
* EXIT A1 == TCB ADDR
* A2 == JTA ADDR
* METHOD IF (PARCEL D OF CRASHF NONZERO) HALT COS
* ELSE ABORT THE USER WITH 'UNDEFINED USER CALL'
**
*****M06586KA.2615
M06586KA.2616
M06586KA.2617
KC3860A.270
M06586KA.2618
M06586KA.2619
M06586KA.2620
M06586KA.2621
M06586KA.2622
M06586KA.2623
M06586KA.2624
M06586KA.2625
M06586KA.2626
M06586KA.2627
M06586KA.2628
M06586KA.2629
M06586KA.2630
M06586KA.2631
M06586KA.2632
M06586KA.2633
M06586KA.2634
M06586KA.2635
M06586KA.2636
M06586KA.2637
```

		* CRASH	
7247b	1000 00000000X	A0 CRASHF,O.	
d	<macro>	ERRAN	
7251b	0401 00000032	S1 ERIUC	IF (CRASHF NONZERO) HALT
d	006 00001461b+	J ABTJ1	ELSE 'INVALID USER CALL AND ABORT THE USER'

HISTORY TRACE

XFT - History Trace Function Table

Debug passes and makes entries into table

Function table gives ability to shut off different types of entries

XTT - History Trace Table

1024 word circular buffer maintained by Debug in Exec

Tasks create messages for Debug

Word 2 from Header points to next entry

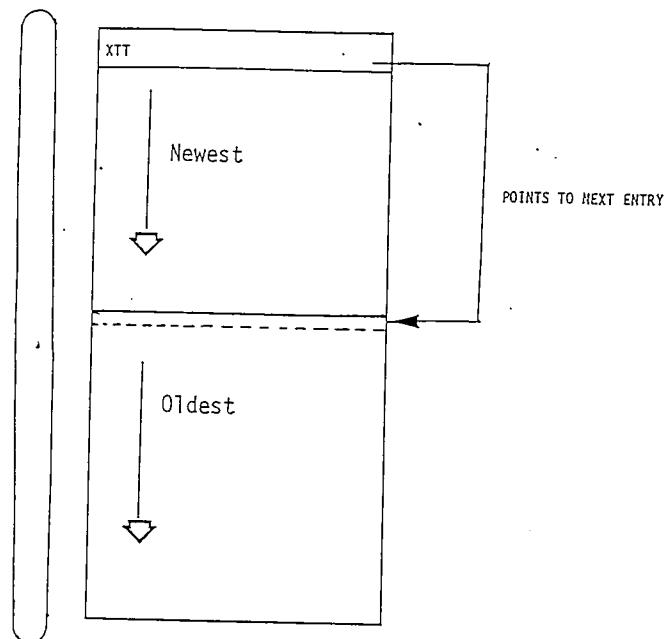
Address biased by XTT address

Entries made with the Post Macro

Each entry is four words

- See SM-040, page 2-75 for each Entry

HISTORY TRACE



DMEM, FWA=1400, LWA=16000.

EXEC TABLES

FDUMP 1.14
SYSDUMP

01/04/85
01/04/85

13:58:19
00:44:57

PAGE 10

MEMORY ERROR LOG

Interrupt Handler is XMEME

Could have occurred reading Exec Area in Idle

Corrects error if correctable

Communicates to Memory Error Processor (MEP)

Has an Exchange Package

A1 is Interrupted Exchange Package

Makes calls to:

Save Memory Error Info

Possibly suspend other CPU

Count Memory Errors

Analyze Syndrome Bits

Halt on Uncorrectable Hit

Build Message Table and Packet

Call MEP to Log the Error

Call to attempt Memory Correction

Returns to Exec Scheduler

Disables single bit count after the installation parameter I@MECCT exceeds 20 in the specified time by I@METO.

Address 15 in STP disables Bit Counting when non-zero.

A single bit hit should not hang the system.

A double bit hit in the user should not hang the system.

A double bit hit in STP will hang the system.

A double bit hit in EXEC is unpredictable.

Memory Error Log

--CRAY OPERATING SYSTEM EXECUTIVE--
area

CRAY XMP CAL 1.14(12/11/84) 12/13/84 14:50:14 Page 45
(45)

```
*****  
**  
* X-MP cluster register dump area.  
**  
* Name: Memory Error Log Table (MEL).  
* Purpose: Logs all single and double bit memory errors.  
*  
*****  
  
2714a <macro> 0 ISFWB NW=4  
2715 04650523000000000000000000000000 B@MEL BSS CON 'MEL' L Header  
2716 0521172504051417200000 1 MELTC BSSZ DATA 'TOTAL=' L  
2717 0515112344351421236400 1 MELSC BSSZ DATA 'SINGLE=' L  
2720 0421172524111421236400 1 MELOC BSSZ DATA 'DOUBLE=' L  
2722 0421052504250325036400 1 MELDS BSSZ DATA 'DETECT=' L  
2724 * BSSZ 1 Non-zero if Single bit detection disabled  
2725 0415172445110520652075 SCRAYX DATA 'CORRECT=' L  
2726 <skipped>— MELNC IFE C@CPTYPE,NE,@CRAYXMP  
2726 00000000000000000000000000000001 MELNC CON 0 Non-zero if single bit correction disabled  
2726 * SCRAYX ELSE CON 1 Non-zero if single bit correction disabled (Default = 1 for XMP)  
2727 0415252445110523452075 SCRAYX END IF  
2730 1 MELCB BSSZ DATA 'CURRENT=' L Current Bank  
2731 1 MELCC BSSZ 1 Current Chip select  
2732 1 MELCS BSSZ 1 Current syndrome  
2733 1 MELCE BSSZ 1 Current error type  
2734 1 MELCR BSSZ 1 Current read mode  
2735 1 MELCT BSSZ 1 Current RTC  
2736 04610124652075000000000000000000 DATA LAST=' L  
2737 1 MELLB BSSZ 1 Last Bank  
2740 1 MELLC BSSZ 1 Last Chip select  
2741 1 MELLS BSSZ 1 Last syndrome  
2742 1 MELLE BSSZ 1 Last error type  
2743 1 MELLR BSSZ 1 Last read mode  
2744 1 MELLT BSSZ 1 Last RTC  
31 L@MEL = W.*-B@MEL Required for FDUMP  
LC5921VA.187  
LC5921VA.188  
LC5921VA.189  
LC5921VA.190  
LC5922GE.5  
LC5922GE.6  
LC5922GE.7  
LC5922GE.8  
LC5922GE.9  
LC5922GE.10  
LC5922GE.11  
LC5922GE.12  
LC5922GE.13  
LC5922GE.14  
LC5922GE.15  
LC5922GE.16  
LC5922GE.17  
LC5922GE.18  
LC5922GE.19  
LC5922GE.20  
LC5922GE.21  
LC5922GE.22  
P10711AA.1  
P10711AA.2  
P10711AA.3  
P10711AA.4  
P10711AA.5  
P10711AA.6  
P10711AA.7  
P10711AA.8  
P10711AA.9  
P10711AA.10  
P10711AA.11  
LC5922GE.23  
LC5922GE.24  
LC5922GE.25  
LC5922GE.26  
LC5922GE.27  
LC5922GE.28  
LC5922GE.29  
LC5922GE.30  
LC5922GE.31  
LC5922GE.32  
LC5922GE.33  
LC5922GE.34  
LC5922GE.35  
LC5922GE.36  
LC5922JA.12
```

POINTERS AND TABLES

Tables have ASCII labels.

Some tables exist in the task itself.

Useful to avoid dumping all of memory.

Used with COS debugger and the IOP operator station.

Used by analyst only.

Pointers to:

EXEC Tables

STP Tables

STP Common Routines beginning Address

STP Task IDs and Base Address

Helps match STP listings to memory.

STP hang message also in this area.

COS TABLE POINTERS

'EM, FWA=1400, LWA=16000.

EXEC TABLES

FDUMP 1.14
SYSDUMP

01/04/85
01/04/85

13:58:19
00:44:57

PAGE

2

DMEM, BIAS=32000, FWA=0, LWA=1000.

STP TABLES

FDUMP 1.14
SYSDUMP

01/04/85
01/04/85

13:58:19
00:44:57

PAGE

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STP EXECUTING ENVIRONMENTS

\$STOP008

- A. Determine the halting task from address SY006 in STP.
- B. Locate the particular hang macro by finding the address in the task.
- C. Use the following suggestions to approach halts in individual tasks.

1. Station Call Processor

Tables to be concerned with are LXT, SST, LIT and rarely SDT.

Locate in SCP XP A2 which is usually LXT entry pointer.

Determine from LXT the ID and last message codes and SCBs exchanged with front end.

- SST contains SCP continuation address, pointer to expected SCB table, and some flags. In particular, I/O outstanding flag.
- Also, SST points to SDT entry of dataset currently being processed.

Use EXEC trace to determine the last few SCB exchanges.

2. Disk Queue Manager

Tables are: EQT, DCT, RQT, DNT, DAT

Two typical DQM Hangs:

- ° O AI encountered in DAT on R/W or deallocation.

This difficult problem with allocation usually follows change to DQM, PDM or EXP.

Must locate failing DAT by looking in RQT entry for DNT address which will be used to locate DAT.

Often multiple dumps are required to locate this.

EQLST field may help.

- ° Looping in DQM

Usually related to configuration of EQT.

Use EXEC trace to note only DQM running and locate P address in DQM.

Problem is probably in device initiator part of DQM.

Use EQT and DCT.

3. Permanent Dataset Manager

Tables are PDD, DSC, DNT, DSC, and DSP.

PDM save area in SPT at address BGNCON
FCODE - Function Code
SVBØ - Return Address
PDBSE - Address of PDD

Trace not usually useful.

Typical problem:

Read past EOD on DSC due to bad input from another task.

4. Job Class Manager

Tables are SDT and CSD.

5. Job Scheduler

Tables are JXT, MST and occasionally SDT.

- Use trace to see what job state changes and memory moves have occurred.

Validate job size in JXT with MST:

JSH storage and constants at address B@STP.

Typical problem:

Operand range error.
Look at memory moves.

6. User Exchange Processor

Tables are JTA and JXT

Use A1 in EXP and XP to locate JTA address

JTA must be dumped

EXP temporary storage is in JTA at W@JTAI, W@JTS7 and at W@JTESTK.

First word of stack is stack pointer and first word of each entry is usually B0.
Stack used a lot by JCL routines.

S0 of user's XP (word 30) in JTA is user's function code.

Use trace to see what job-related events were occurring.

7. Exec Message Processor (MEP)
Table is METTA in STP
8. Log Manager
Tables are LOGJXT, \$SYSTEMLOG, PDD, SYSTEMLOG DNT, \$SYSLOG DSP.
Messages are queued up in pool 1.
Storage in STP at address LGBMEM.
CCSYS and CCUSR valuable
9. System Performance Monitor
Tables are IC, STT, MCT, DCT, CSD.
Storage in STP at SPM INTVL.
10. Disk Error Correction
No Hangs in it
11. Tape Queue Manager
12. Overlay Manager
13. Flush Volatile Device - FVD
14. Start Up
Tables - many
Usually looking at code can indicate what was wrong
Break pointing is helpful
Save areas are resident in STARTUP - sometimes STARTUP must be dumped.

Z	SCP	STG	DQM	PDM	JCM	JSH	DQM	DEC	EXP	MEP	MSG	SPM	IQM	FYD
AUT	-AUT	PDD	-DAT	DAT	-CSD	CSD	(DOL)	EQT	CNT	-AEM	AUT	CSD	ILT	EQT
CNT	-IBT	SDT	DCT	CSD	SDT	-JXT	-(DNT)	-DEX	(DSP)	DCT	VPT	DRT	VCT	
DAT	-LCT	-SST	(DNT)	(DNT)	MST	-(DSP)	(DNT)	(DNT)	JTA	+MCT	VCT			
(DNT)	-LIT	-DRT	DRT	-RJI	RJI	JXT	(DSP)	JXT	+ICT	+ICT	TRB			
DRT	-LXT	(DSP)	-DSC	SDT	LFT	-LFT	-DUX	-DUX	+STT	+STT	IST			
-DSC	PDD	-EQT	(DSP)	-TXT	-TXT	-ODN	-FSH	-FSH	-LGJ	-LGJ				
(DSP)	-SDT	GRT	DXT	JTA	(PDD)	GRT	POD	POD	SDT	SDT				
-DYL	-STT	JXT	EQT	SDT	JXT	JXT	-LOT	-LOT						
DXT	-RQT	JCB	JTA	-SWT	-SM	-SM								
-EFT	JTA	PHR	JXT	TCB	-UPT	-VAX								
-EQT	-EQT	SQP	PDD	-JTA	-JTA	-VUX								
GRT	JXT	TXT	-POI	-TXT	-TXT	JTA								
JTA	JXT	-PDS	-QDT	100	100									
(DDT)	PDI	SDT	-QDT	IRT	IRT									
QDT	QDT	-XAT	TRB											
-RJI														
SDT	TCB													
TCB	TDT													
TDT	TXT													
TXT														
ZSP	USER	DISK/SSD DRIVER	FED DRIVER	PACKET DRIVER	SCP									
		(BGN)	(LDT)	(LDT)	(DSP)									
		(BAT)	(OPT)	(OPT)	(BAT)									
		(DDL)	(JBI)	(JBI)	(DDL)									
		(DSP)	(DRPB)	(DRPB)	(DSP)									
		(DNT)	(RCB)	(RCB)	(DNT)									
		(JAC)	(NCB)	(NCB)	(JAC)									
		(JCB)	(MHB)	(MHB)	(JCB)									
		(LFT)	(IJPB)	(IJPB)	(LFT)									
		(ODN)	(PDD)	(PDD)	(ODN)									
		(JST)	(JST)	(JST)	(JST)									

COS DUMP ANALYSIS

1. STOP BUFFER ____ System utility to format a dump created by SYSDUMP
2. \$STOP MACRO ____ Allows analyst to turn on and off any type of history trace entry
3. ERRSN MACRO ____ History trace for inter-task communication
4. EXCHANGE PACKAGES 100-200 ____ Location in STP TABLES where the STP hang message is put on an STP hang condition
5. XFT ____ Creates a copy of the Cray system memory on the master device scratch buffer for dumps
6. XTT ____ Macro executed by a task when a hang condition exists
7. ITCT ____ Location to look first in a dump to determine who halted first
8. FDUMP ____ Macro executed by EXEC to hang the operating system
9. SYSDUMP ____ Table containing History trace events of the system
10. SY006 ____ Executing CPU Exchange packages on a hang

21 PYJAMA GM 10.000

Cleopatra PA 24250000

REF ID: A9072 .1

No bus no rail or taxi's awolla
No bus no rail or taxi's awolla

ORADAM ROTER: S

Historical slice for future research

ОГЛАШЕНИЕ

Pre STP Hand message is part
of the STP hand configuration.

A EXCHANGE PACKAGES 100-300

Service adoption pattern for
various categories of the CIOs

TRY A

6 new 426 & 1d between 0126M
2119g positive print

TIX 3

to determine which settled later

三

prsnl at 03X3 yd before xgs 0105M
msje re psls 1960 ent

中華書局影印

met have end to achieve

9781528722

Executive CPU Examples

ABOVE OF

IOS Dump Analysis

9

C

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LEARNING OBJECTIVES

With the aid of all furnished reference materials, upon completion of the IOS Dump Analysis module, the learner should be able to:

1. Determine if IOS halted and which IOP halted first.
2. Locate the \$PUNTIF Hang Message.
3. Determine the \$PUNTIF Macro.
4. Locate the \$PUNTIF Macro in the listings.
5. Analyze the History Trace entries.
6. Analyze the Operand Registers.
7. Locate and analyze the ERRBUFF Tables.
8. Find the execution Environment leading to the hang.

IOS HANG

Determine the hung IOP from the hang message.

Run FDUMP to print out the appropriate IOP memory, registers and history trace.

Examine the Operand Registers

Determine the overlay base address from operand register 3.

Determine the overlay number currently running from operand register 63.

Determine executing overlay from LIST0 or the address pointed to by operand register 3.

Examine the Exit Stack and busy-done flags for the channels.

FDUMP will provide a formatted print-out.

EXSAVE will also contain this information.

Determine the E Pointer value and the E Pointer-1 contents.

If this is less than op reg 3 then the hang is in the KERNEL.

If this is more than op reg 3 then the hang is in an OVERLAY and must have %B subtracted from it for a relative address.

Locate the \$PUNTIF macro in the listing.

Examine information in the history trace buffer.

The time is ms.

The overlay that logged an event.

Kernel events are logged.

EACT+1 points to the active activity descriptor chain for all activities in the IOP.

ESMD points to active SMOD.

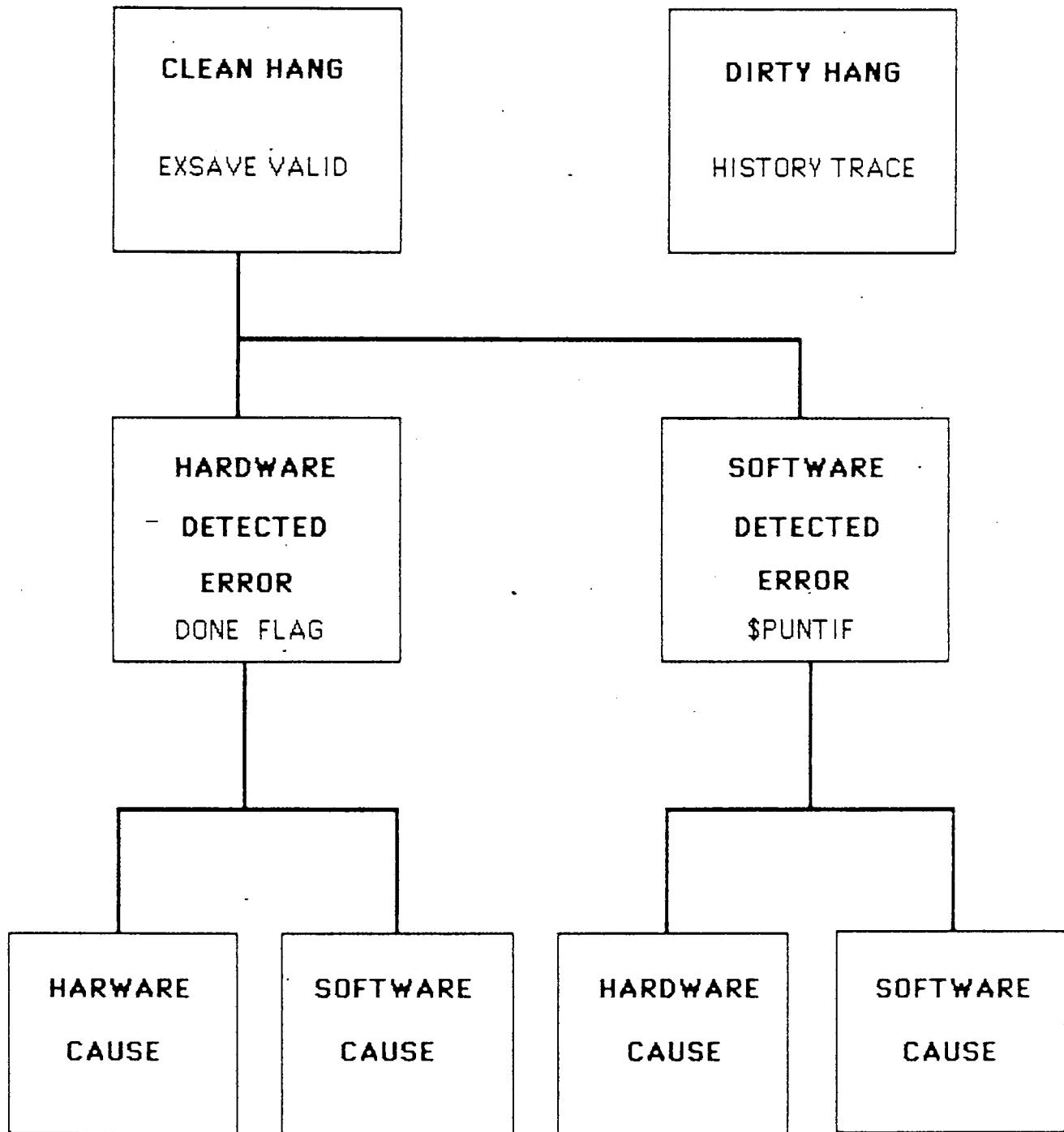
Activity descriptor address.

Size of SMOD entry.

Overlay table address.

A,B,C,E,Exit Stack.

IOS HANG



KERNEL ERROR HALT PROCESS

Entered at 'STOPIT' when software detects an error (\$PUNTIF MACRO is executed).

Disables interrupts

Saves A, C, B, E registers, exit stack, and all channel DN and BZ flags at 'EXSAVE' in kernel.

Saves operand registers at 'OPREGS' in kernel.

Sends error halt message to kernel console

Halts other IOP s (at 'DONE4')

Passes control to SYS_DUMP.

\$PUNTIF FORMAT

LOCATION	RESULT	OPERAND
	PUNTIF	cond1, andor, cond2, CODE=
	cond1 andor code	Valid AMPL conditional clause 'and' or 'or' Symbol of the form PT\$ which defines the reason for halt

KERNEL HALT (PUNT) CODES

CODE	MEANING
000	No error code specified on \$PUNTIF macro
001	Local memory error (always HARDWARE)
002	Buffer memory error on deadstart (always HARDWARE)
003	Buffer memory error (always HARDWARE)
004	High-speed channel error (always HARDWARE)
005	Invalid message received from CPU
006	Invalid parameter in disk request from CPU
007	Program was executing at location 0
010	Local memory location 0 was overwritten
011	Undefined message received on IOP communication channel
012	Overlay does not exist
013	Station stack overflow or underflow
014	Local memory buffer not available
015	Buffer memory disk buffer not available
016	Invalid local buffer release call
017	Buffer memory incorrectly configured
020	IOP message channels incorrectly configured
021	SMOD is too large for area on buffer memory
022	Invalid local memory address
023	Illegal interrupt program sequence code
024	Stop request received from CPU
025	Low-speed channel error (always HARDWARE)
026	Block number validation trap
040	Block Mux interrupt processor error
041	Bad CRW address in device table
042	Block mux start I/O error
044	Block Mux configuration error
050	DD49 disk software error

HALT MESSAGE

, TYPE=IOP0, FWA=4000, LWA=12000, R.

IOP0 LOCAL MEMORY

FDUMP 1.14
SYS_DUMP

01/04/85 18:20:31
01/03/85 23:55:42

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005000	000000	000000	000000	000000	064560	000000	000000	000000			
J005010	000000	000000	000000	000000	064570	000000	000000	000000			
0005020	000000	000000	000000	000000	000000	000000	000000	000000			
J0005030	000000	00043	000000	013740	073410	100313	000141	000000			
00005040	000000	000000	000000	000000	000000	000000	000000	000000			
J00005050	000000	000000	000000	000000	000037	106777	000000	000000			
000005060	000000	000000	001000	000000	000001	060000	000000	000000			
000005070	000047	072100	072170	072260	072350	000000	000000	000000			
000005100	000000	000000	000000	000000	000000	000000	073244	000000			
000005110	000000	000000	000000	000000	000000	000000	040000	072464			
000005120	000000	000000	000000	000000	072564	072524	000000	000000			
000005130	072524	072724	000000	072664	000000	000000	000000	000000			
000005140	072764	000000	000000	000000	000000	000000	000000	076450			
000005150	000000	000000	000000	000000	000000	000000	000000	000000			
000005160	000000	045340	066240	070300	000057	164000	160000	000010			
000005170	000010	000037	000010	000241	064750	000011	000001	021000			
000005200	000313	000006	000400	063330	000003	000000	010610	000024			
000005210	000015	000060	064764	000007	000002	105000	076620	073040			
000005220	000001	000001	000003	000125	000027	000065	000005	000000			
000005230	000000	000000	000000	017240	000000	000000	017306	017367			
000005240	000000	017415	017717	017415	017717	017415	017717	000000			
000005250	000000	030022	027575	025565	026163	000000	000000	026677			
000005260	026677	000000	000000	026677	026677	000000	000000	027121			
000005270	027121	000000	000000	000000	000000	010610	000000	063350			
000005300	000000	014610	000006	063700	000000	020610	000010	064230			
000005310	000000	024610	000012	064650	000000	010610	000000	000000			
000005320	000000	014610	000000	000000	000000	020610	000000	000000			
000005330	000000	024610	000000	000000	000000	010610	000010	064610			
000005340	000000	014610	000006	000000	000000	020610	000010	000000			
000005350	000000	024610	000012	005361	005431	005361	005361	000000			
LOCATIONS 000005360 THROUGH 000005427 CONTAIN 000000											
000005430	000000	000000	000000	000000	063600	064130	064460				
000005440	000000	000000	000000	000000	000000	000000	000000	000000			
000005450	000000	000001	000002	000003	000000	000000	000006	000010			
000005460	000012	050554	000000	000000	064770	000000	000000	000000			
000005470	000000	000000	000120	000000	000000	000000	000000	000000			
LOCATIONS 000005500 THROUGH 000005547 CONTAIN 000000											
000005550	000000	071100	000002	065000	000560	002600	001000	020000			
000005560	000000	177777	177777	177777	177777	177777	177777	177777			
000005570	177777	177777	177777	177777	177777	000002	064000	000002			
000005600	000040	002400	000000	000000	000000	000000	000000	000000			
000005610	000000	000000	000000	000000	000000	000000	000000	000000			
000005620	000000	000000	000000	000000	000002	062000	000000	005015			
000005630	020111	047520	026161	020110	040514	052040	030060	032040			
000005640	005015	000000	005015	041522	040531	020110	040514	052040			
000005650	005015	000000	000000	000000	044517	050040	051524	047520			
000005660	020103	052522	051105	047124	020117	053105	051114	040531			
000005670	035040	020040	020040	020040	020040	020040	005015	000000			
000005700	020120	052516	052040	053501	051440	044516	020124	044105			
000005710	020113	042522	047105	046040	005015	000000	000000	000000			
000005720	000000	000000	000000	000000	000000	000000	000000	000000			
000005730	004441	017130	000000	000000	002301	017352	000000	000000			
000005740	001641	017466	000000	000000	001201	017560	000000	000000			
000005750	000661	017630	000000	000000	006521	017663	000000	000000			
000005760	002040	030610	000000	000000	010420	030712	000430	032560			
000005770	011000	031333	000430	045740	001300	031773	001430	000000			
000006000	006760	032047	000430	000000	006560	032406	001430	000000			

IOP-1 HALT 004
CRAY HALT
TOP STOP
CURRENT OVERLAY

PUNT WAS IN THE KERNEL

! X
6 p
Q 1 5p
2 K 3 p5

LOCATING THE MACRO

Found through Exit Stack contents.

Exit stack can be found in two places.

R on FDUMP directives gives all registers

EXSAVE area

E Pointer-1 give \$PUNTIF Address.

E Pointer in Punt of KERNEL.

Operand registers give overlay number.

DMEM, TYPE=IOP1, FWA=4000, LWA=12000, R,	IOP1 LOCAL MEMORY IOP-1 INTERNAL REGISTERS	FDUMP 1.14 SYSDUMP	01/04/85 01/03/85	18:20:31 23:55:42	PAGE 16
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```

P=016313
A=0000000 C=1
B=014
EXIT STACK: E=05
 00 017157 003545 003613 003710
 04 012141 016313 016201 016201
 10 000000 000000 000000 000000
 14 000000 000000 000000 000000

```

DMEM, TYPE=IOP1, FWA=4000, LWA=12000, R,	IOP1 LOCAL MEMORY IOP-1 OPERAND REGISTERS	FDUMP 1.14 SYSDUMP	01/04/85 01/03/85	18:20:31 23:55:42	PAGE 14
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000000000	000000	012224	005155	025530	000000	000000	045310	045334	m+X	J J
000000010	000045	000666	000000	000000	000000	000000	000000	000001	%	-
000000020	000006	000001	057301	060000	000006	000001	000000	000000	EW	
000000030	000000	005353	005357	042567	000000	000000	000000	000000	G	n o
000000040	000000	177777	043774	000000	000000	005156	005157	000020	J	S o
000000050	045224	000024	051770	005157	060000	000023	000000	000000		
000000060	017240	100313	005220	000010	016276	000000	000000	000000		
000000070	000000	000000	000001	000000	001174	000000	000156	000000		
000000100	000001	025536	000000	007655	000005	000033	000000	000001		
000000110	000000	000045	075674	000107	104423	000000	000106	175761		
000000120	042560	005730	000000	002201	000043	000000	000000	013652		
000000130	000000	026670	000000	013110	000000	025250	025064	005770		
000000140	000004	000000	000040	005563	027730	000001	000000	000000		
000000150	000000	011345	000005	000001	000004	000000	000000	025520		
000000160	002210	025074	000014	060000	000000	000000	046154	001104		
000000170	000000	000003	000004	000000	000000	000000	037054	000000		
000000200	000000	000000	000001	000000	000000	000001	000001	041554		
000000210	000000	046154	000000	000001	041613	005433	041254	005431		
000000220	000001	057307	000001	024006	000006	000000	000002	000000		
000000230	013730	041254	000010	000006	000001	057301	060000	000006		
000000240	000010	000037	107517	062500	000260	000000	000000	000000		
000000250	000000	000000	046314	050116	050340	050637	042520	177777		
000000260	000005	001000	000000	000000	000000	000000	000000	000000	Oe@	L PNP Q EP

HANG MACRO

I/O PROCESSOR KERNEL, VERSION 1.14, SN12, MENDOTA HEIGHTS
COMMON MEMORY CONTROL

TOP APLX.15(12/21/84) 12/21/84 15:17:29 Page 48
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```

* TIMEOUT
12111 <macro>           SIF (A = MIOSTOUT) .>>>> 1 K.1726
12115 002000 000000        I = 0 K.1727
12117 010002 024025        RICF = MIOSTMO SET ERROR TYPE K.1728
12121 070005                P = CEREC GOTO ERROR ROUTINE K.1729
12122 <macro>             SENDIF .<<<< 1 K.1730
12122 <macro>             SENDIF .<<<< 0 K.1731
12122 <macro>             SENDTIL K.1732
12123 010001 024025        RICF = MIOSDTE DEFAULT TO DATA ERROR K.1733
12125 001000                EXIT K.1734
                                         **** K.1735
                                         **** K.1736
                                         **** K.1737
                                         **** K.1738
*          MEMORY I/O ERROR RECOVERY *
*          * K.1739
*          * K.1740
*          B - CHANNEL TO RECOVER * K.1741
*          RICL - CURRENT RETRY COUNT * K.1742
*          RICF - ERROR CODE * K.1743
*          * K.1744
                                         **** K.1745
                                         **** K.1746
12126 160000-              CEREC   * K.1747
12126 <macro>             SPUNTI F (B = RI%HISP) OR (B = RI%HISP + 1),CODE=PTSHISP .>>>> 0 K.1748
                                         .<<<< 0 SIF 2
                                         SENDIF 2
12142 160000-              IOB:0    .CLEAR CHANNEL K.1749
                                         * IF IN MIOP - CHECK ERROR LOG CHANNEL K.1750
                                         * IF SET, LET IT RECORD ERROR K.1751
12143 <macro>              SIF (RI%MYID = MIOP) AND (ERA = DN) .>>>> 0 K.1752
12151 077000 /030025        R = IREPORTO RECORD ERROR K.1753
12153 <macro>              SENDIF .<<<< 0 SIF 1
                                         * UPDATE RETRY COUNT - SENSE LIMIT K.1754
12153 026026                RICL = RICL + 1 K.1755
                                         * ERROR FATAL IF RETRY LIMIT EXCEEDED K.1756
12154 <macro>              SIF (A > MIOSMAX) .>>>> 0 SIF 1
12163 <macro>              SPUNTI F (B = [MOS]),CODE=PTSMOS MOS ERROR .>>>> 0 K.1765
12167 <macro>              SPUNTI F CODE=PTSHISP HIGH SPEED CHANNEL ERROR K.1766
12171 <macro>              SENDIF .<<<< 0 SENDIF 1
                                         * RESET PARAMETERS FROM SAVED SET K.1767
                                         * AND RETRY THE I/O K.1768
12171 <macro>              SIF (B = [MOS]) .IF BUFFER MEMORY .>>>> 0 K.1769
                                         .>>>> 0 SIF 1
                                         K.1770
                                         K.1771
                                         K.1772
                                         K.1773

```

HISTORY TRACE

Provides a means for tracing, to a certain degree, the path of execution through the code.

Stores pertinent data relating to selected events in a local memory buffer.

Local trace buffer is dumped to a circular buffer in buffer memory.

May be used in debugging and fine tuning the system.

See SM-46 page 11-4 for more detail.

Parameters depend on type of entry printed in format by FDUMP.

HISTORY TRACE

DXTR TYPE=IOP0

EVENT	RTC-L	OVL-#	PAR-1	PAR-2	PAR-3	PAR-4	PAR-5
I-HAND	007655	CONSL	000006	177004	050554	000057	000010
TASK	007655	CONSL	056004	001067	000426	076560	000010
MEM-10	007655	CONSL	000002	037063	100164	000021	
K-CALL	007655	CONSL	MOSR	001067	065133	076560	000456
TASK	007655	CONSL	056004	000006	000426	076560	000010
K-FNCT	007655	BABEL	CALL	065133	000426	010060	006430
K-CALL	007655	BABEL	CALL	001223	065065	076560	000462
TASK	007655	BABEL	057634	001417	000372	076560	000010
K-CALL	007655	BTD	RETURN	000227	065133	076560	000450
TASK	007655	BTD	057350	000006	000016	076560	000010
OVL-LD	007655	BTD	000016	057350	000021	000000	025066
MEM-10	007655	BABEL	000001	032735	057350	000053	
K-FNCT	007655	BABEL	CALL	065133	000016	006020	007430
K-CALL	007655	BABEL	CALL	001417	065065	076560	000462
INTRPT	007655	BABEL	000021	061057	057634	000000	131214
INTRPT	007655	BABEL	000016	061057	057634	000000	131214
INTRPT	007655	BABEL	000007	061057	057634	000000	131214
TASK	007655	BABEL	057634	001223	000372	076560	000010
MEM-10	007655	KERNEL	000001	000002	111200	064770	000033
MEM-10	007655	KERNEL	000004	000002	107200	064770	000024
A-TO-A	007655	CRAY10	000007	100313	000043	000000	000000
MEM-10	007655	CRAY10	000003	000000	013740	005030	000010
K-FNCT	007655	CRAY10	FE-10	000001	060000	001000	000000
K-CALL	007655	CRAY10	FE-10	000707	065031	073410	000464
INTRPT	007655	CRAY10	000007	035553	034674	000000	131214
TASK	007655	CRAY10	034674	000657	000430	073410	000010
A-TO-A	007655	CRAY10	000007	100312	000043	000000	000000
MEM-10	007655	CRAY10	000003	000000	013730	005030	000010
K-FNCT	007655	CRAY10	FE-10	000001	024006	000006	000001
K-CALL	007655	CRAY10	FE-10	000657	065031	073410	000464
TASK	007655	CRAY10	034674	000436	000430	073410	000010
K-CALL	007655	CRAY10	RELDAL	000436	065031	073410	000464
TASK	007655	CRAY10	034674	00366	000430	073410	000010
MEM-10	007655	KERNEL	000001	000002	107200	064770	000024
MEM-10	007655	KERNEL	000004	000002	111200	064770	000033
K-CALL	007655	CONSL	RETURN	000677	065133	076560	000456
TASK	007654	CONSL	056004	001067	000426	076560	000010
MEM-10	007654	CONSL	000002	000001	037042	100164	000021
K-CALL	007654	CONSL	MOSR	001067	065133	076560	000456
TASK	007654	CONSL	056004	000006	000426	076560	000010
K-FNCT	007654	BABEL	CALL	065133	000426	010060	006430
K-CALL	007654	BABEL	CALL	001223	065065	076560	000462
INTRPT	007654	BABEL	000020	057642	057634	000000	131214
I-HAND	007654	BABEL	000020	000000	070300	070340	000102
TASK	007654	BABEL	057634	000006	000372	076560	000010
OVL-LD	007654	BABEL	000372	057634	000020	000000	025065
MEM-10	007654	DISP01	000001	000000	145317	057634	000347
K-FNCT	007654	DISP01	CALL	065065	000372	007700	005430
K-CALL	007654	DISP01	CALL	000444	065021	076560	000461
K-CALL	007654	DISP01	041130	003624	000437	076560	000010
K-CALL	007654	MULTIP	RETURN	000063	065066	076560	000440

DXTR TYPE=IOP1

EVENT	RTC-L	OVL-#	PAR-1	PAR-2	PAR-3	PAR-4	PAR-5
MEM-10	007655	AMSG	000006	000001	057301	060000	000006
K-CALL	007655	AMSG	TRANSF	001174	045334	045310	000157
INTRPT	007655	AMSG	000006	025536	025530	000045	075674
I-HAND	007655	AMSG	000006	100313	047125	000077	000023
TASK	007655	AMSG	025530	000006	000010	045310	000023
ACOM	007655	ACOM	000043	041254	100312	000000	000000
MEM-10	007655	ACOM	000002	000000	013730	041254	000010
MEM-10	007655	ACOM	031064	000006	000007	045250	000023
INTRPT	007655	KERNEL	000006	013107	000000	000045	075674
I-HAND	007655	KERNEL	000006	100312	047124	000100	000023
INTRPT	007654	KERNEL	000007	013114	000000	000045	065043
A-TO-A	007654	AMSG	000007	100311	000044	000000	000400
MEM-10	007654	AMSG	000003	000000	013720	042014	000010
TASK	007654	AMSG	025530	001174	000010	045310	000023
MEM-10	007654	AMSG	000010	000037	107517	062500	000260
MEM-10	007654	AMSG	000002	000001	060520	062500	000260
MEM-10	007654	AMSG	000010	000037	106777	060000	000520
MEM-10	007654	AMSG	000002	000001	060000	060000	000520
K-CALL	007654	AMSG	TRANSF	001174	045334	045310	000457
TASK	007654	AMSG	025530	000006	000010	045310	000023
ACOM	007654	ACOM	000043	042014	100311	000000	000000
INTRPT	007654	ACOM	000007	012036	031064	000045	064637
MEM-10	007654	ACOM	000002	000000	013720	042014	000010
TASK	007654	ACOM	031064	000006	000007	045250	000023
A-TO-A	007654	AMSG	000007	100310	000044	000000	000400
MEM-10	007654	AMSG	000003	000000	013710	037554	000010
TASK	007654	AMSG	025530	001174	000010	045310	000023
MEM-10	007654	AMSG	000010	000001	057271	060000	000006
MEM-10	007654	AMSG	000002	000001	024000	060000	000006
K-CALL	007654	AMSG	TRANSF	001174	045334	045310	000457
INTRPT	007654	AMSG	000006	025536	025530	000045	064637
I-HAND	007654	AMSG	000006	100311	047123	000077	000023
TASK	007654	AMSG	025530	001174	000010	045310	000023
MEM-10	007654	AMSG	000010	000001	057271	060000	000006
MEM-10	007654	AMSG	000002	000001	024000	060000	000006
INTRPT	007654	AMSG	000006	025530	025530	000045	064637
I-HAND	007654	AMSG	000006	100311	047123	000077	000023
TASK	007654	AMSG	025530	000006	000010	045310	000023
MEM-10	007654	AMSG	000001	000001	057271	060000	000006
MEM-10	007654	AMSG	000002	000001	024000	060000	000006
K-CALL	007654	AMSG	TRANSF	001174	045334	045310	000457
INTRPT	007654	AMSG	000006	025536	025530	000045	064637
I-HAND	007654	KERNEL	000006	100310	047122	000100	000023
INTRPT	007476	KERNEL	000007	013110	000000	000035	107710
A-TO-A	007476	AMSG	000007	100307	000044	000000	000000
MEM-10	007476	AMSG	000003	000000	013700	037314	000010
TASK	007476	AMSG	025530	001174	000010	045310	000023
MEM-10	007476	AMSG	000004	000001	057520	062500	000260
MEM-10	007476	AMSG	000006	000037	107517	062500	000260
MEM-10	007476	AMSG	000004	000001	057000	060000	000520
INTRPT	007476	AMSG	000007	012044	025530	000035	107710
MEM-10	007476	AMSG	000006	000037	106777	060000	000520
K-CALL	007476	AMSG	TRANSF	001174	045334	045310	000457
TASK	007476	AMSG	025530	000006	000010	045310	000023
ACOM	007476	ACOM	000043	037314	100307	000000	000000
MEM-10	007476	ACOM	000002	000000	013700	037314	000010

ERRBUFF

Error logger entry buffer

512 word buffer for error logger

Before IOP #27

After IOP #27

See error logger HM-29, page 7-60 for breakdown

A single bit hit in Buffer Memory is reported.

A double bit hit in Buffer Memory will hang the system.

A single bit hit in Local Memory (2AW-majority rule) is undetectable or reportable.

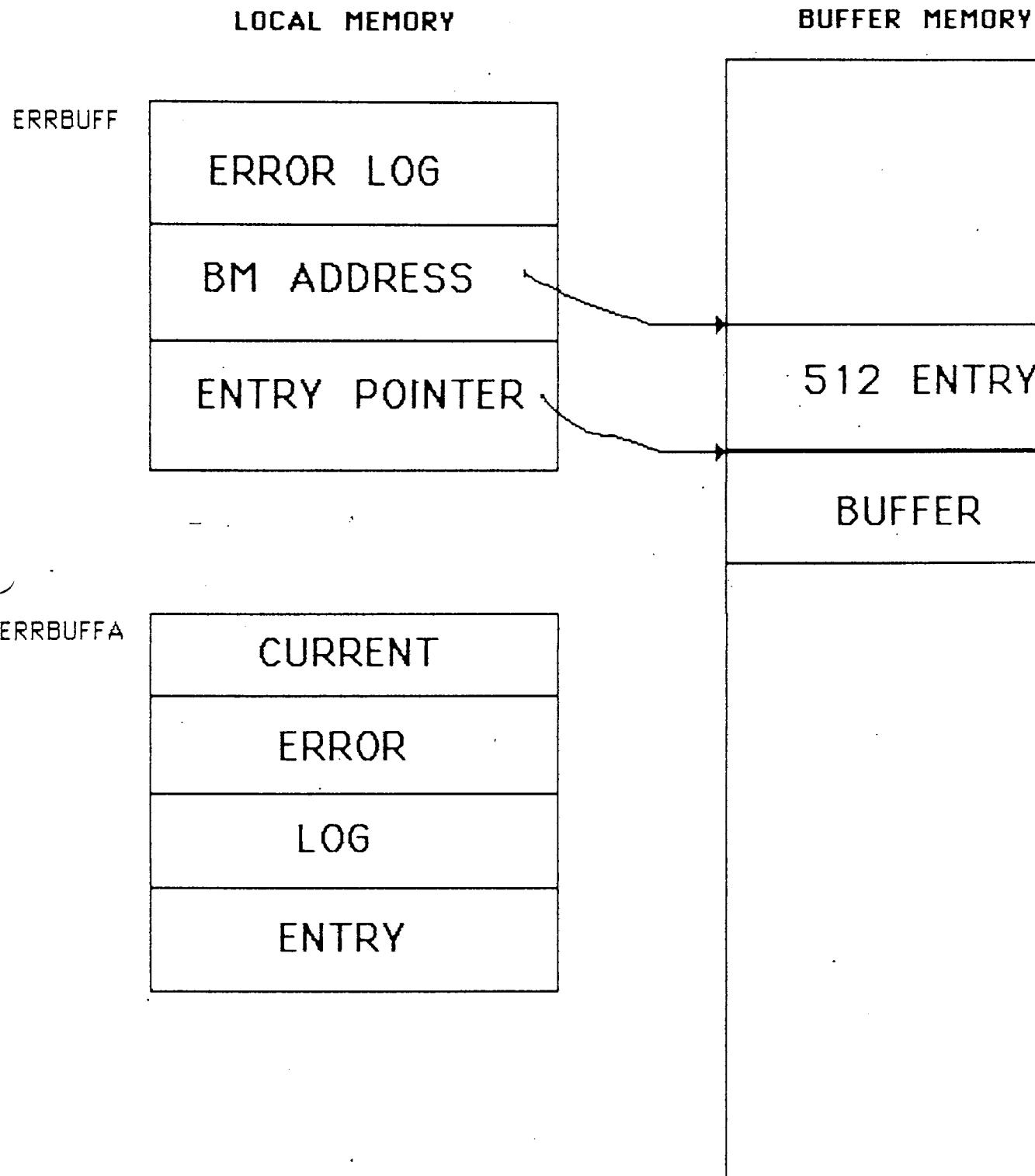
A parity error will hang the system.

A high speed channel error will hang the system.

CPU		MEMORY		IOP		DMA		IOP		DMA	
TYPE	SYND	CPU	MEM	PORT	ADDRESS	WORD	CODE	RNK	SOF	BYT	CODE
IOP	SB	2016		0	11410000						

PSR
001

ERRBUFF



POINTERS AND TABLES

Operand registers contain pointers

Local memory has a table area

Tables are not labeled, to find a table you need matching listings

Match up with ASCII label tables

Demon Queue Pointers

Disk Control Block Printers

CRT Buffers

EACT - Activity Descriptor Chain Pointers

Buffer Memory Allocation

Disk Buffer Pointers DAL Packet Pointers

EITB - Interrupt Handler Table

Interprocessor MSG Queue

Local Memory Errors

Buffer Memory Errors

Halt Message

Overlay Table

OPERAND REGISTER POINTERS

I/O PROCESSOR KERNEL, VERSION 1.14, SN12, MENDOTA HEIGHTS

IOP APML X.15(12/21/84) 12/21/84 15:17:29 Page 2
(2)

			BASEREG	AA			
			SCRATCH	AB,AC			
*					*****	*	K.43
*						*	K.44
*					Kernal operand registers	*	K.45
*						*	K.46
*****							K.47
							K.48
							K.49
							K.50
0	<macro>	0	0	REGISTER AA	.Base register		K.52
				SREGORG		SREG	
0	<macro>	0	AA	REGISTER (AB,AC)	.APML scratch registers		K.53
				SREGORG		SREG	
0	<macro>	1	AB	EQUALS SREGORG			
		2	AC	EQUALS SREGORG			
0	<macro>	3	%B	REGISTER %B	.Overlay base address		K.54
				SREGORG		SREG	
0	<macro>	4	BB	EQUALS BB			K.55
				SREGORG		SREG	
0	<macro>	5	%PSMOD	REGISTER %PSMOD	.Previous SMOD address		K.56
				SREGORG		SREG	
0	<macro>	6	%ACTIVE	REGISTER %ACTIVE	.Activity descriptor address		K.57
				SREGORG		SREG	
0	<macro>	7	%SMOD	REGISTER %SMOD	.Current SMOD address		K.58
				SREGORG		SREG	
0	<macro>	10	%FUNC	REGISTER %FUNC	.Kernel service request type		K.59
				SREGORG		SREG	
0	<macro>	11	%EX	REGISTER %EX	.Kernel call entrance		K.60
				SREGORG		SREG	
0	<macro>	12	BD	REGISTER (BD,BE,BF,BG,BH)	.CRT I/O		K.61
				SREGORG		SREG	
0	<macro>	13	BE	EQUALS SREGORG			
		14	BF	EQUALS SREGORG			
0	<macro>	15	BG	EQUALS SREGORG			
		16	BH	EQUALS SREGORG			
0	<macro>	17	%INIT	REGISTER %INIT	.Initialization complete flag		K.62
				SREGORG		SREG	
0	<macro>	20	CA	REGISTER (CA,CB,CC,CD,CE,CF)	.MOS and HiSp I/O		K.63
		21	CB	EQUALS SREGORG			
0	<macro>	22	CC	EQUALS SREGORG			
0	<macro>	23	CD	EQUALS SREGORG			
0	<macro>	24	CE	EQUALS SREGORG			
0	<macro>	25	CF	EQUALS SREGORG			
0	<macro>	26	CL	REGISTER CL			K.64
				SREGORG		SREG	
0	<macro>	27	%DEBBA	REGISTER %DEBBA	. TEMP *****		K.65
0	<macro>	27	%DEBBA	EQUALS SREGORG			SREG
0	<macro>	27	%DEBBA	EQUALS SREGORG	. Debugger base address		K.66
0	<macro>	30	%DEBUG	REGISTER %DEBUG			SREG
0	<macro>	30	%DEBUG	EQUALS SREGORG	. Debugger base address		K.67
0	<macro>	30	%DEBUG	REGISTER (ED,EE,EF,EG)	. Kernel registers		SREG
		31	ED	EQUALS SREGORG			
0	<macro>	32	EE	EQUALS SREGORG			
0	<macro>	33	EF	EQUALS SREGORG			
0	<macro>	34	EG	EQUALS SREGORG			
0	<macro>	35	EH	REGISTER (EH,EI,EJ,EK)			
				SREGORG			

KERNEL TABLE POINTERS

I/O PROCESSOR KERNEL, VERSION 1.14, SN12, MENDOTA HEIGHTS
Tables and data areas

IOP APMX X.15(12/21/84) 12/21/84 15:17:29 Page 25
(25)

			*****	K.598
			*	K.599
			*	K.600
			*	K.601
			*****	K.602
		BLOCK ONE		K.604
		CON "TABLES"		K.605
1140W	0251242024111421251452	CON "SDATE"		K.606
	<edit>	CON "12/21/84"		*EDITED 0
1141W	0304621363106113634061			*EDITED 0
	<edit>	CON "STIME"		K.607
1142W	0304651643046716431071	CON "15:17:29"		*EDITED 0
		*****		*EDITED 0
				K.615
4614	000000	BHFLG PDATA I@BNFLG	.0 = no block number check	

		*	DEBLOC is the location to which the Debugger is copied to	K.618
		*	allow debugging during IOP initialization. The Debugger	K.619
		*	relocatable code occupies a maximum of 51 words.	K.620
				K.621
				K.622
	174004	DEBLOC EQUALS 0'174004		

		*	Disk demon queue. The queue entries are linked via cell	K.625
		*	DD@DEM in the DCB.	K.626
				K.627
4615	000000	DISKQ 0	.Pointer to first entry	K.628
4616	000000	DISKQT 0	.Pointer to last entry	K.629

		*		K.631
		*		K.632
		*	DD49 spiral conversion table	K.633
		*		K.634
		*****		K.635
				K.636
4617	000000	D4CNVRT 0		K.637
4620	000012	D'10		K.638
4621	000025	D'21		K.639
4622	000037	D'31		K.640
		*****		K.642

MEMORY CHAIN HEADERS

SYMBOL	ADDRESS	CONTENTS
EDES		Pointer to First Free DAL Entry
EDES + 1		Pointer to Last Free DAL Entry
EDNM		Number of Free DAL Entries Available
EMEM		Pointer to First Free Memory Entry
EMEMSIZ		Maximum Free Memory Available
EBUF		Pointer to First Free I/O Buffer
EBUH		Pointer to Last Free I/O Buffer
EBNM		Number of I/O Buffers Available
EBUFSIZ		Maximum Number of I/O Buffers Available

FINDING THE EXECUTING ENVIRONMENT

This section is a list intended to serve as a guide to analyzing I/O Subsystem dumps.

Record any operator interactions with the Kernel for later reference.
Record time and date dump was taken.

Examine punt code in message. Get meaning of coded dumps from LISTP key-in.

Take dump according to Cray Research systems analyst's specifications.

Dump Kernel tables, DALS, non-Kernel tables and free memory, Disk Control Blocks (DCBs).

Make sure listing matches dump. Assembly date in listing should match date in Deadstart Done message. If they do not match, the approximate difference in the Kernel can be determined from the dump by finding IAA in the listing and comparing it to the first entry in the exit stack.

Determine overlay base address from operand register 3 (%B). If zero, the halt occurred within the Kernel resident area.

Determine overlay number currently running from operand register 63 (%OVLNUM).

Determine overlay from LIST0 listing or by examining address in operand register 3. (First eight characters in an overlay is its name in ASCII.)

Get exit stack (E) pointer from location EXSAVE+3. EXSAVE is a label in the Kernel immediately after the Kernel tables. Its format is:

- 0 - Contents of A at halt
- 1 - Carry (C)
- 2 - B register
- 3 - Exit Stack pointer (E)
- 4-23 - Exit Stack
- 24-163 - DN + BZ (done and busy status) of all channels at halt

Get P from EXSAVE+4+E-1. P is the address of a return jump or point in code when an interrupt occurred.

If P is in an overlay, determine relative P by subtracting base address (%B). (P is in an overlay if P greater than %B:)

Once the appropriate \$PUNTIF macro is found, use trace dump to determine sequence of events leading up to halt (section 10 or this manual describes trace entries).

EXSAVE LAYOUT AFTER A HALT

* EXSAVE *	
A	
C	
B	
E	
0	
1	
2	
3	
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
0 BZ	
0 DN	
•	
•	
•	
47 BZ	
47 DN	

Examine information in history traces:

The time of the trace entry in millisecond increments is saved, which gives useful impression of length of time between events.

The overlay in control at the time of the trace is always saved. If the Kernel is in control (usually in the idle loop), the overlay number is 177777.

On task activation traces ('TASK' - type 3) the address of the Activity Descriptor is saved. This information is useful to find which of the activities in the system is doing what and to analyze activity conflicts and possible timing problems.

Service requests to the Kernel are always traced and are usually helpful to understand sequence of an activity's functions. In FDUMPs they are printed as K-FNCT (in raw dumps they are type 5).

If you suspect what is causing a halt (for example, A RELMEM) but cannot find it in trace (it may have happened too far in the past), try to recreate halt but with only one or two specific trace events enabled.

Look for a pattern in the trace dump that would indicate that the system is looping through a group of overlays repeatedly. Try to isolate the reason for the loop.

If the Kernel does not halt but seems to hang up and you are unable to access the system via the Kernel CRT, there may be an infinite loop in an overlay.

Find the overlay in register 3 (%B).

Examine the operand registers to determine the extent and reason for the loop.

The done (DN) and busy (BZ) states of all channels are saved in the dump immediately after the exit stack. Although they are normally printed by FDUMP, you must have some more information when reading a raw dump:

If the channel is DN, the first parcel of the two for this channel is set to 1.

If the channel is BZ, the second parcel is set to 1.

Accumulator channels from the IOPs for communication usually have the input half busy (BZ).

The command channel for input from the Cray mainframe is normally awaiting input and is busy (BZ).

The CRT input channels are usually waiting for characters to be entered from the keyboard and thus are busy (BZ).

Important Operand Registers

Symbol	Value	Description
AA	0	Base address of Kernel (always zero)
AB,AC	1,2	APML Scratch Registers
%B	3	Base address of overlay (zero if none)
%ACTIVE	6	Current Activity Descriptor (177777 if none)
%SMOD	7	Address of SMOD being run (177777 if none)
%OVENUM	63	Pointer to current overlay number (1,77777 if none)
%PUNT	64	Address of Punt Routine
RD,RE	100,101	E and (E) at last interrupt
%CLKU,%CLKL	102,103	Kernel Real-time Clock
%ICHAN	106	Channel of last interrupt (zero if all are processed)
%HISP	165	Channel number of high speed channel (zero if none present)

Examine dedicated Kernel registers for irregularities.

Examine Activity Descriptors (AD) to see if information in them is meaningful. The last AD activated will have its address in %ACTIVE (operand register 6) if it is active. All Activity Descriptors in the IOP are linked together through parcel 1 (KA\$ALNK) of the Activity Descriptors. The chain begins at EACT+1.

Examine SMOD area in Local Memory to find registers at time of last service request to Kernel. Address of SMOD area is found in location ESMD in the Kernel tables. There is one SMOD area for each IOP, used only when an activity is active. Other SMODs are saved in Buffer Memory at an address found in the AD at location AD@MSU (parcels 3 and 4).

Things found in SMOD that are good clues to problems are:

Activity Descriptor addresses in local memory

Size of SMOD entry

Overlay table address of overlay making last call

A,B,C,E

Exit stack

Operand registers saved on last CALL

Global registers if SMOD just read into memory

For disk-related problems, examine DCBs to see if any irregularities are evident. Note the following items.

Pointers to DCBs are found in Kernel tables DCCB through DCCB+17. (The last DCB to have any processing done is often in register DA register 200.)

Channels that are being used have a nonzero value in parcel 0 (DD@FLG) and addresses of executable DALs are in parcel 2 (DD@EDL).

The DCB contains a count of recovered and unrecovered errors on the channel and saved information about the last unrecovered error (such as cylinder, head, section).

PROGRAM EXECUTION ENVIRONMENT

PROCESSOR QUEUE - ECPQ

OVERLAY NUMBER %OVLNUM -

OVERLAY NAME -

OVERLAY BASE ADDRESS %B -

ACTIVE AD %ACTIVE -

ACTIVE SMOD %SMOD -

CURRENT AD SS ESMD -

LOCATION 0 OF SS -

LOCATION AD+SMOD@P -

SMOD+SM@CAL@P -

LAST HISTORY TRACE EVENT -

PROGRAM EXECUTION ENVIRONMENT

PROCESSOR QUEUE - ECP0

- OVERLAY NUMBER VOLMIN

- OVERLAY NAME -

- OVERLAY BASE ADDRESS 88 -

- ACTIVE AD ACTIVE -

- ACTIVE SMD SMD -

- CURRENT AD 22 ESM0

- LOCATION OF 22 -

- LOCATION AD+SM0#P

- SM0D+SM#CAL#P

- LAST HISTORY TRACE EVENT -

IOS DUMP ANALYSIS

Match the Following terms and their description:

- | | |
|-----------------------------|---|
| 1. HALT MESSAGE | ____ Operand register containing the address the executing overlay is loaded at |
| 2. \$PUNTIF | ____ Points to the Hang macro address to help locate it in the listing |
| 3. %B | ____ Operand register containing the overlay number that was executing |
| 4. EXIT STACK | ____ Area in the Kernel tables where the IOP's A, B, C, E, Exit stack and Busy Done flags are saved on a \$PUNTIF |
| 5. CONTENTS OF E-1 IN STACK | ____ Operand register containing the address the active SMOD is at |
| 6. %OVLNUM | ____ Return jump history stack after an interrupt or an exit |
| 7. %ACTIVE | ____ Macro executed by the IOS to halt on an error condition |
| 8. EXSAVE | ____ Operand register containing the address of the active activity descriptor |
| 9. BUSY and DONE flag set | ____ Tells you which IOP halted and the punt code attached to the \$PUNTIF macro |
| 10. %SMOD | ____ High Speed Channel Error |

120 DUNS ANALYSIS

Major tasks following first stage and their dependencies

— Global and regional configuration file
— Sublease file executed over Java
— Is loaded in

1. INIT MESSAGE

— Points of distribution module
— Sublease to local police if in
the vicinity

2. BURNIN

— Global and regional configuration file
— Overlay update first was
executed in

3. BB

— Access of the kernel space module
— file 10P A, B, C, D, E, F, G, H, I stack
and binary done (use of shared
memory)

4. EXIT STACK

— Global and regional configuration file
— Sublease file selective SMD if ac

5. CONTENTS OF E-1 IN STACK

— Return from interrupt stack
— file of interrupt to the exec

6. MEMORY

— Macro executable file 102 to
load all other configuration files

7. STACK

— Global and regional configuration file
— Sublease of the static memory area
accessible

8. EXCAVATE

— File A on which 10P passed and
the output code starts to fire
SPNTRI module

9. GRSA AND DRCU FILE SET

— High speed channel block

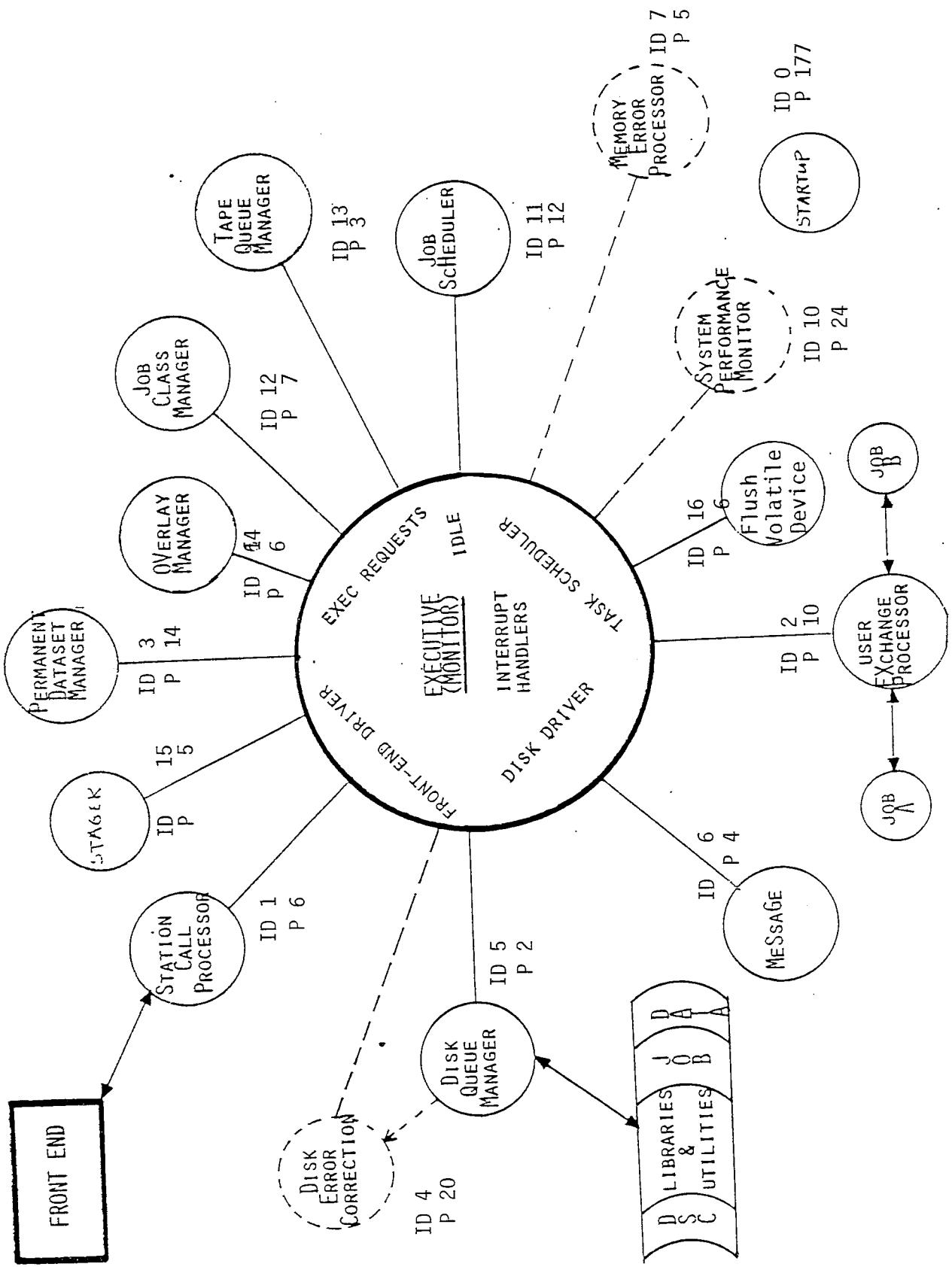
10. ZOOMED

Appendix A - Quick References

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OVERLAY DESCRIPTIONS

C

This appendix describes the overlays used by I/O Subsystem software. The list is alphabetical according to overlay name. Each overlay has one or more of the following type codes.

<u>Type</u>	<u>Description</u>
D	Data overlays. These overlays contain data and cannot be executed.
K	Kernel overlays
F	File system overlays associated with the I/O Subsystem Editor. See Appendix F of the I/O Subsystem (IOS) Operator's Guide, CRI publication SG-0051, for a description of the capabilities of the Editor.
C	Concentrator overlays
S	Station overlays
I	Interactive station overlays
B	Block multiplexer overlays. These overlays drive the block multiplexer channels.
T	Tape overlays
R	System dump and restart overlays
Ns	NSC (Network Systems Corporation) channel concentrator overlays

KERNEL HALT (PUNT) CODES

CODE	MEANING
000	No error code specified on \$PUNTIF macro
001	Local memory error (always HARDWARE)
002	Buffer memory error on deadstart (always HARDWARE)
003	Buffer memory error (always HARDWARE)
004	High-speed channel error (always HARDWARE)
005	Invalid message received from CPU
006	Invalid parameter in disk request from CPU
007	Program was executing at location 0
010	Local memory location 0 was overwritten
011	Undefined message received on IOP communication channel
012	Overlay does not exist
013	Station stack overflow or underflow
014	Local memory buffer not available
015	Buffer memory disk buffer not available
016	Invalid local buffer release call
017	Buffer memory incorrectly configured
020	IOP message channels incorrectly configured
021	SMOD is too large for area on buffer memory
022	Invalid local memory address
023	Illegal interrupt program sequence code
024	Stop request received from CPU
025	Low-speed channel error (always HARDWARE)
026	Block number validation trap
040	Block Mux interrupt processor error
041	Bad CRW address in device table
042	Block mux start I/O error
044	Block Mux configuration error
050	DD49 disk software error
051	The debugger was not loaded
052	Bad buffer memory allocation request
053	Bad local memory address on hi speed I/O call
054	Invalid I/O length specified
055	No hi speed channel configured for request
056	Illegal activity activation requested
057	Buffer memory DAL queue exhausted
060	Illegal Demon call
061	Undefined kernel service request
062	Illegal kernel service request
063	Bad kernel service request parameter
064	Requested device not configured
065	Illegal IOP requested on kernel service request
066	Requested queue full
067	Illegal I/O address specified
070	SMOD error
071	Local memory space exhausted
072	Illegal overlay load requested
073	Corrupted local memory chain
074	Bad local memory release
075	Bad I/O parameter
076	Unexpected interrupt received
077	Disk error
100	AMAP not available for system initialization
101	Illegal overlay number read during initialization
102	IOP initialization error
103	MOS configuration error
104	Overlay too large for loading
105	Premature tape EOF encountered
106	Exit stack fault
107	Illegal device type configured

No.	Ov1	Name	Size	BM-Upr	BM-Lwr	No.	Ov1	Name	Size	BM-Upr	BM-Lwr
00	SDMPA	1110	01	021373		01	SDMPB	0460	01	021615	02
03	SDMPD	0240	01	022023		04	SDMPE	0154	01	022073	05
06	AMAP	0410	00	020670		07	AMSG	1740	00	020772	05
11	BLOCK	1534	00	021756		12	BMOL	4610	00	033652	13
14	BTD	0254	00	022305		15	BTH	0374	00	022360	16
17	BXSET	0340	00	022524		20	CALL	2010	00	022614	21
22	CHNST	0774	00	023747		23	CLKSNC	1010	00	024146	24
25	CONFIG	1334	00	024573		26	CPOL	2474	00	035265	27
30	CPTEST	2370	00	025142		31	CRAY	1330	00	025640	32
33	DISKIO	1024	00	026560		34	DIVIDE	0444	00	026765	35
36	DKIOEX	0430	00	030012		37	DKLOOK	0100	00	030120	40
41	DKSET0	0260	00	030220		42	DM3	0230	00	030274	43
44	DOMP	2554	00	030663		45	DTB	0240	00	031416	46
47	ERR	1604	00	031656		50	ERRDMP	1514	00	032217	51
52	F80ME	2104	00	035004		53	FDMPDR	0520	00	037165	54
55	GETEXT	0164	00	037370		56	HDRPAG	1074	00	037425	57
60	HSPTES	0000	00	040615		61	INFORM	0200	00	041260	62
63	INDD39	2120	00	041663		64	INDD49	2164	00	042307	65
66	LSTP	2700	00	043114		67	LPT	1360	00	036671	70
71	MFINIT	2330	00	043674		72	MOSTES	1524	00	044362	73
74	MULTIPLY	64	00	045604		75	NOBEAT	0400	00	045621	76
77	PATCH	0150	00	051420		78	OUTCALL	0100	00	046123	79
81	0102	0640	00	051420		80	PLOTIT	0504	00	051570	81
85	SCRUB	0105	00	052212		86	SETIME	0106	00	052250	87
91	START0	0534	00	053014		92	START1	0314	00	053143	93
93	START3	0113	00	053415		94	START4	0330	00	053571	95
97	STOP	0250	00	054022		98	SYSS	0114	00	054076	99
101	TCOM	0230	00	055262		102	TDUMP	0122	00	055330	103
104	TRACE	0124	00	056120		105	TRACK	0125	00	055406	106
107	UBTAPE	1500	00	056535		108	UNBLK	0730	00	057055	109
113	WATCH	0234	00	057274		114	X CLOCK	1720	00	057343	115
115	XDISK	0754	00	057575		116	XDISKA	0136	00	057576	117
119	XDK	1020	00	061557		120	XMT	1220	00	058072	121
124	XPR	0143	00	062466		125	XPRINT	0774	00	062612	126
127	XPRNTB	0146	00	063205		128	X TAPE	0147	00	063357	129
132	XTAPEB	1320	00	063712		133	XTAPEC	0152	00	064176	134
135	ACOM	0151	00	064511		136	BADDAT	0260	00	065131	137
140	D3ERR	2550	00	065303		141	D310R	0160	00	066035	142
143	D3MSG	0162	00	066573		144	D3RLR	0163	00	067067	145
146	D3SLR	0165	00	067514		147	XTAPEA	0750	00	068357	148
151	D4ERR	2540	00	067704		149	XTAPED	0610	00	069153	150
170	D4MSG	0514	00	072424		151	D3ECC	0155	00	070453	152
173	D4RLR	0654	00	073036		153	D3LOG	0161	00	070620	154
176	D4STAT	0234	00	073766		155	D3SKR	0164	00	070760	156
179	EMRECK	3170	00	075516		160	D4DEM	0166	00	070745	161
204	MEMIO	2064	00	077257		161	D4IOR	0171	00	071434	162
207	XDERR	0207	00	077007		163	D4ORV	0170	00	072547	164
212	XDERR	0212	00	077171		165	D4RES	0174	00	073211	166
215	BMXCON	3240	00	100553		167	D4SKR	1424	00	073211	168
220	BMXOPE	0510	00	103040		169	DD49	3420	00	074035	170
223	BCOM	1730	00	104026		171	FIRECODE	0205	00	076354	172
226	CONMAN	1120	00	105333		173	REPORT	0210	00	077674	174
231	TAPEIO	2000	00	106711		175	XDKFMT	1140	00	076132	176
234	TAFUN	0230	00	107534		177	BMXCPU	2674	00	101423	178
237	TDEM0	0504	00	110505		179	BMXS10	1550	00	103162	180
242	TEX	0223	00	112009		181	BUFMAN	2140	00	104414	182
245	TRCLN	0226	00	113033		183	DSCGET	1120	00	105557	184
251	TRREQ	0231	00	113530		185	TAPEND	1520	00	107176	186
253	TRORN	0234	00	113746		187	TAPMOV	1174	00	107635	188
255	TRREDO	0754	00	114433		189	TDEM1	2460	00	111171	190
261	RSTRT	0226	00	115356		191	TE	1224	00	112514	192
264	SDMP1	1614	00	117167		193	TRBOC	0450	00	113051	194

No.	Ov1	Name	Size	BM-Upr	BM-Lwr	No.	Ov1	Name	Size	BM-Upr	BM-Lwr
00	SDMPC	0350	01	021731		02	SDMPB	0240	01	022023	01
03	SDMPD	0240	04	022073		04	SDMPE	0154	01	022072	01
06	AMAP	0410	00	020670		05	AMSG	1740	00	020772	05
11	BLOCK	1534	00	021756		12	BMOL	4610	00	033652	00
14	BTD	0254	00	022305		15	BTH	0374	00	022360	16
17	BXSET	0340	00	022524		20	CALL	2010	00	022614	21
22	CHNST	0774	00	023747		23	CDEM	2010	00	024126	24
25	CONFIG	1334	00	024573		26	CLOCK	1110	00	024350	27
30	CPTEST	2370	00	025142		31	CPSPIN	0350	00	025062	32
33	DISKIO	1024	00	026560		34	CRTDEM	2150	00	026126	35
36	DKIOEX	0430	00	030012		37	DKMP	3460	00	027076	38
41	DKSET0	0260	00	030220		42	DKSET	0300	00	030140	43
44	DOMP	2554	00	030663		45	DOM	1614	00	041320	46
47	ERR	1604	00	031656		48	ECHOCP	0740	00	042744	49
52	F80ME	2104	00	035004		53	F80M	4440	00	044707	54
55	GETEXT	0164	00	037370		56	FIND	0274	00	044731	57
60	HSPTES	0000	00	040615		61	HPDATA	3644	00	045764	62
63	INDD39	2120	00	041663		64	INFORM	0200	00	046123	65
66	LISTP	2700	00	043114		67	INTO	041340	00	046150	68
71	MFINIT	2330	00	043674		72	IOVLNUM	0101	00	046150	73
74	MULTIPLY	64	00	045604		75	IPRN	0104	00	046150	76
77	PATCH	0150	00	051420		78	PRTAPE	0107	00	046150	79
81	SCRUB	0105	00	052212		82	START	0111	00	046150	83
85	START0	0534	00	053014		86	START2	0112	00	046150	87
91	STOP	0250	00	054022		92	STATS	0115	00	046150	93
93	TCOM	0230	00	055262		94	SYSTEXT	0120	00	046150	95
97	TRACE	0124	00	056120		98	TIME	1224	00	046150	99
101	TRACK	0125	00	056535		102	TSTASH	0126	00	046150	103
104	TRACK	0144	00	062466		105	USURP	0131	00	046150	106
107	X TAPE	0750	00	063357		108	X TAPEC	0134	00	046150	109
113	XTAPEC	0610	00	064176		114	XTAPED	0136	00	046150	115
115	XDISKA	0152	00	065131		116	XDISKT	0137	00	046150	117
119	XMT	1220	00	066035		120	XOPN	1130	00	046150	121
124	XPRINT	0144	00	067067		125	XPRNTA	0760	00	046150	126
127	XTAPEA	0750	00	068357		128	XTAPEB	0610	00	046150	129
133	XTAPEB	0152	00	069153		134	XTAPEC	0155	00	046150	135
136	XTAPEC	0155	00	070453		137	XTAPED	0730	00	046150	138
141	D310R	2454	00	061500		142	D3LOG	0161	00	066520	143
144	D3RLR	0700	00	067067		145	D3SKR	0164	00	066520	146
147	D4DEM	3370	00	067745		148	D4ECG	0166	00	067226	149
151	D4IOR	0171	00	071434		152	D4ORV	0170	00	072547	153
170	D4ORV	0170	00	072424		171	D4RES	0175	00	073211	172
173	D4SKR	0177	00	073036		174	D4SLR	1424	00	073516	175
176	D4STAT	0234	00	073766		177	DISK	2664	00	073744	178
179	EMRECK	3170	00	075516		180	DIVPASS	0206	00	070643	181
204	MEMIO	20									

0267	SDMP3A	0550	00	120170	0270	SDMP4	0350	00	120322
0272	SDMP6	0234	00	120455	0273	SDMP7	0250	00	120525
0275	SDMP9	0454	00	120741	0276	SDMP10	0144	00	0274
0300	CLEAR	0154	00	121231	0301	COPY1	1040	00	SDMP8
0303	COPY1	0670	00	121713	0304	COPY2	0740	00	CLEAR
0306	COPY4	0474	00	122423	0307	COPY5	0550	00	0520
0311	DDUMP&	0374	00	123036	0312	DEF	0650	00	0527
0314	DELETE	0650	00	123412	0315	DLOAD	0560	00	0527
0317	DSTAT	0514	00	124146	0316	DLOAD&	0316	00	0610
0322	EDINST	0544	00	124433	0317	DSTAT&	0504	00	120557
0325	EDIT1	1174	00	125251	0320	EDIT&	0644	00	00
0330	EDTYPE	0234	00	125566	0323	EDPRNT	0326	00	121105
0333	FLAW	0630	00	126343	0324	EDRPL	0534	00	121474
0336	FSTAT	0650	00	127655	0325	FDUMP&	0770	00	00
0341	INIT	0774	00	127735	0326	FLOAD	0714	00	122262
0344	PROC	0564	00	130403	0327	FLOAD&	0714	00	00
0347	RENAM&	0430	00	131033	0328	FSTAT&	0504	00	122674
0352	XFMCRE	0710	00	131262	0329	LINGET	0324	00	00
0355	XFMDSR	0634	00	131575	0330	INPUT	0554	00	123307
0360	XFMFLW	0244	00	132062	0331	RENAME	0600	00	00
0363	XFMIO	1020	00	132355	0332	RDUMP&	0770	00	123370
0366	XFMSTR	0200	00	133077	0333	FDUMP&	0714	00	00
0371	CONCER	1134	00	133436	0334	FLOAD&	0714	00	123720
0374	ENDCONC	0734	00	134761	0335	FLOAD&	0550	00	124412
0377	ACQTRM	0377	00	135147	0336	INIT	0321	00	124735
0402	BABEL	1634	00	135424	0340	LINE	0664	00	00
0405	BMGET	0200	00	136200	0341	REDELE	0104	00	00
0410	CFREAD	0560	00	136634	0342	EDDELE	0104	00	00
0413	CLUSTER	1000	00	137657	0343	EDPRNT	0104	00	00
0416	COMBO	0134	00	140527	0344	EDRPL	1460	00	00
0421	COMM03	0710	00	142055	0400	EDTYPE	1460	00	00
0424	COMM06	0604	00	142720	0401	EDTYPE	1460	00	00
0427	COMM09	1600	00	143500	0430	EDTYPE	1460	00	00
0432	COMM12	0760	00	144567	0441	EDTYPE	1460	00	00
0435	COMM15	1104	00	145642	0442	EDTYPE	1460	00	00
0440	CPUGET	0440	00	146523	0443	EDTYPE	1460	00	00
0443	DECOD2	0660	00	147200	0444	EDTYPE	1460	00	00
0446	DISP02	0204	00	150115	0447	DEVDAT	0560	00	00
0449	DISP02	1220	00	151275	0452	DISPLAY	1300	00	00
0454	DKSTAT	2560	00	152125	0455	ERRDIS	1424	00	00
0457	F80	5754	00	153235	0456	F132	5754	00	00
0462	GRAPH	1230	00	156331	0463	GRCDIS	1274	00	00
0465	GREDIS	0200	00	157462	0466	HELP	0450	00	00
0470	HELP02	0200	00	160511	0471	HSPGET	0443	00	00
0473	IDEBUG	1220	00	165030	0474	IDLGET	0450	00	00
0476	IFRMT	0530	00	165762	0477	KEYBD	0514	00	00
0501	LINK	1254	00	166464	0502	LOGON	1400	00	00
0504	MSTAT	1540	00	167312	0505	MSTDIS	1500	00	00
0507	NSCNET	1550	00	170247	0510	NCSCFP	2220	00	00
0512	ONLINE	0440	00	171443	0513	POST	0514	00	00
0515	PROTOCOL	1510	00	172151	0516	QUEUE	0340	00	00
0520	SNAP	0474	00	173066	0521	SOROC	0440	00	00
0523	STADIS	0523	00	175333	0524	STAGEIN	1414	00	00
0526	STATCL	1374	00	175007	0527	STATINT	0525	00	00
0531	STIO	0350	00	175453	0532	STMSG	2044	00	00
0534	STREAMS	1114	00	176317	0535	STRSTAT	1460	00	00
0537	STTAPI	0654	00	177150	0540	STTAPI	0570	00	00
0542	STXDKI	0104	00	180005	0543	STXDKO	0720	00	00
0551	SUMHOW	0664	01	001242	0551	SUMTIM	0674	01	001576
0553	SYSTAT	1754	01	002351	0551	TAPEC	1650	01	003316
0555	TJOB	1674	01	003376	0554	TKSTAT	2110	01	004377
0556	UPDATE	0430	01	004572	0557	XFRMT	1500	01	005224
0561	XMPXP	0564	01	005726	0562	XMP4XP	2754	01	007175
0564	IACON	0760	01	007567	0565	IACON1	1444	01	010000

The EXEC table area contains all EXEC tables, alphabetically ordered. Most table layouts are described in the COS Table Descriptions Internal Reference Manual, publication SM-0045. The other tables are internally documented. The tables are:

CAT Channel Address Table

CBT Channel Buffer Table containing one entry of working storage for each disk driver channel.

CHT Channel Table containing a 1-word entry for each side (input and output) of a physical channel. An entry contains a pointer to the Channel Processor Table for the channel-assigned task ID and the address of the channel processor assigned to the side of the channel. Input sides are assigned even numbers; output sides odd numbers.

CLT Channel Limit Table

CXT Channel Extension Table

ICT Interrupt Count Table

IHT Interrupt Handler Table

MCT Monitor Count Table

MEL Memory Error Log Table

MRT Monitor Request Table

PCT Packet Counter Table

PIQ Packet Input Queue

POQ Packet Output Queue

PRT Packet Routing Table

PWS Processor working storage

RMS Read Margin Select Table

SCT Subsystem Control Table

STT System Task Table consisting of three parts: a header, a task parameter word area, and an Exchange Package area

STX System Task Exchange Package Table

TBT Task Breakpoint Table

TEC Time Event Table

XFT History Function Table

XTT History Trace Table

This area contains tables accessible to all STP tasks (not necessarily in the order noted).

- AUT Active User Table containing an entry for each logged on interactive user
- CMOD Communications modules in 6-word groups that form a pool from which they are allocated as needed. Two words are used as control; two are used as input registers; and two are used as output registers. A task receives all of its requests and makes all of its replies through a CMOD.
- CNT Configuration Table containing information on the availability and type of each device known to the system. The CNT is used only for tape devices.
- CPT Class Parameter Table used by JCM. It contains all job statement parameters used to determine the job class.
- CSD Class Structure Definition Table containing the job class structure. For each class defined in the structure, there is a class map; these appear in CSD in descending order. A header precedes the class maps. Variable length characteristic expressions for each class follow the maps.
- DAT Dataset Allocation Table. A DAT exists for each dataset being used by the system and defines where the dataset logically resides on mass storage, that is, on which logical devices and what portion of a device.
- DCT Device Channel Table serving as a link between a physical or logical disk channel and the EQT. It is an interface to the EXEC disk driver. The DCT holds channel system performance data.
- DRT Device Reservation Table. A DRT exists for each logical disk device known to the system. A DRT contains a bit map showing available and reserved tracks on the device.

- DXI Permanent Dataset Catalog Extension Information Table containing information used by the Permanent Dataset Manager (PDM) such as the size of the Dataset Catalog Extension Table (DXT)
- ECT Error Code Table for controlling abort and reprise processing done by EXP. It contains a 1-word entry for each system error code and is defined using the ERDEF macro.
- EPQ EXEC Packet Queue containing APT entries destined for EXEC. Defined by COMQP (see COMQP in the COS Table Descriptions Internal Reference Manual, publication SM-0045).
- EQT Equipment Table containing an entry for each disk device known to the system
- GRT Generic Resource Table containing an entry for each generic resource in the system.
- IBT Interactive Buffer Table for managing the Interactive Buffer Pool
- ILT ISP Link Table. The ILT contains an entry for each input or output channel using ISP protocol. The ILT is used by STP task IQM and its associated common routine ISPCOM.
- IPT Interjob Communication Path Table
- IST ISPMAIN Status Table. The IST contains an entry for each ISP system that COS allows to log on. It is used by STP tasks IQM and EXP.
- JSQBM Job Sequence Number Bit Map. The JSQBM indicates which job sequence numbers are being used.
- JXT Job Execution Table. The JXT controls all active jobs in the system and can contain as many as 256 entries. Entry 0 (the first entry) is used to represent the system itself.
- LCT Link Configuration Table containing an entry for each CPU channel used for front-end communications
- LIT Link Interface Table. SCP assigns an LIT entry at startup to each CPU channel used for front-end communications. This table is used primarily for channel control.
- LXT Link Interface Extension Table. EXEC assigns an LXT entry for a front-end station at log-on time and releases the entry at log off. This table is used primarily for EXEC-STP communication of information on a front-end station.

- MST Memory Segment Table containing an entry for each segment of memory allocated by the Job Scheduler (JSH) as well as an entry for each free segment. The number of entries in the MST is set to twice the number of JXT entries plus four words. Each MST entry is one word in length.
- PDI Permanent Dataset Information Table containing information used by the Permanent Dataset Manager (PDM), such as the number of overflow and hash pages.
- PDS Permanent Dataset Table consisting of a 1-word header followed by a 1-word entry for each active permanent dataset. The entry indicates how a dataset is accessed and if multiple access exists. If so, the entry tells how many users are accessing the dataset.
- PHR Physical Request Table. DQM uses the PHR to pass data transfer requests to the SSD driver.
- PXT Processor Execution Table contains status information for each physical processor, including which user task is currently connected.
- QDT Queued Dataset Table describing the multitype attributes for a disposed dataset. The table is managed by the Permanent Dataset Manager (PDM) and Exchange Processor (EXP) tasks. The number of entries in the QDT must equal the SDT entry count.
- RIT Registered ID Table for Interjob Communication.
- RJI Rolled Job Index Table containing for each defined JXT, an entry describing the job assigned to the JXT entry, allowing the recovery of jobs from mass storage.
- RQT Request Table used to queue transfer requests for disk management. DQM uses the RQT to manage both logical and physical disk requests. RQT entries are queued to an EQT entry.
- SAC SSD Active Channel Table. The SSD driver uses the SAC to control SSD channel activity.
- SBU System Billing Unit Table containing the values obtained when system billing units are calculated for system resources.
- SDR System Directory containing a Dataset Name Table for each of the datasets comprising the system library. The SDR is initialized after a system startup.
- SDT System Dataset Table containing an entry for each dataset spooled to or from a front-end system. An SDT entry can have appendages allocated out of an STP memory pool to contain TEXT field and station slot information.

- SQP SSD Queued Packet Table. Used to submit SSD data transfer requests in the form of A-packets to the SSD driver. The queue is circular, with the driver removing requests at OUT, and DQM adding requests at IN.
- SST Stager Stream Table. Eight input stream and eight output stream SSTs are contained within each LXT.
- STPD STP Dump Directory containing pointers to task origins, buffers, and so on. An entry gives a mnemonic in ASCII plus the relative STP address for the area.
- SXT System Execution Table containing pointers to execution profile information for system tasks.
- TDT Tape Device Table. The Tape Queue Manager task uses the Tape Device Table to control online tape devices. The TDT contains an entry for each tape device in the system.
- TPQ Task Packet Queue containing pointers to each task's QPT entry, which contains APT entries destined for the STP tasks.
- TXT Task Execution Table contains all information to control all user tasks within the system.
- UCT User Call Table containing a count of the number of times each type of user call is made. This table is used by the System Performance Monitor (SPM).
- UDT User Driver Channel Table. The UDT contains an entry for each input or output channel eligible for use by user jobs, subsystem jobs, or the STP task IQM.
- VPT ISP Virtual Circuit Pointer Table. The VPT contains one entry for each available ISP connection. Each ISP dataset uses one entry.

Details of the STP tables are given in the COS Table Descriptions Internal Reference Manual, publication SM-0045.

Tables that may reside in the user field include the following:

- BAT Binary Audit Table. This table contains an entry for each permanent dataset that meets requirements specified on the AUDIT control statement.
- DDL Dataset Definition List. A DDL in the user field accompanies each request to create a DNT.
- DSP Dataset Parameter Area. A DSP in the user field contains the status of a particular dataset and the location of the I/O buffer for the dataset.
- JAC Job Accounting Table. This table defines an area for data to be returned to the user by an accounting request.
- JCB Job Communication Block, residing at the very beginning of the user field and containing information used by both COS and library routines. Copies of the more important pointers are kept in the job's JTA to assist in JCB validation and re-creation.
- LFT Logical File Table. This table in the user field contains an entry for each dataset name and alias referenced by FORTRAN users. Each entry points to the DSP for a dataset.
- ODN Open Dataset Name Table. A request to open a dataset for a job contains a pointer to the ODN table in the user field.
- PDD Permanent Dataset Definition Table. A PDD in CSP is used for many permanent dataset requests.

See the COS Table Descriptions Internal Reference Manual, publication SM-0045, for detailed descriptions of these tables. This table is available as a listable tape.

STP COMMON ROUTINES

<u>MODULE</u>	<u>ENTRY POINTS</u>	<u>PURPOSE</u>
ERROR	ERROR0, ERROR1	hang the system
REQRPLY	TSKREQ, PUTREQ, GETREQ, PUTREPLY, REPLIES, GETREPLY	task-to-task communications
STPMEM	MEMAL, MEMDE, PMEMDE, SSLDE	memory pool management
CHAINS	CHAIN, CHAINF, UNCHAIN, JCHAIN, JCHAINF, JUNCHAIN	chain management
STPTIME	RQST2, RT2JD, JD2RT	date/time calculations
QUEUES	DQSD2, EQSD2	SDT queue management
QMSG	NXTMSG, FREEMSG, ENQMSG	interactive station message management
MSGQUE	MSGQUE	SCP/operator message processing

STP COMMON ROUTINES

<u>MODULE</u>	<u>ENTRY POINTS</u>	<u>PURPOSE</u>
STPUTIL	BTO, \$OTB, \$DTB, SFN, \$NOCV	utility routines
STPDATS	GETDAT, RELDAT	DAT management
JMEM	JMEMAL, JMEMDE	JTA memory pool management
JTADNT	GETDNT, GETLFT, RELDNT	JTA DNT management
FIXJXPR	FIXJXO, FIXPRI	job pri. calculations
CRACKER	IND	JOB control stmt. cracker
GETPARM	GETPARM	parameter cracker
CONFIG	CONFIG	configuration changes

Kernel Common Routines

<u>NAME</u>	<u>DESCRIPTION</u>
ERGC	BUILDS A NEW ACTIVITY DESCRIPTOR AND SMOD. PLACES AD ON CP QUEUE.
EREB	FINDS SPACE FOR AD AND CLEARS IT. PLACES AD ON CHAIN OF AD.
EREC	GETS MOS FOR SMOD AND SETS UP PARAMETERS IN AD
ERED	INITIALIZES SMOD IN SCRATCH AREA
STOREGS	STORES SPECIFIED OPERAND REGISTERS IN SMOD
LODREGS	LOADS SPECIFIED OPERAND REGISTERS FROM SMOD
EDQU	REMOVES ACTIVITY FROM SPECIFIED QUEUE
ENQU	PUTS ACTIVITY ON SPECIFIED QUEUE
EQCP.	PUTS ACTIVITY ON CP QUEUE AT PRIORITY
EQUE	PUTS ACTIVITY ON SPECIFIED QUEUE AT PRIORITY
EPOQ	PUTS ENTRY ON MESSAGE QUEUE
ETOQ	REMOVE ENTRY FROM MESSAGE QUEUE
QTIME	PUT AN ACTIVITY ON TIMER QUEUE. ACTIVITY MUST ALSO BE ON ANOTHER QUEUE.
DQTIME	REMOVE AN ACTIVITY FROM TIMER QUEUE
DQFIND	LOCATE AND REMOVE AN ENTRY FROM A QUEUE
EMSGIOP	SEND MESSAGE TO ANOTHER IOP VIA ACCUMULATOR CHANNEL

Kerberos Command Reference

\$SYSTEXT MACROS

PDUMP		
SETSTKAT	DEACTIVATION	NAME
FORCE NEWENTRY	BUILDS A NEW ACTIVITY DESCRIBED AS SPECIFIED AD CN OF SOURCE.	EREC
INSMAC		
FTIO OA ZECDR ITI 2EACBZ RCB 2EACBZ	EMBEDS SOURCE FOR OA AND CHAIN TO OA	8383
BASERR		
CRUTYPE	DA IN 2ECDMABAR RU 2ECD UMA 2ECD RCB 2ECD 2ECD	8383
SVREGS		
LDREGS	INITIALIZES SOME IN SCROLLBACK AREA	8383
OPENA		
@VALREG	2ECD	2ECD
@GENREW	2ECD	2ECD
@GENTP		
\$RCW -	REMOVES ACTIVITY FROM 2ECD	8003
\$TSTRG		
\$BIO	PITS ACTIVITY ON SCROLLBACK QUEUE	8003
PRM1SYM	PITS ACTIVITY ON SCROLLBACK QUEUE	8003
PRM2SYM	PITS ACTIVITY ON SCROLLBACK QUEUE	8003
RPM2MIGRATION	PITS ACTIVITY ON SCROLLBACK QUEUE	8003
RECUR	PITS ENTRY ON MESSAGE QUEUE	8043
SNAPSHOT		
%REGADDR	REMOVES ENTRY FROM MESSAGE QUEUE	8073
%QDOUBLE		
%FORTIO	ALSO BE ON AUTOMER QUEUE	8M10
%ARGADDR		
FEDLP	REMOVES ENTRY FROM LOG	8M10
(SYERP ERROR CODES)	NAME OF ENTRY TO BE USED	8040
DEFLOCK	SEND REQUEST TO AUTHORITY FOR A LOCKON	80102003
LOCKON		
LOCKOFF		

~~2050AM~~ 3 COSTXT MACROS

DRT	LOADCL	CLEAR16
EQT	SAVECL	COPIXP
\$GTTOKEN	MSGQ	012210
\$DOICH	SMDEF	W3T3B
CONFIG	WAIT\$SET	GE28R0
%GETNUM		12W31
FLAW		JOT32
STRTFLW		9E12B
ENDFLW		STOP
SETIF		DATAHBR
POST		
VTRAGE		
TSKREQ		

~~2050AM~~ 4 COSTXT MACROS

	DATA Access Macros	DATA Structure Macros
RJ		
LOCK -	T30	T303
UNLOCK	LOAD	ERUT
ERRU	PUT	EWMS
ERRAZ	320T2	ERKCR
ERRAN	RE0T	EXRER
ERRAP	TUPA	EXPER
ERRAM	RE20T0	EXCEPTION Control Macros
ERRSZ	FLC0D0	312
ERRSN	FLD0S8	SHRTLP
ERRSP	Qwertyx and Read/Save Definitions	29070
ERRSM	QACRDAY	SPURATE
CAPTION	REGBDEZ	DATA Definition Macros
ENDFIELD	REGDISER	220DRB
%GETSYM		01312
FIELD@		0J1E10
\$LINE		EL8AT
GN		
GD		
\$SRÉG		
\$LRÉG		

EXEC SPECIFIC MACROS

CLEARIP
COPYXP
X\$SIO
GETPW
GETSRO
I\$FWB
SETCL
SETIP
STOP
FALLTHRU

\$APTEXT MACROS

Exit Stack Macros	Data Access Macros
EGET	GET
EPUT	LOAD
EINCR	PUT
EDECR	STORE
EXSGET	RGET
EXSPUT	RPUT
Execution Control Macros	RSTORE
\$IF	FLDADD
\$UNTIL	FLDSUB
\$GOTO	Overlay and Register Definition Macros
\$PUNTIF	OVERLAY
Data Definition Macros	REGDEFS
ADDRESS	REGISTER
FIELD	
ISFIELD	
TABLE	

QUIZ 3

ANSWERS

QUIZ 3
ANSWERS

OVERVIEW

Match the following terms and their descriptions.

- | | |
|---------------------------|--|
| 1. STATION | _7_ An interruptable COS program performing system functions |
| 2. EXEC | _5_ 1 XMP CPU in monitor mode and executing EXEC at a time |
| 3. KERNEL | _4_ Log of operating system and user activities |
| 4. \$SYSTEMLOG | _1_ Software that runs on the front end linking COS and the front end operating system |
| 5. SINGLE THREADED | _8_ Privileged work an overlay asks the IOS monitor to do |
| 6. MULTITASKING | _9_ Privileged work an STP Task asks the monitor to perform |
| 7. TASK | _10_ IOS program running under Kernel control |
| 8. KERNEL SERVICE REQUEST | _6_ Several CPU's working on the same job |
| 9. EXECUTIVE REQUEST | _3_ Monitor for each IOP |
| 10. OVERLAY | _2_ Monitor for the Mainframe COS |

SYSTEM PROGRAMS

Match the following terms and their description

- | | |
|---------------------------|---|
| 1. STATION CALL PROCESSOR | _6_ Inter IOP Message Handler for DD-19 and DD-29 disk activities |
| 2. \$APTEXT | _9_ Definitions for COS tables and constants |
| 3. CONCIO | _5_ Consists of one overlay |
| 4. IOP STATION | _10_ Work for IOP to perform |
| 5. DEMON ACTIVITY | _8_ Processes all User exchanges using S0 as the function code |
| 6. ACOM | _2_ Contains definitions of macros, constants, and tables for IOS |
| 7. COSPL / IOPPL | _4_ Considered a front end to COS |
| 8. EXP | _1_ Communicates between the FE station and COS |
| 9. COSTXT | _7_ Source listings for the operating system |
| 10. ACTIVITY | _3_ Processes FEI channel activities in the IOP |

PROGRAM SWITCHING

Match the following terms and their descriptions

- | | |
|--|--|
| 1. EXCHANGE PACKAGE | _4_ Defines an Activity in IOS |
| 2. DISK ACTIVITY LINK | _6_ 6 words of information passed between COS and IOS |
| 3. SYSTEM TASK TABLE | _10_ Activates ACOM, BCOM, ICOM giving Buffer Memory address of DAL being passed |
| 4. ACTIVITY DESCRIPTOR | _7_ All SMOD's of an activity |
| 5. STORAGE MODULE | _8_ Central Memory address for all COS Task exchange packages |
| 6. ANY PACKET | _3_ Controls COS STP Tasks |
| 7. SOFTWARE STACK | _9_ Central Memory location for user exchange package |
| 8. STX | _2_ Information passed through buffer memory to another IOP |
| 9. PROCESSOR WORKING STORAGE | _5_ An Overlay program's executing enviroment |
| 10. INTER A-A MESSAGE
Interrupt Handler | _1_ A COS mainframe programs executing enviroment |

SYSTEM RESOURCES

Match these terms and their description:

- | | |
|---------------------|---|
| 1. MASTER DEVICE | _8_ Considered part of the IOP station |
| 2. MIOP | _6_ Logically considered a fast disk storage device |
| 3. BIOP | _2_ Main responsibility is handling the NSC hyperchannel and Front End channels |
| 4. DIOP | _5_ Main Responsibility is to drive IBM type Block mux devices and tape units |
| 5. XIOP | _1_ Contains the Dataset Catalogue |
| 6. SSD | _3_ Contains a High Speed channel for use by MIOP for front end activities STATIO request |
| 7. BUFFER MEMORY | _4_ Main responsibilty is to drive Disk Storage devices |
| 8. EXPANDER CHASSIS | _10_ Memory used by each IOP for monitor and executing IOS activities |
| 9. CENTRAL MEMORY | _7_ Working storage for IOP activities |
| 10. LOCAL MEMORY | _9_ Memory where COS resides |

STATION PROTOCOL

Match the following terms and their definitions:

- | | |
|----------------------------|--|
| 1. LINK CONTROL PACKAGE | <u>7</u> Does the word size, logic level and hardware protocol conversion between the Cray and front end |
| 2. MESSAGE CODE | <u>6</u> Provides longer distance links between the Cray and a front end |
| 3. DATASET HEADER | <u>10</u> Links the Station communication to the COS tasks |
| 4. STREAM CONTROL BYTE | <u>9</u> Field in an LCP containing error messages |
| 5. STATION | <u>1</u> 6 word message header sent between the station and SCP |
| 6. HYPERCHANNEL | <u>3</u> Segment zero of a dataset transfer that precedes the dataset |
| 7. FRONT END INTERFACE | <u>8</u> Data transferred across the station Cray link with the LCP |
| 8. SEGMENT | <u>2</u> Defines what the segment attached to the LCP contains |
| 9. MESSAGE SUB CODE | <u>4</u> Defines the status of a dataset transfer from the request to the transfer completion |
| 10. STATION CALL PROCESSOR | <u>5</u> Software that runs on the front end under the control of the front end's operating system |

INTERTASK COMMUNICATION

Match the following terms and their description:

1. IMT 7 Used to make an executive request

2. PUTREQ 2 Routine to initiate a synchronous request from another COS Task

3. TASKREQ 5 Used by the user to make system action requests of EXP

4. ITCT 1 Request/Reply packet between STP tasks

5. S0 & S1 6 Registers used for task to task requests/replies

6. S1 & S2 4 Trace table for all task to task requests/replies

7. S6 & S7 3 Asynchronous Request to another Task

INTER-IOP COMMUNICATION

- | | |
|--------------------------|---|
| 1. AMSG | _4_ Activates a suspended activity
in another IOP |
| 2. POPCELL | _7_ Processes Interprocessor DAL's
that are requesting TAPE |
| 3. DISK ACTIVITY LINK | _5_ Kernel service request to
activate another overlay |
| 4. AWAKE | _3_ 40 octal parcel message passed
between IOP's through Buffer
memory |
| 5. CALL | _6_ Processes DAL requests from
MIOP for DD-49 activities |
| 6. ICOM | _1_ Processes the ALERT service
request creating a POPCELL |
| 7. BCOM | _8_ Processes DD-29 DAL's from
MIOP, BIOP or DIOP |
| 8. ACOM | _9_ Points to the Interprocessor
DAL in Buffer Memory |
| 9. INTER-IOP A-A MESSAGE | _10_ Kernel Service request used by
MIOP to initiate a Buffer
Memory to/from Central
Memory for the Concentrator |
| 10. STATIO | _2_ QUEUE and Activity descriptor
information for the
ALERT, AWAKE, ASLEEP,
RESPOND mechanism |

COS DUMP ANALYSIS

- | | |
|------------------------------|---|
| 1. STOP BUFFER | <u>8</u> System utility to format a dump created by SYSDUMP |
| 2. \$STOP MACRO | <u>5</u> Allows analyst to turn on and off any type of history trace entry |
| 3. ERRSN MACRO | <u>7</u> History trace for inter-task communication |
| 4. EXCHANGE PACKAGES 100-200 | <u>10</u> Location in STP TABLES where the STP hang message is put on an STP hang condition |
| 5. XFT | <u>9</u> Creates a copy of the Cray system memory on the master device scratch buffer for dumps |
| 6. XTT | <u>3</u> Macro executed by a task when a hang condition exists |
| 7. ITCT | <u>1</u> Location to look first in a dump to determine who halted first |
| 8. FDUMP | <u>2</u> Macro executed by EXEC to hang the operating system |
| 9. SYSDUMP | <u>6</u> Table containing History trace events of the system |
| 10. SY006 | <u>4</u> Executing CPU Exchange packages on a hang |

IOS DUMP ANALYSIS

Match the Following terms and their description:

- | | |
|-----------------------------|--|
| 1. HALT MESSAGE | _3_ Operand register containing the address the executing overlay is loaded at |
| 2. \$PUNTIF | _5_ Points to the Hang macro address to help locate it in the listing |
| 3. %B | _6_ Operand register containing the overlay number that was executing |
| 4. EXIT STACK | _8_ Area in the Kernel tables where the IOP's A, B, C, E, Exit stack and Busy Done flags are saved on a \$PUNTIF |
| 5. CONTENTS OF E-1 IN STACK | _10_ Operand register containing the address the active SMOD is at |
| 6. %OVLNUM | _4_ Return jump history stack after an interrupt or an exit |
| 7. %ACTIVE | _2_ Macro executed by the IOS to halt on an error condition |
| 8. EXSAVE | _7_ Operand register containing the address of the active activity descriptor |
| 9. BUSY and DONE flag set | _1_ Tells you which IOP halted and the punt code attached to the \$PUNTIF macro |
| 10. %SMOD | _9_ High Speed Channel Error |

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ANTWERP 2

...Observe the following examples:

337

On a TURMITE
send binary Data (lads) via seven
bits 10's A,B,C,E Extra space
and less in the Kettler 9912a where

EXTRA STATEMENT

[View Details](#)

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Skills in Information Systems

MUJUVOS

of 601 and 602 between 1930 and 1932.
no libraries noted as no files

EVITDAK 3

objectives of the civil service

WACKED

SPRINGFIELD **WAGGON**
The **brown** **cab** **spokes** **of** **the**
black **iron** **wheels** **are** **the**
best **in** **the** **country**.

See page 340 for value.

10773 (smmed3) beagle Roth JF

COMER TO

EXERCISE 3

ANSWERS

EXERCISE 3
ANSWERS

Exercise 1 EXEC & KERNEL Monitor Flow

Skills: To have some familiarity with the COS/IOS monitors

Tasks:

COS

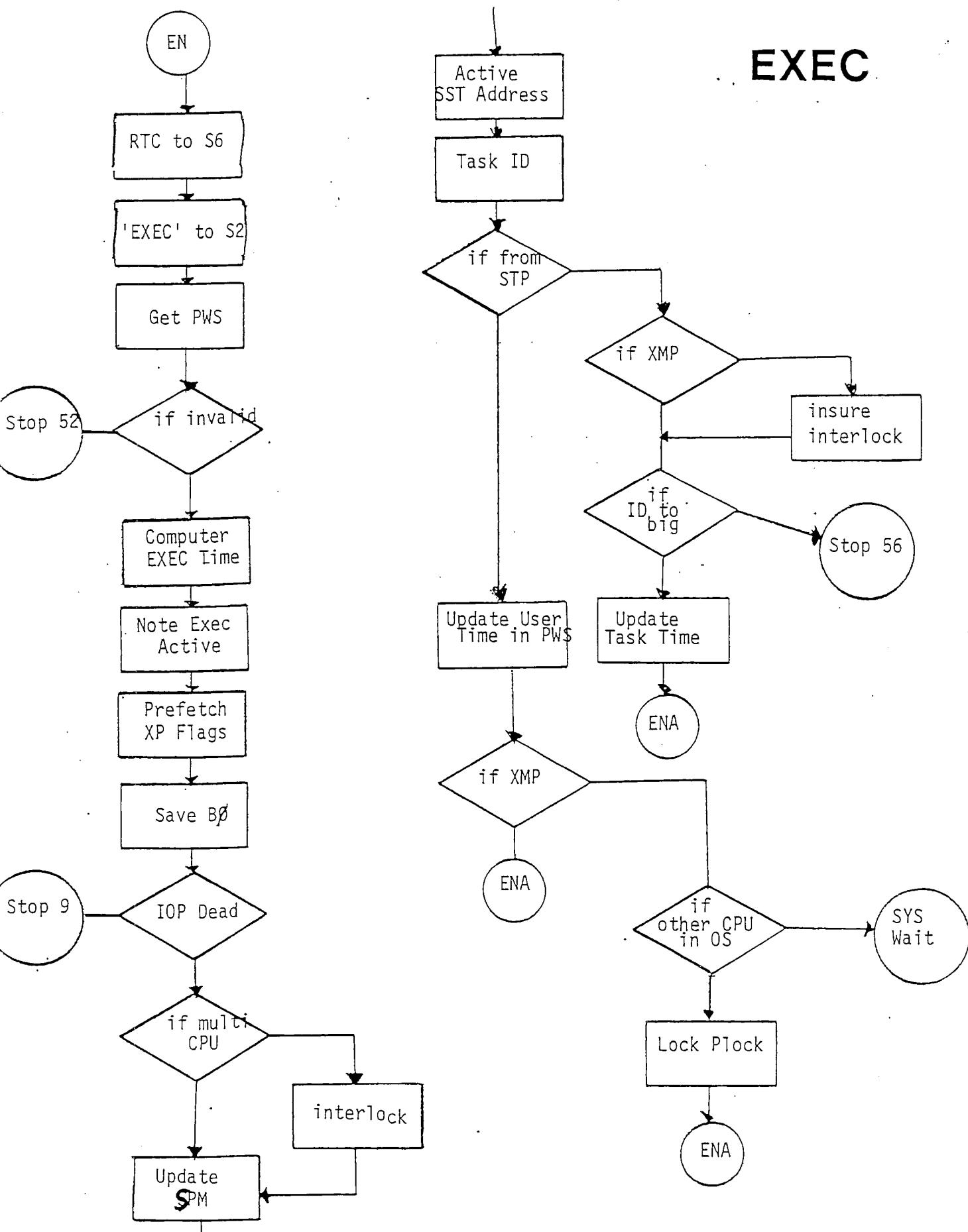
- a. Flowchart what occurs in EXEC on an interrupt starting at EN
(four pages of code only)

IOS

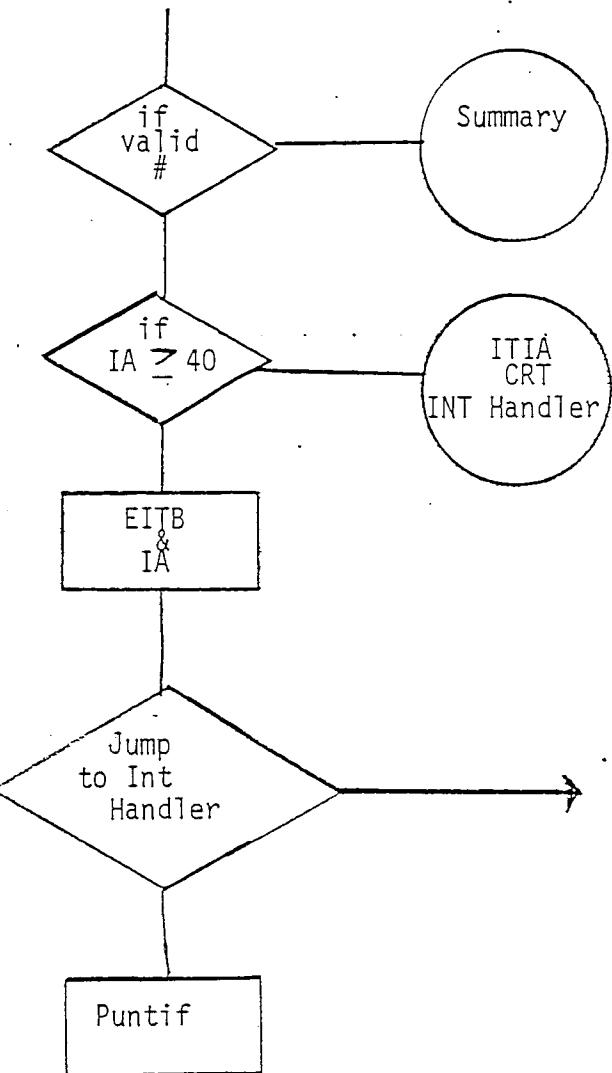
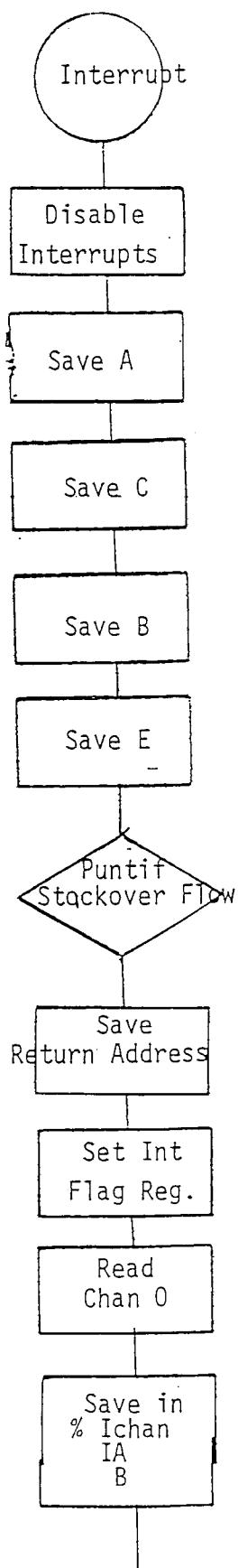
- a. Flowchart what occurs in the KERNEL starting at address
INTERRUPT (two pages of code only)

Intended Lesson Results: To be able to go into the monitors and know the flow through the monitor on an interrupt and how to find your way around the monitor listings

EXEC



KERNEL



Exercise 2 TASKS AND OVERLAYS

Skill: To have some familiarity with the system programs

COS

- a. List the Functions performed by PDM
 1. Save User Input, Output Dataset
 2. Access User, Spooled Dataset
 3. Delete User, Spooled Dataset
 4. DSC Page Request
 5. DXT Page Request
 6. Load User, Input, Output Dataset
 7. Update Active PDS
 8. Permanent Dataset Request
 9. Dump Request
 10. Dequeue System Dataset Table (SDT) entry
 11. Queue SDT entry to Available, Input, Output Queue
 12. Adjust, Modify User Dataset
 13. Rewrite Job Input Dataset SDT entry
 14. Pseudo access for RRJ Rolled Job Recovery in Z
 - 15 Access for PDSDUMP
 16. Permit Alternate User Dataset Access
- b. What table is the PDM function request code passed in
 1. Permanent Dataset Definition PDD

IOS

- a. List the Functions performed by ACOM
 1. Initiate Disk Request
 2. Release Disk Activity Link DAL in Originating IOP
 3. Transfer Data Central to Buffer Memory over HISP
 4. Transfer Data Buffer to Central Memory over HISP
 5. Send status to Mainframe task
 6. Central memory to Buffer memory done
 7. Buffer memory to Central memory done
- b. When is ACOM used and by who
 1. Used when an A-A message 110000 to 140000 is received on an A-A interrupt
 2. Used by the Disk Subsystem for interprocessor DAL's and BIOP BM to CM to BM transfers
 3. Integral part of Disk Subsystem

Intended Lesson Results: To be able to locate a system program listing and determine what the program does and who uses it when.

Exercise 3 Tasks and Activities

Tasks: COS Dump 2

- a. List all the Tasks in the STT in sequential order
- b. Give their priority
- c. Give their task ID number
- d. Give the task exchange package address

<u>TASK</u>	<u>PRIORITY</u>	<u>ID</u>	<u>XP ADDRESS</u>
1. STP	777	0	4740
2. SCP	1	1	4760
3. EXP	11	2	5000
4. PDM	14	3	5020
5. DEC	20	4	5040
6. DQM	2	5	5060
7. MSG	5	6	5100
8. MEP	10	7	5120
9. SPM	24	10	5140
10. JSH	13	11	5160
11. JCM	12	12	5200
12. TQM	3	13	5220
13. STG	6	14	5240
14. FVD	15	15	5260
15. IQM	4	16	5300

Tasks:

IOS Dump 2

- a. List all the Activities in the EACT+1 chain in sequential order
- b. Give each activities priority
- c. Give each activities storage module address

EACT+1 IS LM ADDRESS 5116 AND CONTAINS 73530

<u>AD ADDRESS</u>	<u>NAME</u>	<u>PRI</u>	<u>SMOD ADDRESS</u>
73530	IAOUT	10	64770
73404	KEYBD	10	
103164	IAIOP1	10	
102204	NSCNCO	10	
101670	NSCNCO	10	
101440	READ	6	
101414	KEYBD	3	
73274	TSTASH	7	
72440	PROTOCOL	5	
73554	CLI	6	
73320	NSCIO	1	
73360	CONCIO	10	
73204	CONCIO	10	
73024	SCRUB	17	
72764	ICOM	0	73010
72724	BCOM	0	72750
72664	CDEM	0	72710
72624	CRTDEM	7	72650
72564	CLOCK	7	72610
72524	AMSG	0	72550
72464	ACOM	0	72510

Intended Lesson Results: To be able to identify the tasks and activites in the system and at what address their executing environments are

Exercise 4 System Memory Maps

Skill: To be able to find the address that programs are executing at

COS Dmp 2

- a. At what address do the EXEC tables end at 15650
- b. At what address does STP begin at 32000
- c. At what STP relative addresses are the tasks loaded at

<u>TASK</u>	<u>STP RELATIVE ADDRESS</u>
STP	274446
SCP	274713
EXP	313316
PDM	454012
DEC	453741
DQM	446170
MSG	427424
MEP	467066
SPM	467373
JSH	431067
JCM	442536
TQM	354202
STG	467761
FVD	471611
IQM	472511

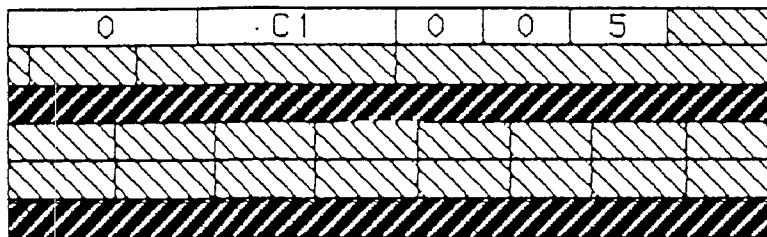
- d. At what address does STP end 535472

IOS Dmp 2

- a. At what address do the Kernel tables ends at 12360
- b. At what address does the Overlay memory chain begin, at 31300
- c. At what address does the DAL chain begin at (5162) is 67540
- d. At what address does free memory chain begin at (5157) is 73174
- e. At what address does IO buffer chain begin at (5165) is 154000

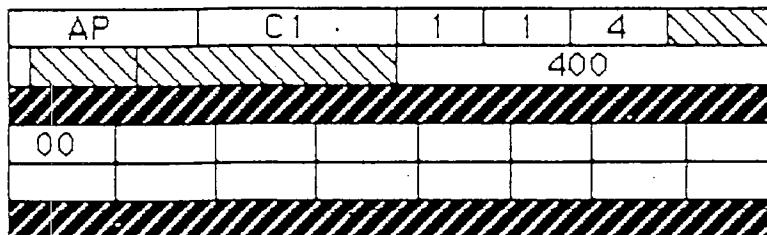
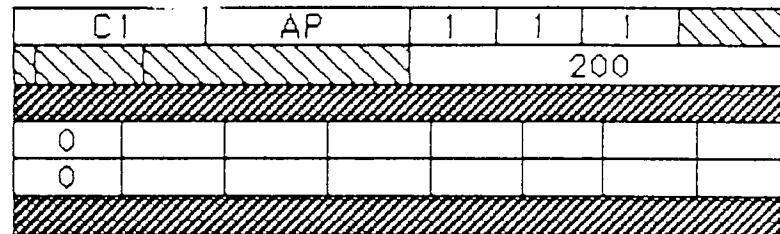
Intended Lesson Results: To be able to determine where to find addresses
pointers for relative programs and have some perspective on how Cray
system memories are utilized

LCP SEQUENCE



RESTART → FE

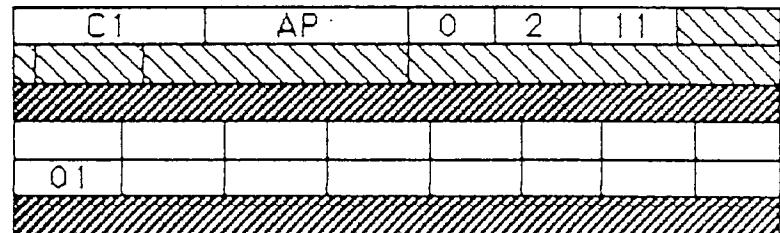
CRAY ← LOGON



START → FE

IDLE

CRAY ← CONTROL

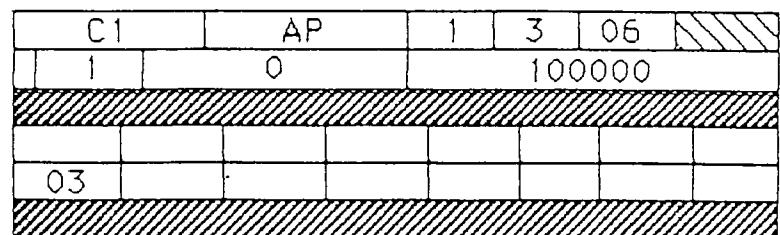


CONTROL → FE

RCV

CRAY ← DATASET HEADER

SND



AP	C1	0	3	11	
04					

CONTROL → FE

RCV

CRAY ← DATASET
SEGMENT
SND

C1	AP	1	4	07	
1	1			100000	
03					

AP	C1	0	4	11	
04					

CONTROL → FE

RCV

CRAY ← CONTROL
END

C1	AP	0	5	11	
06					

AP	C1	0	5	11	
10					

CONTROL → FE

SVD

CRAY ← CONTROL
IDLE

C1	AP	0	6	11	
00					

Exercise 6 COS Dump Analysis

Skill: To analyze a COS dump for Failure

COS DUMP 1

a. Locate the Stop buffer

address 1474 STOP08 STP hang

b. Locate the STP hang message if an STP hang

address 32310 task 2 Hang address 333007B
-323540B

c. Locate the STOP macro

Listing Relative 6247B

d. Locate the ERR macro if an STP hang

ERRAN macro in EXP at address 6247B

e. Locate the active Task

EXP

f. Locate the active task exchange package

address 4240

g. Analyze the EXEC history trace Events

CRASHF flag was set non-zero with SCP debugger by analyst
Then a job was submitted with a F\$CRASH System Action Request
This is useful for debugging

Intended Lesson Results: To be able to find out which program halted COS
and study the events that lead up to the halt and why it halted.

Cos Dump 1

DXTR TYPE=CPU	P-ADDRESS SAVED B00	XA	PN	SH00-SM13	INTERVAL	DATA
	00146106C 00000000B 1761	0	01010110000000 0000000000000000	0360741322645513251524	0475201322645513237076 <<----STOP----->>	
EXEC ASC	003240120 00324012C 5000	0	01010000000000 0000000000000000	00000000000000000000000000000000	04251024000000000000000000000000	
EXEC ERIC	000000200D 00000000A 4100	0	01010000000000 0000000000000000	00000000000000000000000000000000	04251024000000000000000000000000	
EXEC UNIC	000002000A 00000000A 4100	0	01010000000000 0000000000000000	00000000000000000000000000000000	04251024000000000000000000000000	
EXEC ERIC	003240020 003240028 5000	0	01010000000000 0000000000000000	00000000000000000000000000000000	04251024000000000000000000000000	
EXEC ERIC	003240120 00324012C 5000	0	01010000000000 0000000000000000	00000000000000000000000000000000	04251024000000000000000000000000	
EXEC ERIC	001442416 00144261D 5160	0	01010000000000 0000000000000000	00000000000000000000000000000000	045123220 6523251520 00000000000000000000000000000000	
EXEC ERIC	001530360 00144612D 5160	0	01010000000000 0000000000000000	00000000000000000000000000000000	045123220 6523251520 00000000000000000000000000000000	
EXEC ERIC	001530360 00144612D 5160	0	01010000000000 0000000000000000	00000000000000000000000000000000	045123220 6523251520 00000000000000000000000000000000	
EXEC ERIC	001461308 001442631D 5160	0	01010000000000 0000000000000000	00000000000000000000000000000000	045123220 6523251520 00000000000000000000000000000000	
ITEM COUNT EXCEEDS 10 ITEM DXTR LIMIT; DIRECTIVE TERMINATED						

*** FILES, DOS=DUMP2, SYM1=EXCSYM.

*** AUTO.

39

W5 Dump 1

EXECUTIVES

Ergonomics in Design 16000

F DUMP 1:14
01/08/85 18:07:03 PAGE 1

EXP - USER EXCHANGE PROCESSOR **USER-REQUESTED COS HALT PROCESSOR (F1CRASH)**

CRAY XMP CAL 1.14 (12/27/84) 12/27/84 11:15:57 P

```

***** NAME ----- CRASHF *****

o PURPOSE TO ALLOW A USER TO HALT COS (TO BE USED FOR DEBUGGING
  o PURPOSE $1,
    o ENTRY AI == TCB ADDR
      o A2 == JIA ADDR
    o EXIT AI == TCB ADDR
      o A2 == JIA ADDR
    o METHOD D OF CRASHF NONZERO HALT COS
      o ELSE ABORT THE USER WITH "UNDEFINED-USER-CALL"
    o
    o
***** 7247b+ CRASHF = *****

d <macro> 7247b+ CRASHF = A0 CRASHF +-----+
  ERAN S1
  ERUC S2
  IF (CRASHF NONZERO) HALT
  ELSE INVALID USER CALL
  AND ABORT THE USER

```

EXP - USER EXCHANGE PROCESSOR
CONDITIONAL STATEMENT BLOCK

CRAY XNP CAL 1.14(12/27/84) 12/27/84 11:15:57
FCSF
NAME-CS8
PURPOSE - PERFORM THE VARIOUS FUNCTIONS WHICH ARE RELATED TO

PROJECT: U15#2
LIBRARY: SWCE
TYPE: CNTL

NUMBER: CRASH
LEVEL: 1.21
USERID: U15#2

DATE: 85/12/13
TIME: 18:43
PAGE: 1 OF 1

START

COL -----1-----2-----3-----4-----5-----6-----7-----8

1 JOB,JN=U15#2A.
1 ACCOUNT,AC=2651#,US=SYSTEM,UPW=CRAY.
1 ****
1 *
1 * THIS JOB CREATES THE DATASET FOE CESW I EXERCISE 1
1 *
1 ****
1 CAL.
1 LDR.
1 /EOF
11 IDENT CRASH
11 START HERE
1 HERE = *
11 S F\$CRASH
11 EX
11 ENDP
11 END

LC592240.3
0377405063256106507430 CYCTIME CON INTERVAL=1,UNITS=SEC,TYPE=RECIP
0*9499999990.0E-18 1/105263158

LC592240.3

REC

SYSTEM TASK PROCESSOR TABLES
SYSTEM TABLES

CRAY XMP CAL 1.14(12/11/84) 12/14/84 10:55:53 Page 35
Block: /TABLE2/ (35)

12054	0405022J05112400000000	ABORTF	CON	"ABORT" L	JOB-ABORT FLAG: SET PARCEL 0 NONZERO	STPTAB.58
12055	0415222025151000000000	CRASHF	CON	"CRASH" L	CRASH SYSTEM WHEN ANY JOB ABORTS	STPTAB.58
12056	0251242404151723453075	*	CON	"TSCONV"	COS-HALT FLAG: SET PARCEL 0 NONZERO TO ENABLE F\$CRASH CALL	H08580CH.59
12057	0000000000000000047516	TSCONV	CON	"ON" R	NZ if #TSConv enabled	LC5923RH.2
12060	000000000000000000000000	JXTMAX	CON	D"0	MAX ACTIVE JXT ENTRIES ALLOWED	STPTAB.58
12061	0000000000000000000010	TOUICK	CON	D"8	TIME OF A QUICK JOB	STPTAB.58
12062	000000000000000000000020	TLBIAS	CON	D"16	BIAS FOR INITIAL PRIORITY CURVE	STPTAB.59
		*		*		STPTAB.59
		*		*		NO8113ED.7
		*		SYSTEM EXECUTION TABLE (SXT)		NO8113ED.8
		*		THE SYSTEM EXECUTION TABLE IS USED TO CONTROL WHICH TASKS HAVE		NO8113ED.9
		*		EXECUTION PROFILING ENABLED FOR THEM AND WHERE THE APPROPRIATE		NO8113ED.10
		*		INFORMATION IS. THERE IS ONE ENTRY PER TASK. IN TASK ID ORDER NO8113ED.11		NO8113ED.11
		*				NO8113ED.12
12063	0515302500000000000000		CON	"SXT" L		NO8113ED.13
12064	17	B\$SXT	BSSZ	MAXTN		NO8113ED.14
		*		*		NO8113ED.15
		*		MEMORY POOL TABLES		STPTAB.59
		*				STPTAB.59
	6	SZ\$MPH	=	SZ\$MPH	GET SYMBOL INTO SYMBOL TABLE	STPTAB.59
12103	00000000000000000005	POOLTBL	CON	MAXPOOL	MAXIMUM VALID POOL NUMBER	NO904488.45
12104	000000000372000012111	VWD		D"16/0,D"24/MP15,D"24/POOL1		STPTAB.59
12105	00000000010000016031	VWD		D"16/0,D"24/MP25,D"24/POOL2		STPTAB.59
12106	00000000000000017031	VWD		D"16/0,D"24/MP35,D"24/POOL3		KC4532UA.2
12107	000000000000300017033	VWD		D"16/0,D"24/MP45,D"24/POOL4 ISP POOL		NO904488.46
12110	000000000026400017036	VWD		D"16/0,D"24/HP55,D"24/POOL5		NO9402AC.5
12111	000000010101010003720	POOL1	VWD	D"16/0,D"24/0*01010101,D"24/MP15		STPTAB.59
12112	3716	BSSZ		MP15-MPHT,		STPTAB.59
16030	000000010101010003720	VWD		D"16/0,D"24/0*01010101,D"24/MP15		STPTAB.60
16031	0000000202020200001000	POOL2	VWD	D"16/0,D"24/0*02020202,D"24/MP25		STPTAB.60
16032	776	BSSZ		MP25-MPHT		STPTAB.60
17030	0000000202020200001000	VWD		D"16/0,D"24/0*02020202,D"24/MP25		STPTAB.60
17031	0000000303030300000000	POOL3	VWD	D"16/0,D"24/0*3030303,D"24/MP35		KC4532UA.3
		IFE		I@MP3SZ,NE,0,1 DEFINE A POOL IF TAPE DRIVES DEFINED		KC4532UA.4
			BSSZ	MP35-MPHT		KC4532UA.5
17032	0000000303030300000000	VWD		D"16/0,D"24/0*3030303,D"24/MP35		KC4532UA.6
17033	0000000404040400000003	POOL4	VWD	D"16/0,D"24/0*04040404,D"24/MP45		NO904488.47
17034	1	BSSZ		MP45-MPHT		NO904488.48
17035	0000000404040400000003	VWD		D"16/0,D"24/0*04040404,D"24/MP45		NO904488.49
17036	000000050505050000264	POOL5	VWD	D"16/0,D"24/0*05050505,D"24/HP55		NO9402AC.6
17037	262	BSSZ		MP55-MPHT		NO9402AC.7
17321	000000050505050000264	VWD		D"16/0,D"24/0*05050505,D"24/HP55		NO9402AC.7
17322	0000000000000000000000	IDWRD	CON	0	MEMORY POOL ID'S	STPTAB.60
17323	0000000000000001010101		CON	0*01010101		STPTAB.60
17324	0000000000000002020202		CON	0*02020202		STPTAB.60
17325	0000000000000003030303		CON	0*03030303		KC4532UA.7

Exercise 7 IOS Dump Analysis

Skill: To Analyze an IOS dump for failure

IOS DUMP 1

- Locate the hang message in the punted IOP

address 5630 IOP1 HALT 004

- Locate the PUNTIF macro

address 12141 in the Kernel High Speed Channel Driver

- Analyze the history trace table events

- Locate the active overlay number, name and activity descriptor

*10 AMSG activity descriptor is at 45310

- Locate the active SMOD (ESMD)

address 45334

- Locate the relative P register of the active programs SMOD

p= 1174 relative to %B

- Determine what operand registers have useful overlay parameter registers

%HISP contains the channel number

High speed channel transfer from AMSG and the Busy & Done flag were set on completion and recovery was tried

Intended Lesson Results: To be able to find out which program halted IOS

44

TOP APML X.15(12/21/84) 12/26/84 10:44:32 Page 53
(12)

SC
eray AMSG

* AM\$XFER Transfer data between the Cray and buffer memory.
* *****

XFER * Initialize registers
* transfer length
* Cray address
<macro> RIMG, DA@LNO, RIDAL
<macro> RIMI, DA@CM0, RIDAL
<macro> RIMJ, DA@CM1, RIDAL
<macro> RIMK, DA@BM0, RIDAL .buffer memory address
<macro> RIML, DA@BM1, RIDAL .function code
<macro> RIMM, DA@FC, RIDAL
TRANSFER RIMM, RIMI, RIMJ, RIMK, RIML, RIMG
AMSG. 356
AMSG. 357
AMSG. 358
AMSG. 359
AMSG. 360
AMSG. 361
AMSG. 362
AMSG. 363
AMSG. 364
AMSG. 365
AMSG. 366
AMSG. 367
AMSG. 368
AMSG. 369
AMSG. 370
AMSG. 371
AMSG. 372
AMSG. 373
AMSG. 374
AMSG. 375
AMSG. 376
AMSG. 377
AMSG. 378
AMSG. 379
AMSG. 380
SIF 1
AMSG. 381
AMSG. 382
SENDIF 1
AMSG. 383

* AM\$XFRDN Transfer between the Cray and buffer memory has
* completed. Reactivate the waiting activity.
* *****

XFRDN * GET RIEN, DA@ACT, RIDAL .Get issuing activity address
* SIF (A # 0)
* RKERN EQCP .Reschedule the activity
* SENDIF .Dispose of the DALS
* P = MIREED
1226 <macro>
1226 <macro>
1233 <macro>
1235 <macro>
1237 <macro>

1237 071713

ESSOR KERNEL, VERSION 1.14, SN12, MENDOTA HEIGHTS
 MEMORY CONTROL
 IOP APML X.15(12/21/84) 12/21/84 15:17:29 Page 47
 (47)

```

    12032 070074          .TRY AGAIN
    12033 <macro>          .<<<< 0
    12033 001000          P = CEREC
    12033 <macro>          $ENDIF
    EXIT
    **** MEMORY I/O SUBROUTINES ****
    ****
    * WAIT FOR MEMORY I/O TO COMPLETE
    *
    * B - CHANNEL NUMBER TO TEST
    *
    * CHNWTDN *
    * RIMIOTICK = 0
    * RIMIOTOCK = A
    *UNTIL (IOB = DN)
    * COUNT LOOPS
    * RIMIOTICK = RIMIOTICK + 1
    * IF MILLISECOND EXPIRES
    *IF (A = MIOS$SEC)
    * IT IS POSSIBLE FOR THE I/O CHANNELS
    * BETWEEN THE I/O AND THE CPU TO BE LOCKED OUT
    * INDEFINITELY BY HIGHER PRIORITY CONSTANT
    * MEMORY REFERENCES SUCH AS VECTOR STORES AND
    * LOADS. THIS CODE GENERATES A REQUEST TO THE CPU
    * TO INTERRUPT CURRENT MEMORY REFERENCES TO ALLOW
    * I/O TO COMPLETE.
    SIF (B = RI%HISP) OR (B = RI%HISP + 1) .HIGH-SPEED CHANNEL
    .>>>> 1
    .>>>> 2
    SIF (RIMIOTOCK = 0) .FIRST MS ONLY
    * LOG OCCURENCE
    (HSPLOCK) = (HSPLOCK) + 1
    .<<<< 2
    $ENDIF
    .<<<< 1
    *
    * UPDATE COUNTERS
    RIMIOTICK = 0 .CLEAR LOOP COUNTER
    RIMIOTOCK = RIMIOTICK + 1 .ADVANCE TIMEOUT COUNTER
  
```

Line Number	Text	Value
12032	070074	
12033	<macro>	
12033	001000	
12034	010000	024071
12034	024102	
12036	<macro>	
12037	026071	
12041	<macro>	
12042	<macro>	
12046	<macro>	
12060	<macro>	
12063	002000	000000
12065	050000	024025
12067	010000	024036
12071	<macro>	
12076	077000	/003316
12100	020025	054000
12102	014000	/005446
12106	<macro>	
12106	<macro>	

TOS DMP1

DXTR TYPE=10P1

F DUMP 1, 14
SYS DUMP
01/03/85 15:54:27
PAGE 54

EVENT	RTC-L	OVL#	PAR-1	PAR-2	PAR-3	PAR-4	PAR-5
MEM-10	007655	AMSG	000006	000001	057301	060000	000006
K-CALL	007655	AMSG	TRANSF	001174	045334	005310	001517
INTPT	007655	AMSG	000005	025536	025530	000005	075674
I-HAND	007655	AMSG	000006	100313	047125	000077	00023
TASK	007655	AMSG	025330	000006	000000	015310	000023
ACOM	007655	ACOM	000003	007254	100312	000000	000000
MEM-10	007655	ACOM	000002	000000	013730	041254	000010
TASK	007655	ACOM	031064	000006	000007	045250	000023
INTPT	007655	KERNEL	000006	013107	000000	0000045	075674
I-HAND	007655	KERNEL	000006	100312	047124	000100	000023
INTPT	007655	KERNEL	000007	013114	000000	0000045	056013
A-TO-A	007654	AMSG	000007	100311	000014	000000	001010
MEM-10	007654	AMSG	000003	000000	013720	012014	000010
TASK	007654	AMSG	025330	001174	000010	045310	000023
MEM-10	007654	AMSG	000010	000037	107517	062500	002660
MEM-10	007654	AMSG	000002	000001	060520	062500	000320
MEM-10	007654	AMSG	000001	000037	106777	060000	000320
MEM-10	007654	AMSG	000002	000001	060000	060000	000020
K-CALL	007654	AMSG	000004	060000	060000	000020	000000
TASK	007654	AMSG	025330	001174	045314	045310	000157
ACOM	007654	ACOM	000013	042014	100311	000000	000000
INTPT	007654	ACOM	000007	012036	031061	0000045	061637
MEM-10	007654	ACOM	000002	000000	013720	012014	000010
TASK	007654	ACOM	031064	000006	000007	045250	000023
A-TO-A	007654	AMSG	000007	100310	000014	000000	000100
MEM-10	007654	AMSG	000003	000000	013710	037554	000010
TASK	007654	AMSG	025330	001174	000010	045310	000023
MEM-10	007654	AMSG	000010	000001	057271	060000	000006
MEM-10	007654	AMSG	000002	000001	021000	060000	000006
K-CALL	007654	AMSG	000002	000002	045334	045310	000157
INTPT	007654	AMSG	000006	025336	025330	0000045	061637
I-HAND	007654	AMSG	000006	100311	045123	0000045	000023
TASK	007654	AMSG	025330	000006	000010	045310	000023
ACOM	007654	ACOM	000013	037554	100310	000000	000000
MCH-10	007654	ACOM	000002	000001	031710	037554	000010
TASK	007654	ACOM	031064	000006	000007	045250	000023
INTPT	007654	KERNEL	000006	013114	000000	0000045	061637
I-HAND	007654	KERNEL	000006	100310	047122	000100	000023
INTPT	007654	KERNEL	000007	013110	000000	0000035	107710
A-TO-A	007654	AMSG	000007	100307	000004	000000	000000
MCH-10	007654	AMSG	000003	000003	013000	037314	000010
TASK	007654	AMSG	025330	001174	000010	045310	000023
INTPT	007654	AMSG	000006	000006	057520	062500	000260
I-HAND	007654	AMSG	000006	000037	107517	062500	000260
MEM-10	007654	AMSG	000004	000001	057000	060000	000520
INTPT	007654	AMSG	000007	012044	025330	0000035	107710
MCH-10	007654	AMSG	001006	000037	106777	060000	000520
K-CALL	007654	AMSG	001174	045334	045310	000057	000057
TASK	007654	AMSG	025330	000006	000010	045310	000023
ACOM	007654	ACOM	000043	017314	100507	000000	000000
MEM-10	007654	ACOM	000002	013700	037314	000010	000000

C4

Exercise 6 COS Dump Analysis

Skill: To analyze a COS dump for Failure

COS DUMP 2

a. Locate the Stop buffer

address 1474 STOP06 Operand Range Error in STP

b. Locate the STP hang message if an STP hang

none

c. Locate the STOP macro

address 17557B

d. Locate the ERR macro if an STP hang

None

e. Locate the active Task

JSH The Job Scheduler at address 217 relative in COPY

f. Locate the active task exchange package

address 5160

g. Analyze the EXEC history trace events

An Operand Range Error occurred while JSH was using an STP common routine COPY to move a job using V0 and V1 block transfers, instruction 176 and 177.

Intended Lesson Results: To be able to find out which program halted COS and study the events that lead up to the halt and why it halted.

DULER -- SUBROUTINES
SHUFFLE SOME DATA IN MEMORY

XMP CAL 1.14(12/11/84) 12/14/84 11:26:44 Page 244
(1244)

c NAME: NOVEHEN

PURPOSE: To move a block of words in memory

ENTRY: A3 - Address of destination area

A6 - Address of source area

A7 - Number of words to move

EXIT: (A7) Words are moved from (A5) to (A3)

DESTROYS: (A0), (S0-S3)

METHOD: If "A3=A5," then return. else call common routine COPY.

7333d <macro> 7333d+ NOVEHEN = *JUHP

7334c 024000 A0 800.

7334d 1100 00007350+ HEMB0,0 A0

7335b 1306 00007351+ HEMS6,0 S6

7336d 1307 00007362+ HEMS7,0 S7

Haker's POST-call:

7336b 071105 S1 A5

C 071203 S2 A3

d 071707 S7 A7

7337a 054130 S1 S1<D>24

b 054230 S2 S2<D>24

c 051117 S1 S1:S7

d 0407 00046526 S7 CHVR

7340b- 054760 S7 S7<D>48

c 051117 S1 S1:S7

d <macro> POST S2,S1,S2

Set up registers for COPY call.

7342a 071104 S1 A4

b 071206 S2 A6

c 071307 S3 A7

d 030405 A4 AS

7343a 030503 A5 A3

b 030607 A6 A7

c 0207 0000000X A7 JSHSTK

d 007 0000000X R COPY

Restore registers.

7344a 030504 A6 A4

b 023620 A7 S1

c 023620 A6 S2

d 023730 A7 S3

N09340BA.1 N09340BA.2 N09340BA.3 N09340BA.4 N09340BA.5 N09340BA.6 N09340BA.7 N09340BA.8 N09340BA.9 N09340BA.10 N09340BA.11 N09340BA.12 N09340BA.13 N09340BA.14 N09340BA.15 N09340BA.16 N09340BA.17 N09340BA.18 N09340BA.19 N09340BA.20 N09340BA.21 N09340BA.22 N09340BA.23 N09340BA.24 N09340BA.25 N09340BA.26 N09340BA.27 N09340BA.28 N09340BA.29 N09340BA.30 N09340BA.31 N09340BA.32 N09340BA.33 N09340BA.34 N09340BA.35 N09340BA.36 N09340BA.37 N09340BA.38 N09340BA.39 N09340BA.40 N09340BA.41 N09340BA.42 N09340BA.43 N09340BA.44 N09340BA.45 N09340BA.46 N09340BA.47 N09340BA.48 N09340BA.49 N09340BA.50 N09340BA.51 N09340BA.52 N09340BA.53 N09340BA.54 N09340BA.55 N09340BA.56 N09340BA.57 N09340BA.58 N09340BA.59 N09340BA.60 N09340BA.61 N09340BA.62 N09340BA.63

DULER -- SUBROUTINES
SHUFFLE SOME DATA IN MEMORY

XMP CAL 1.14(12/11/84) 12/14/84 11:26:44 Page 245
(45)

7345c 1206 00007351+ HER5,0	S6	N09340BA.58
7346d 1207 00007352+ HER57,0	S7	N09340BA.59
c 1000 00007350+ HER0,0	AD	N09340BA.60
b 023600,0	000	N09340BA.61
b 023730,0	000	N09340BA.62

59

COPY Common subroutine. Memory to memory copy.

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 (4)

Line	Text	Time
244	RETURN	2:26 D
04	BACOPY	2:27 R
10c4	COPY	2:27 E
0	CTID COSIXT	2:27 D
0X	ERROR1	3:2 R
144	MOVEBG	3:49 R
		2:27 D
		2:27
		2:30
		2:31
		2:54
		3:54

HOS
DMP
E

DXTT TYPE=10PO

EDUUP 1.14
Svärdförbundet

86

CIA : 11 - READ READY WAITING/ERROR FLAGS

A CIA : 11 function request reads the content of the interface status register into the accumulator. The status bit assignment is listed in table 7-10.

Table 7-10. Ready waiting/error flags

Bit	Meaning
2^0	Channel Parity Error flag for bits 2^0-2^3
2^1	Channel Parity Error flag for bits 2^4-2^7
2^2	Channel Parity Error flag for bits 2^8-2^{11}
2^3	Channel Parity Error flag for bits $2^{12}-2^{15}$
2^{15}	Ready waiting flag

CHANNEL FOR OUTPUT TO CRAY MAINFRAME CHANNEL

The IOP can have one or more channels for sending data to a Cray mainframe input channel. Data is transferred in block mode directly from Local Memory. The functions are listed below.

- COA : 0^t Clear channel.
- COA : 1^t Enter Local Memory address; start transfer.
- COA : 2 Enter parcel count.
- COA : 3 Clear error flag.
- COA : 4 Set/clear external control signals.
- COA : 6^{tt} Clear Interrupt Enable flag.
- COA : 7^{tt} Set Interrupt Enable flag.
- COA : 10 Read Local Memory address.

COA : 14 Read error flags.

^t Allow 1 CP before checking Busy and Done flags.

^{tt} Allow 1 CP before checking the interrupt channel number (IOR# : 10).

JOB FLOW

10

()

()

()

TUG MODULE OBJECTIVES

With the aid of all furnished reference materials, upon completion of the Job Flow module, the Learner should be able to:

1. Trace a Job Flow through the Systems Programs.
2. Explain when the job is passed to the next program.
3. Explain the function of each program.

23. STATION JOB OUTPUT

Prepare the COS job. .1
.2 system generate bsddefnrt file in disk edf dir
.3 edf and binary version edf, subout woff do

.4 import epesys2 edf subout woff do a copy .1

Submit the file containing the COS JOB to the station. .2

.3 import doce to nofconut edf nisfqx3 .3

The station queues the job for transfer to the Cray.

The station asks the Cray if it can send the file.

The Cray responds saying yes you can.

The station sends the file.

The Cray places the file on the input queue.

JOB DATASET

SECTOR	1	JOB,JN=PASSCNT.				
000001	0000000000000000000012	0451172042611223436520	0405232464151625027040	0200401002004010020040		
000005	0200401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040		
000009	0200401002004010020040	0200401002004010020040	0200401002004010020040	1000000000000000000012		
000013	04050320647523452054	040503172310615230462	0320542525147525047107	0261252405347525047107		
000017	0270401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040		
000021	0200401002004010020040	0200401002004010020040	1000000000000000000012	0415012302704010020040		
000025	0200401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040		

000033	0200401002004010020040	1000000000000000000012	0461042442704010020040	0200401002004010020040	LDR.	
000037	0200401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040	IDENT	

000045	10000000000000000000000	1600000000000000000012	0200401002004010020040	020040100224210523452040	LOOP	
000049	0200401002011423647520	0200401002004010020040	0200401002004010020040	0200401002004010020040	START	HERE
000053	0200401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040	020040100211021251105	
000057	1000000000000000000012	0200401002004010020040	020040102465210124452040	0200401002004010020040		
000061	0200401002004010020040	0200401002004010020040	0200401002004010020040	1000000000000000000012		
000065	0200401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040		
000069	0250401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040		
000073	0200401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040		
000077	0200401002004010020040	0200401002004010020040	1000000000000000000012	0475162122004010020040	ONE	
000081	0200402064751610020040	02004010020081610020040	0200401002004010020040	0200401002004010020040	CON 1	
000085	0200401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040	HERE S1	
000089	0200401002004010020040	1000000000000000000012	0441052444244010020040	0200402463044010020040		
000093	0200401002011723442454	0200401002004010020040	0200401002004010020040	0200401002004010020040		
000097	0200401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040		
000101	1000000000000000000012	0461172365004010020040	0200401024653104010020040	0200401002012314425523	1	
000105	0304401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040		
000109	0200401002004010020040	0200401002004010020040	0200401002004010020040	1000000000000000000012		
000113	0200401002004010020040	02004022424004010020040	0200401002011423647520	0200401002004010020040		
000117	0200401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040		
000121	0200401002004010020040	0200401002004010020040	1000000000000000000012	0200401002004010020040		
000125	0200402124710424020040	0200401002004010020040	0200401002004010020040	0200401002004010020040		
000129	0200401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040		
000133	0200401002004010020040	1000000000000000000012	0200401002004010020040	0200402124710410020040		
000137	0200401002004010020040	0200401002004010020040	0200401002004010020040	0200401002004010020040		

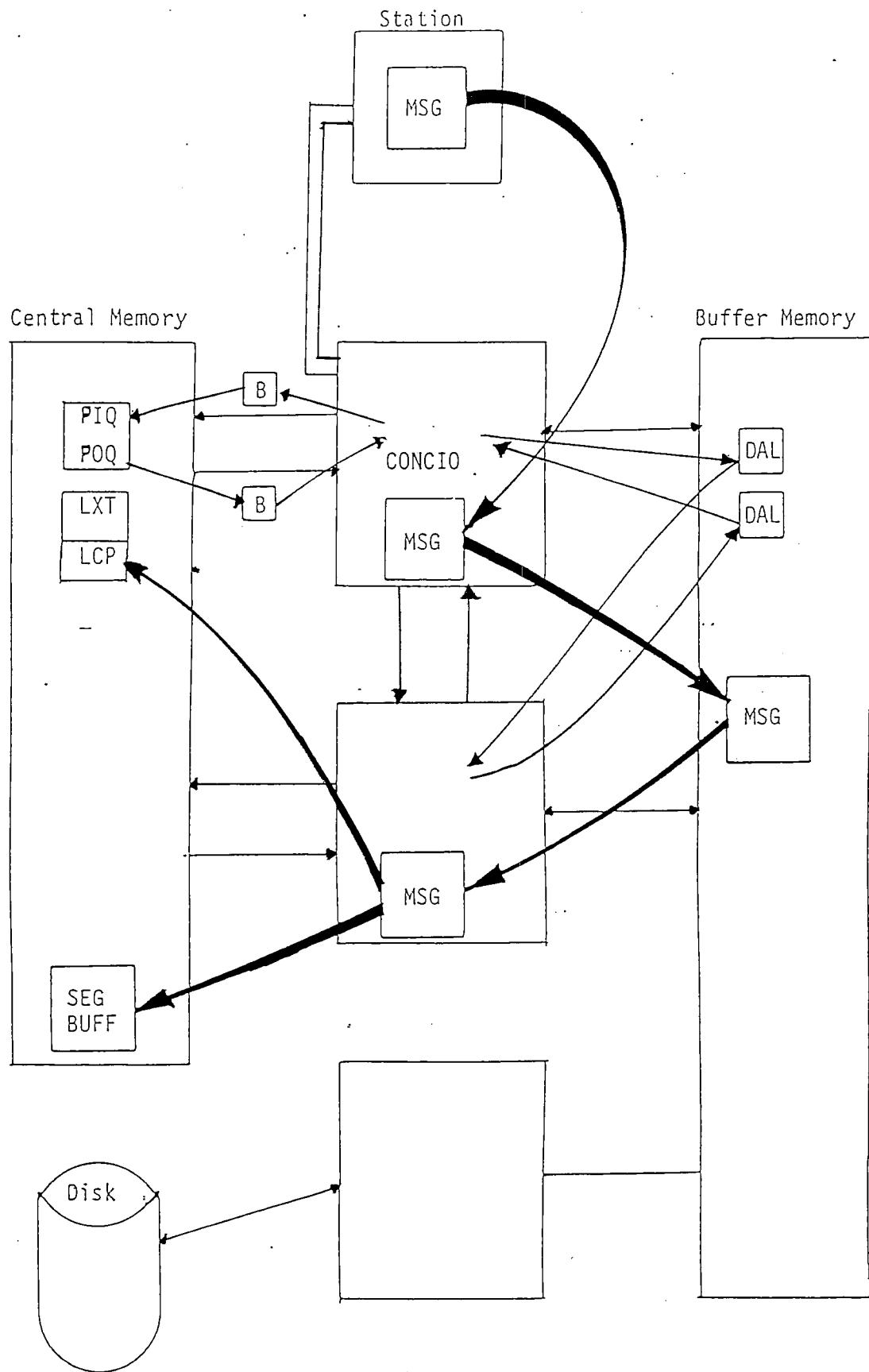
000145	1000000000000000000000	1500000000000000000000	1700000000000000000000	0000000000000000000000		
000149	0000000000000000000000	0000000000000000000000	0000000000000000000000	0000000000000000000000		

000509	0000000000000000000000	0000000000000000000000	0000000000000000000000	0000000000000000000000		

OVERVIEW OF FRONT-END MESSAGE FLOW

1. Front end sends message consisting of an LCP and possibly segments and an LTP to MIOP's local memory (CONCIO).
2. MIOP writes the message to MIOP buffer memory area (CONCIO).
3. MIOP builds B packet and sends to COS SCP.
4. SCP determines what the segment buffer and the LXT address is.
5. EXEC builds a B packet with the address information, sending it back to MIOP.
6. MIOP gets central memory address from B packet and builds a DAL and writes it to buffer memory.
7. MIOP sends BIOP an accumulator message with the buffer memory address of the DAL.
8. BIOP reads the message into local memory.
9. BIOP writes the message to central memory over the high speed channel using the DAL address information.
10. When BIOP is done with the transfer, a DAL will be sent to MIOP via buffer memory.
11. MIOP builds a B packet and sends it back to SCP telling it the message is in central memory.
12. SCP processes message LCP and builds a response LCP.
13. MIOP receives response LCP address from the B packet.
14. MIOP sends address information to BIOP in a DAL through buffer memory.
15. BIOP reads COS response message from central memory to local memory.
16. BIOP writes response message to buffer memory.
17. MIOP reads response message to local memory (CONCIO).
18. MIOP sends response message to the front end (CONCIO).

STATION CONCENTRATOR



STATION CONCENTRATOR TABLES

Concentrator Table (CT@)

The Concentrator Table is a LM-resident table containing addresses and information used by a concentrator. This table is used only by software executing in the MIOP.

Concentrator ID Table (CE@)

Contains the B Packet for each logical ID logged on to COS. Contains central memory and buffer memory addresses for each ID.

Front-End Channel Information Table (FEI@)

The FEI tables are created at system initialization time. One FEI table is created for each physical channel pair configured as a front-end interface.

Front-End Channel Ordinal Reservation Table (COR@)

The COR tables are created at system initialization time. There is one COR table for each of the maximum number of front-ends concurrently logged on through all NSC Adapter channels.

NSCIO Table Definition (NIO@)

The NSCIO Table is the control table for I/O between the IOS and an A130 NSC Adapter. The table is created by the NSC routine when an NSC channel is initialized.

Input Status Buffer (NSB@) NIO@IB

This table describes the input status buffer used to read status from the NSC A130 Adapter.

Output Function Buffer (NFB@) NIO@OB

This table describes the output function buffer, used to send functions to the NSC A130 Adapter.

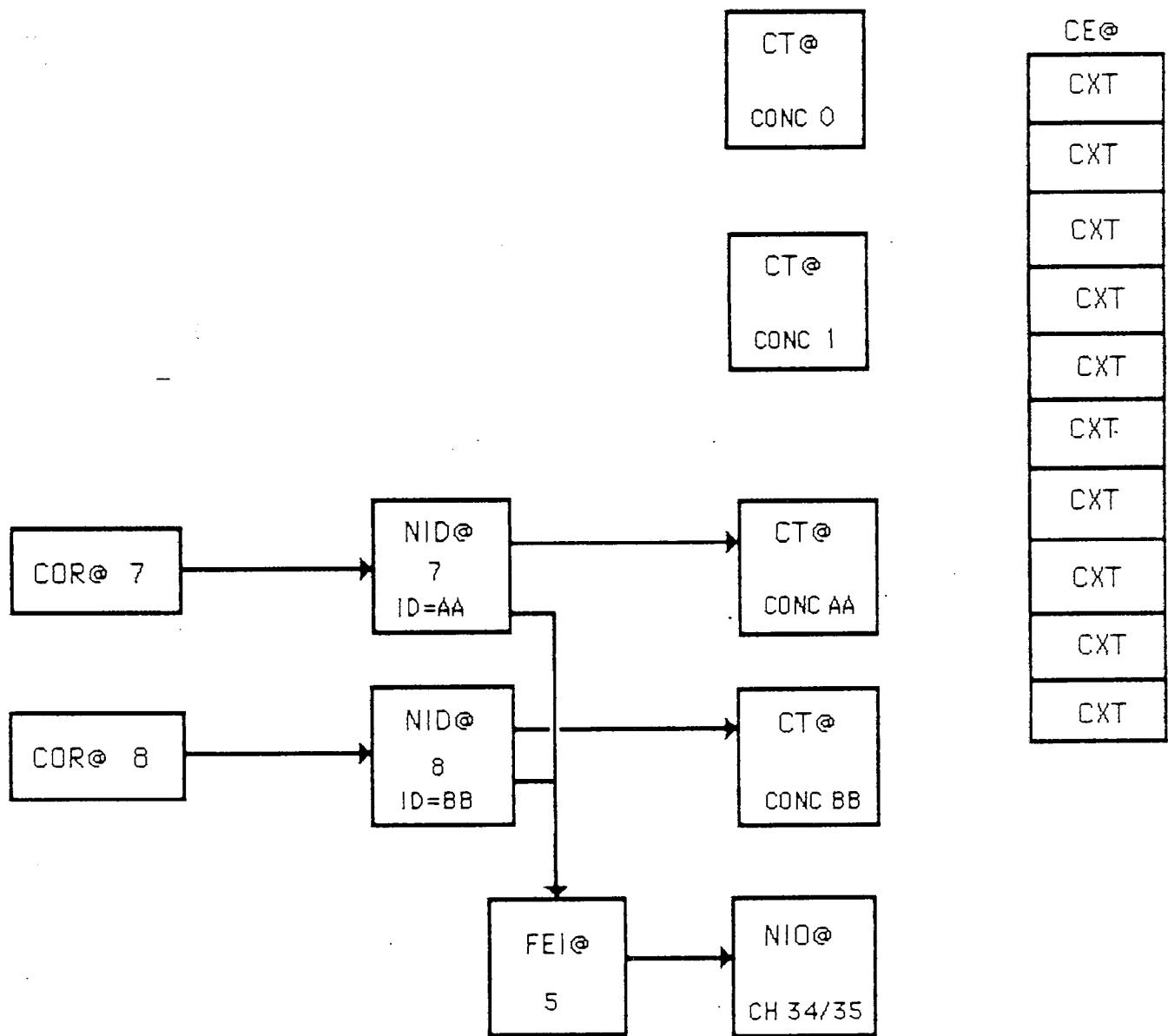
Message Proper (MP@) NIO@MP

This table describes the Message Proper, which is used for communication between trunks on the NSC A130 Adapter.

NSC Front-End ID Table (NID@)

The NSCID tables are created by the NSCIO routine when an NSC channel is initialized. There is one table created for each of the maximum number of front-ends concurrently logged on through all NSC adapter channels.

CONCENTRATOR TABLES



STATION CALL PROCESSOR

Respond to front-end requests

Streaming

I0

Status

Provides for operator guidance

Receiving datasets and jobs

Transmit output dataset

Independent of front-end type

Allocates I0 Buffers

Segment buffer - filled on read from FE channel

When full call DQM

Recovery from link error and front-end failures

Readied by Exec when I0 pair completes on a FE assigned channel

Enters dataset in the SDT

SYSTEM DATASET TABLE

SDT contains information on all datasets that are transferring

An entry on a queue represents one dataset

Seven Queues

Available Queue - contains available memory for all SDT

Input Queue - jobs waiting to be initiated

Execute Queue - jobs already initiated

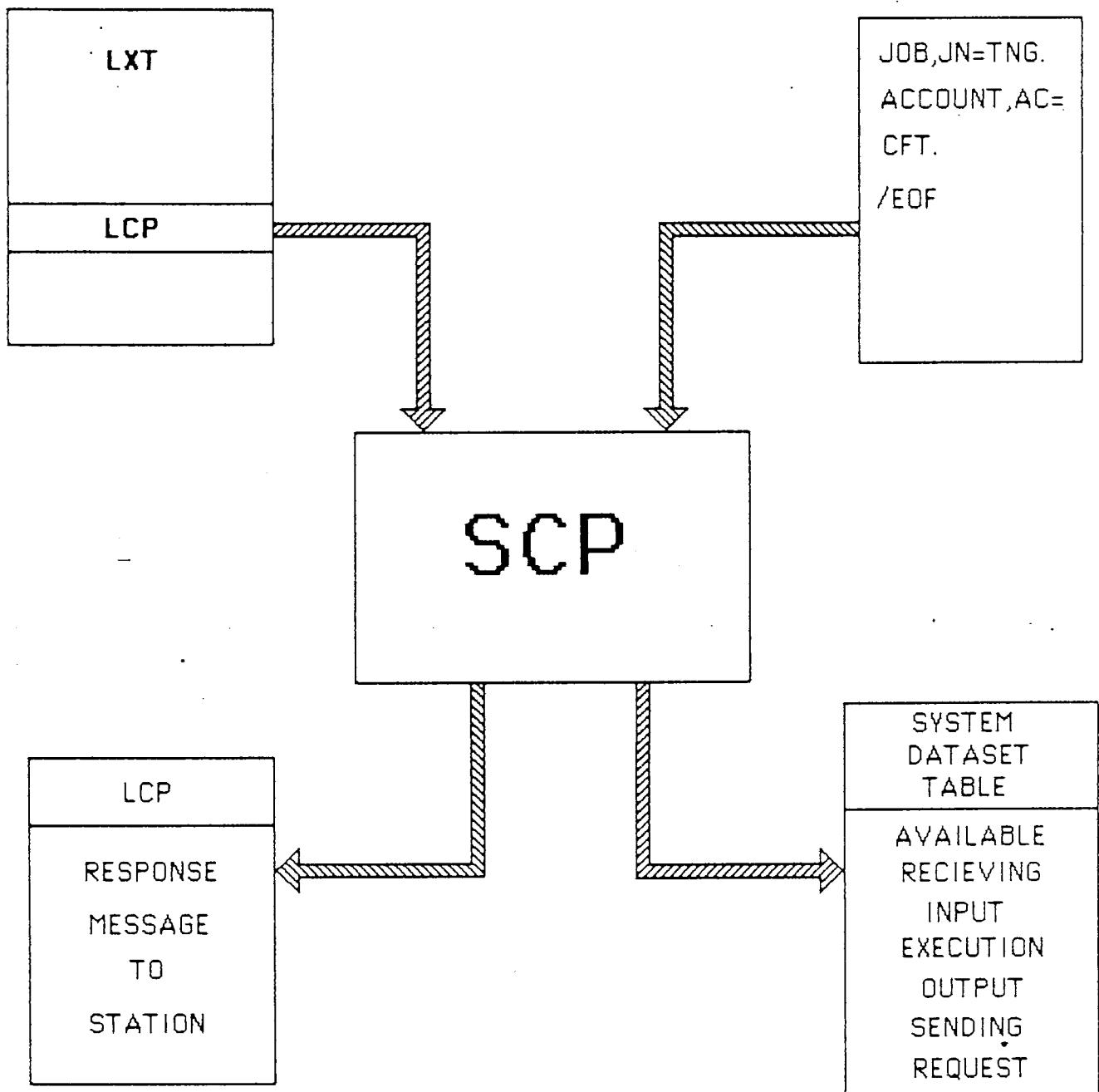
Output Queue - datasets waiting to be sent to front-end

Sending Queue - datasets in the process of being sent

Receiving Queue - datasets in the process of being received

Request Queue - for dataset acquire requests

STATION CALL PROCESSOR



FED DRIVER

Three types of drivers:

IFC Channel Coupler Request Processor
IOP Request Processor
NSC Request Processor

CHT - Channel Table

Contains the task parameter block address
Contains the LIT entry address
Handler address update by the FED after each interrupt

CXT - Channel Extension Table

There is one entry for each I/O Subsystem Ordinal
Passes the message to packet driver

LCT - Link Configuration Table

There is one LCT entry per channel configured for front-end I/O.
The ordinal defines whether the channel is on the CPU or I/O subsystem the type defines the channel's characteristics
The channel may be configured as 'On.' which will simulate a CHANNEL ON operator command during COS startup.

LIT - Link Interface Table

The Link Interface Table is used by both the Station Call Processor and EXEC and contains SCP-EXEC communication areas, working storage, and channel buffers.

An LIT entry is assigned by SCP at deadstart to each channel which is to be used by SCP for link interface communications.

LXT - Link Extension Table

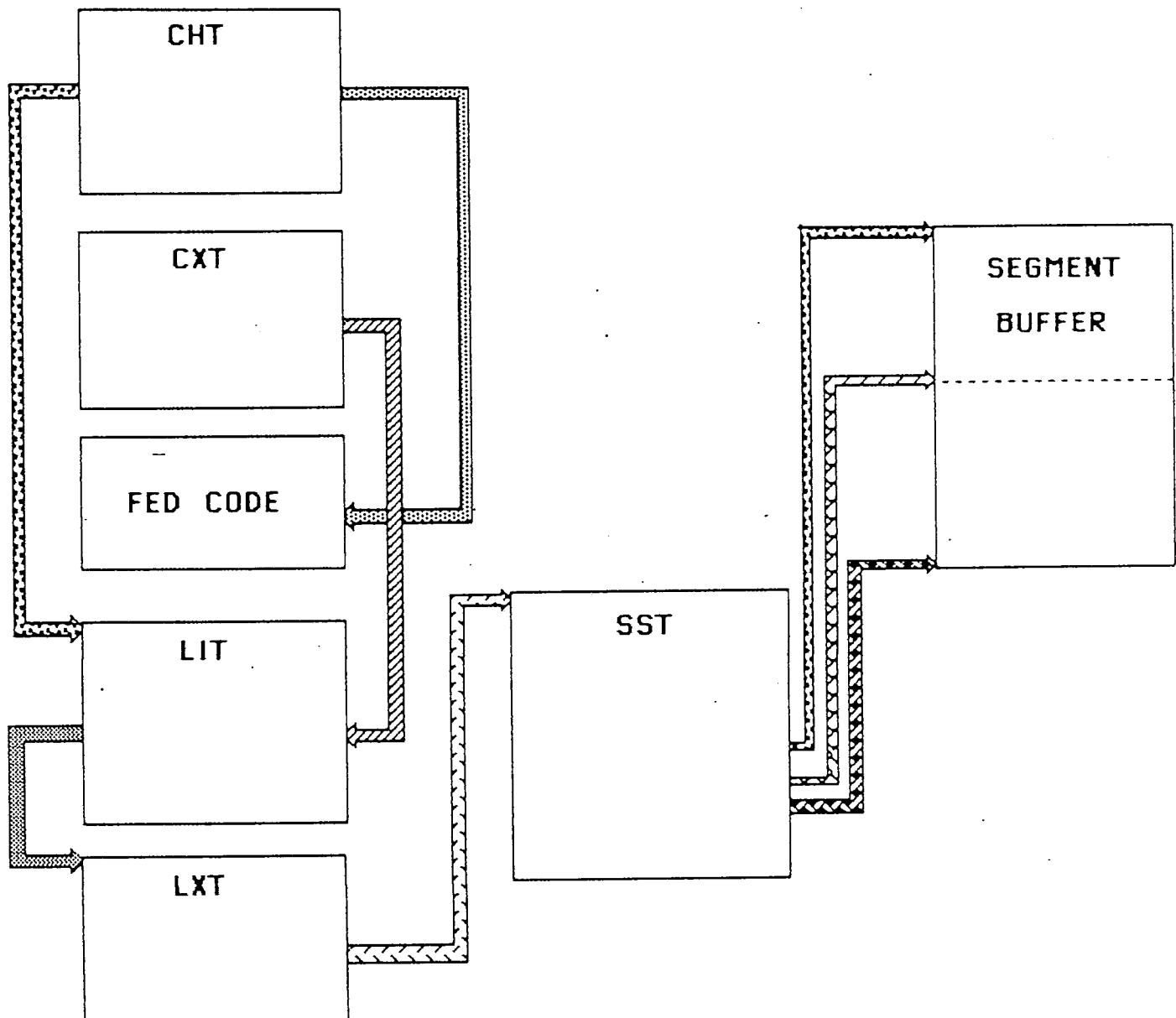
The Link Extension Table (LXT) is used for communication between EXEC and STP. It contains an entry for each configured logical front-end ID; each entry is allocated by FED on receipt of a log on message, and released after an output operation if the OFF bit is set.

Receipt of a front-end message is signaled by FED with INT (interrupt) bit. FED does not modify an entry after setting INT until the next output request is received for that entry.

SST - Stager Stream Table

Manages Segment and Disk Buffers.

EXEC'S FRONT-END TABLES



STAGER

Called by SCP

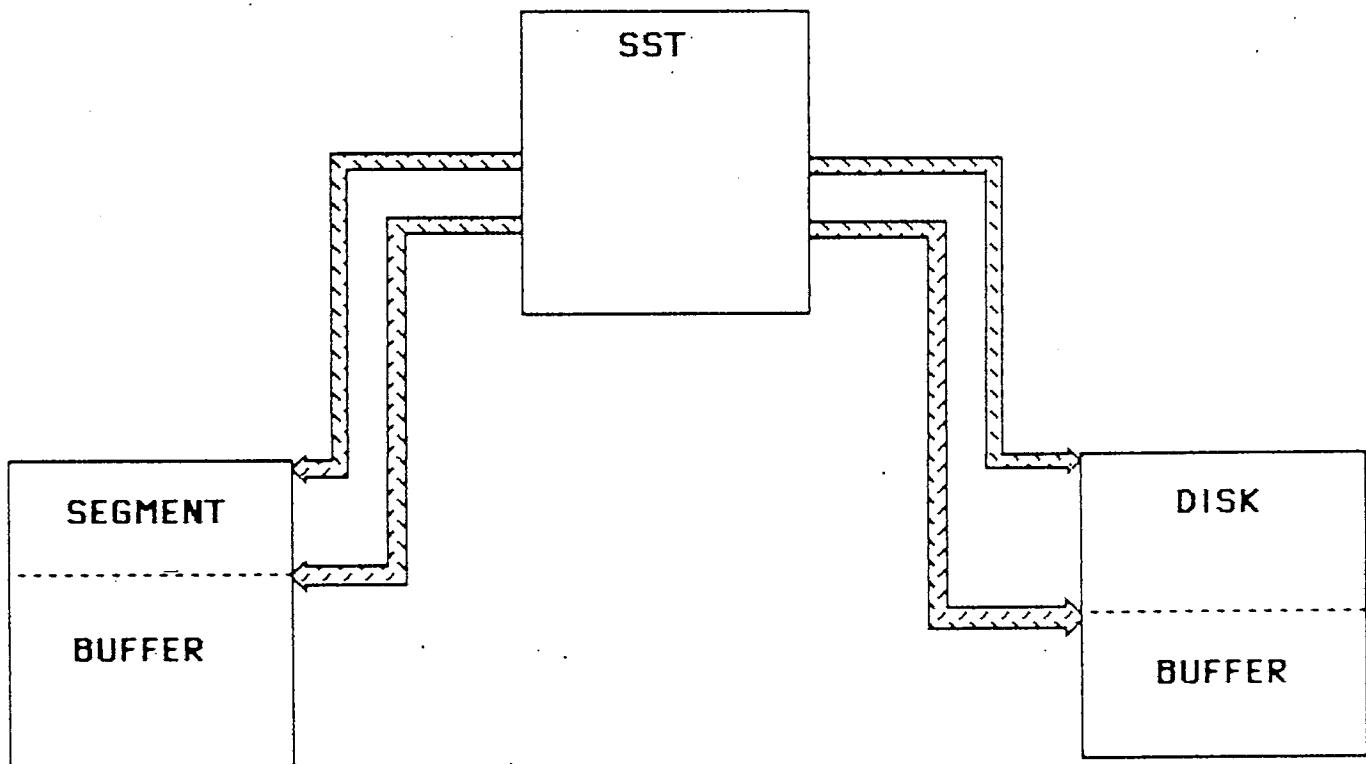
Manages Segment and Disk Buffer

Moves Data between Segment Buffer and Disk Buffer

The Staging Stream Tables are contained in the LXT. It holds information concerning the state of streams for an ID.

SST used to be called the Link Stream Table (LST)

STAGER STREAM TABLE



DISK QUEUE MANAGER - DQM

Manages disk storage request queue

Performs I/O request processing

- Preallocate disk space

- Queue I/O requests

- Deallocate disk space

Update Device Reservation Table (DRT) for each disk

Readied by another task when needed

Allocation/Deallocation of disk space

- Pre-allocate

- Dynamic Allocation

Disk space is allocated by track

- If specified by request, the Logical Device Name from the DNT is used

- Otherwise DQM rotates among the controllers and disks as specified by the order of EQT

DATASET NAME TABLE

One entry for each dataset

Built when a dataset is made local to a job

Contained in the job table area

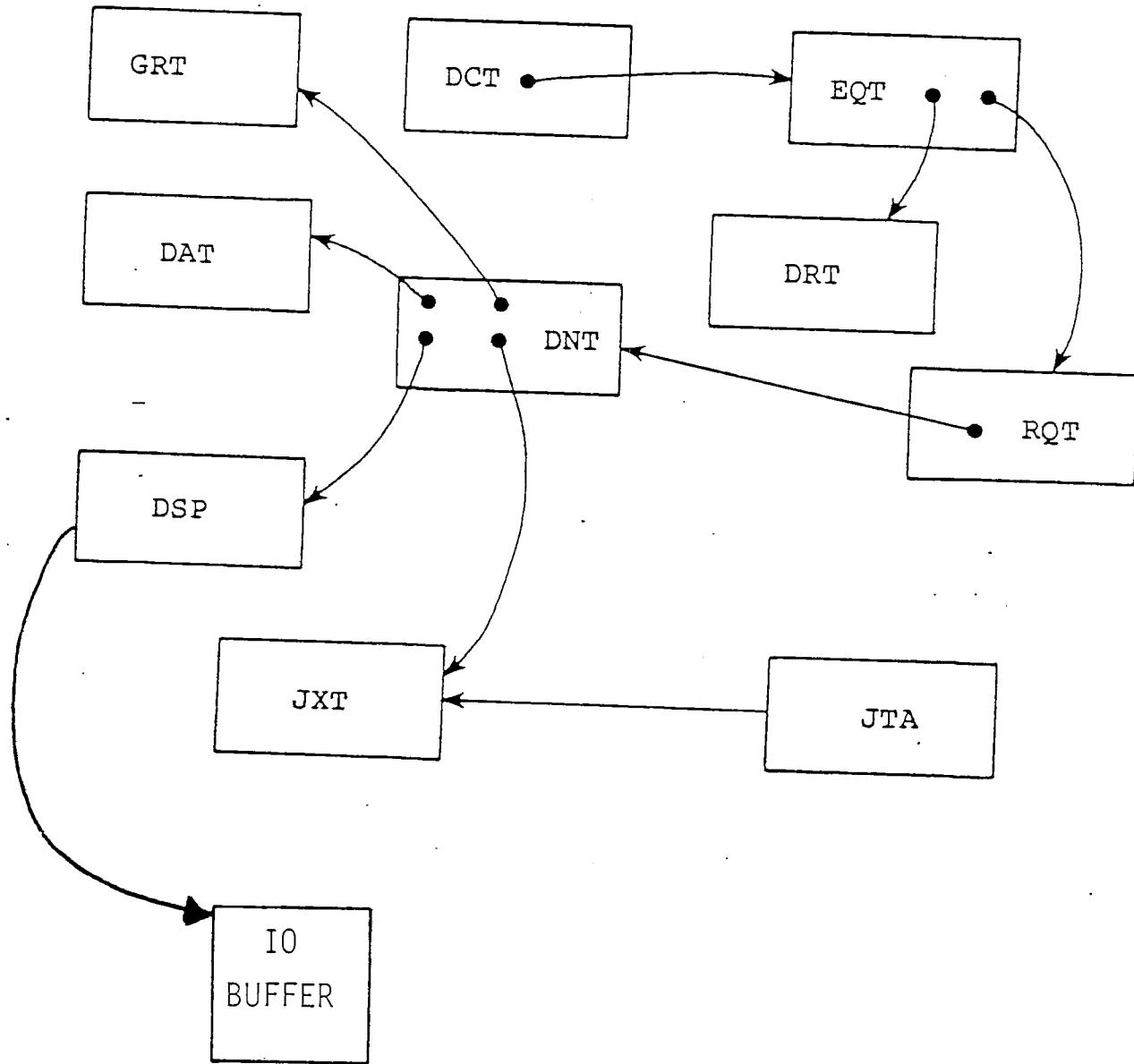
Maintained and used by DQM

Contains the status of the dataset

Points to the datasets' other tables

A system verb has a DNT in the SDR

DATASET NAME TABLE LINKS



DISK DEVICE TABLES

DCT - Device Control Table

Points to current active EQT entry for each channel

DRT - Device Reservation table

One DRT for each device-Disk,SSD,BMR

Contains a bit map indicating which tracks are allocated

Bit positions in the DRT correspond to the allocation index logical track address

Each bit in the bit map represents one allocation unit (AU), such as one track on a disk.

A set bit implies that the AU is in use.

EQT - Equipment Table

One entry per disk unit

Contains status and error information

Contains current request queue

Has the control word for request chain

Points to DRT and IO request queue

RQT - Request Queue Table

Entries are double linked

Points to the DNT for the dataset

Queue header is in EQT entry

One queue chain for each disk

Contains physical IO requests

User requests placed on end of queue

System requests placed second on chain

CNT - Configuration Table

The CNT informs the operating system of the status of on-line tape and disk devices.

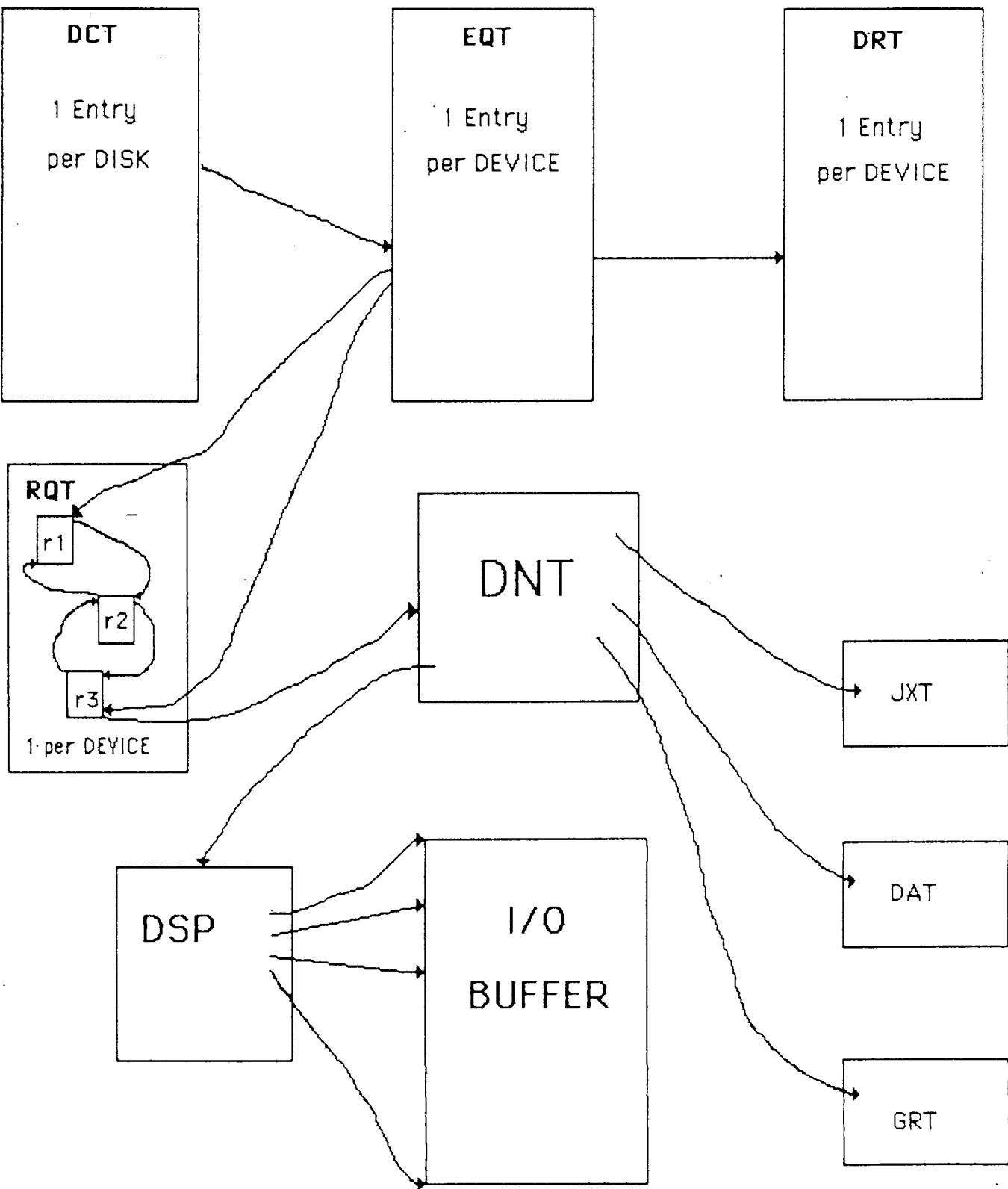
The CNT can be changed during startup by the parameter file or by operator commands.

Each entry, disk or tape occupies 12 words:

A tape entry consists of a 4-word configuration table and from 0 to 4 tape subentries.

A disk entry consists of a 3-word configuration table and 9 words that contain no useful information.

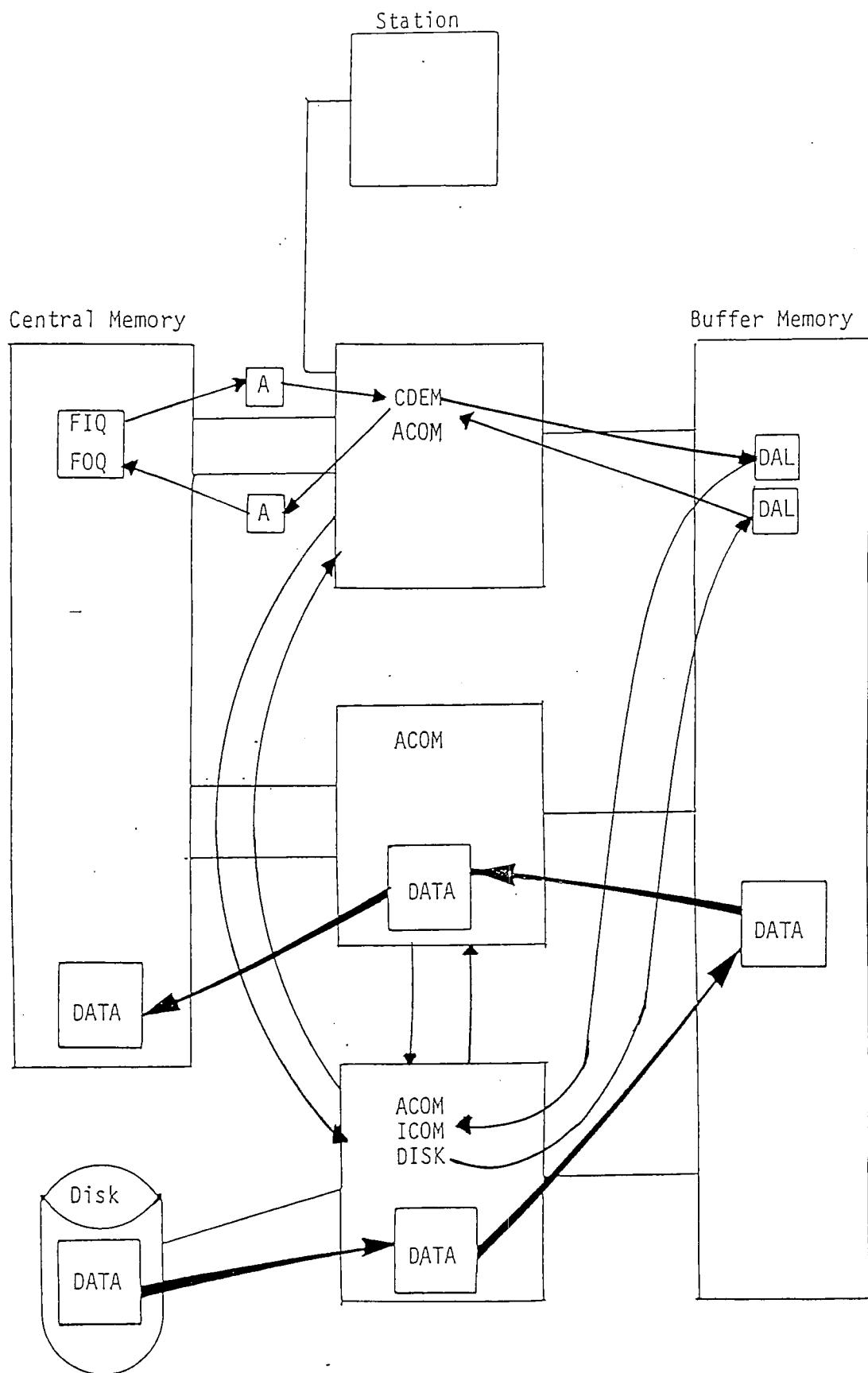
DQM TABLES LINKAGE



OVERVIEW OF A DISK READ THROUGH DIOP (WITHOUT HSP CHANNEL)

1. DQM builds an A packet with the central memory address and disk positional information and the EXEC packet driver sends the packet to IOS.
2. MIOP builds a Master DAL from the packet and writes it to the MIOP message area in buffer memory (CDEM).
3. MIOP sends DIOP an accumulator message with the buffer memory address of the MDAL (CDEM).
4. DIOP reads in the MDAL from buffer memory (ACOM).
5. DIOP builds EDALs for each sector and allocates the local memory and buffer memory disk buffers (DISK).
6. DIOP reads in the sectors and passes the data to the DIOP data buffers in buffer memory (DISK).
7. DIOP changes the EDAL parcel 1 to 4 and writes it to DIOP message area (DISK).
8. DIOP sends BIOP an accumulator message with the buffer memory address of the EDAL (DISK).
9. BIOP reads in the EDAL(ACOM).
10. BIOP reads the DIOP data buffer from buffer memory and sends it over the high-speed channel to the mainframe central memory disk buffer (ACOM).
11. BIOP changes the EDAL parcel 1 to 7 and writes it to DIOP message area in buffer memory (ACOM).
12. DIOP reads in the last EDAL and changes parcel 1 to 5 and sends MIOP the MDAL (DISK).
13. MIOP reads in the MDAL and builds an A packet with parcel 16, byte 1 a Ø (ACOM).
14. MIOP sends the A packet back to EXEC who notifies DQM.
15. DQM reads the A packet for status.

DISK IO



DISK SUBSYSTEM TABLES

Buffer Memory Disk Buffer Allocate Table (BBDISK BB@)

This table controls allocation of disk buffers in Buffer Memory. Each processor has a reserved area in Buffer Memory for its buffers.

Disk Control Block Table (DCCB)

The Channel Control Block contains a pointer to a disk control block for each defined disk channel. The channel numbers are in octal. This table is used only by Kernel software executing in the BIOP or the DIOP.

Disk Control Block (DCB@/DB@)

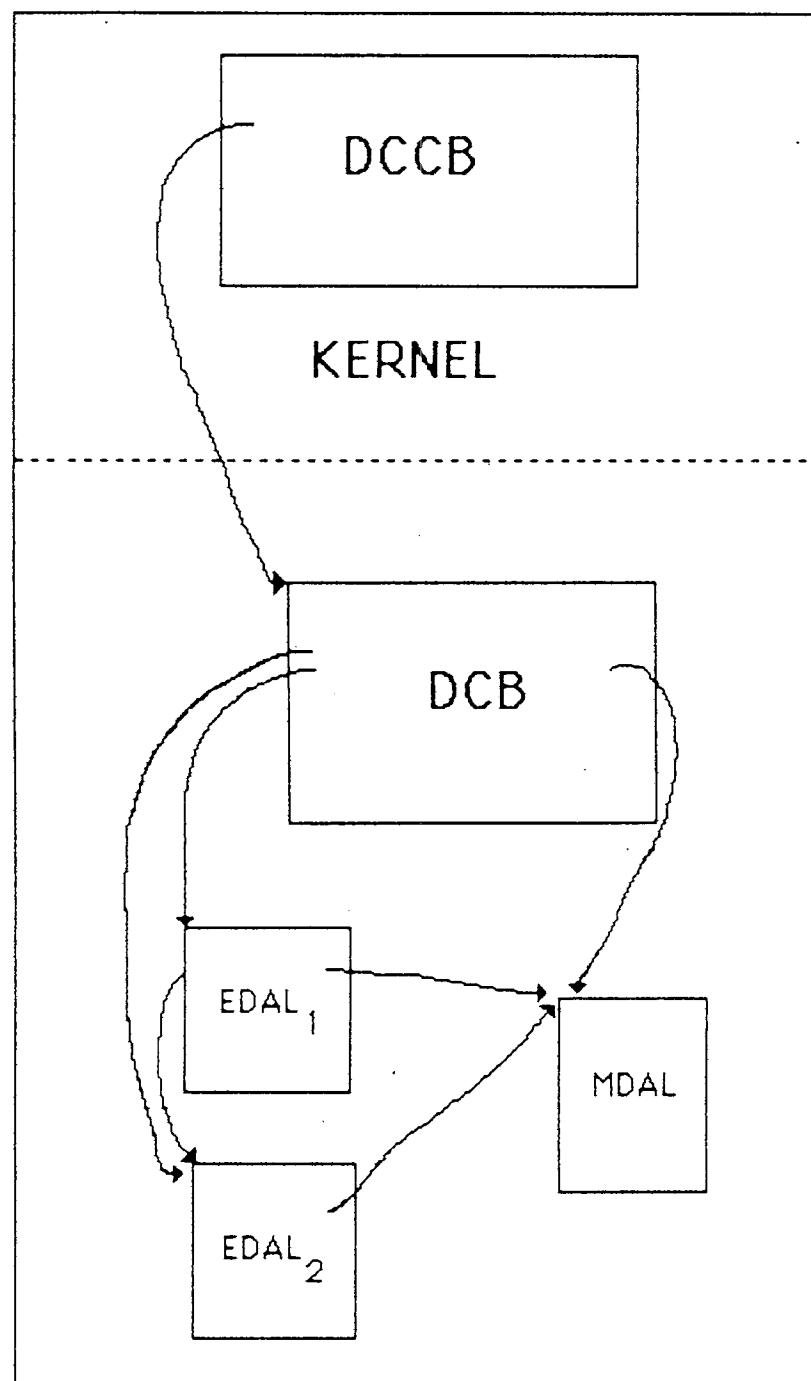
The Disk Control Block (DCB) contains 64⁸ parcels of control information for disk operations. Each disk channel that is defined has a Disk Control Block. This table is used only by Kernel software executing in the BIOP or DIOP. The Channel Control Block (DCCB) contains a pointer to each of these tables.

The read-ahead information for a disk unit follows the Disk Control Block (DCB). Six parcels are allocated for each sector to be read. Parcel 12 of the DCB points to the first read-ahead area, and parcel 10 contains a count of the number of areas allocated.

Disk Demon Queue (DISKQ)

The disk demon queue contains the Disk Control Blocks (DCBs) for the disk units requesting services from the disk demon. The queue thread runs through the cell DD@DEM in the DCB. This queue is used only by Kernel software executing in the BIOP or DIOP.

DCB TO DAL LINKAGE



JOB CLASS MANAGER - JCM

Assigns class before entry into the SDT input queue

Determines class of user job based on system resource needs

Determines number of execution entries per job class

Replaces JCL priority with class rank priority

Assigns number of available entries for each class on startup

The job class structure is defined by writing job class structure definition statements and running the JCSDEF utility

See SM-44 for JCSDEF utility

CSD - Class Structure Definition

The job class structure definition is contained in the CSD.

The CSD header, which contains general information about the structure, precedes the class maps.

One CSD class map exists for each class defined in the structure.

Class maps appear in descending rank order.

The variable length characteristic expressions follow the class maps, and each class contains a pointer to its expression.

The CSD class expressions are variable length.

The length of the CSD must be a multiple of 512 words.

STATCLASS

CRAY STATION. VERSION 1.13, IOS. L S R M

05/09/84 23:56:14

CRAY JOB CLASS STATUS

FRAME 1

CSDN = DAY-X

JOB IN SYSTEM	14	ACTIVE JOBS	13
MAX JXTS (LIMIT)	0	AVAIL POOL JXTS	- 67
DEFINED CLASSES	24	CLASSES WTG JXTS	1

CLASS	ACTIVE	WAITING	RESERVED	MAXIMUM	STATUS
FORDLG	0	0	1	2	OFF
PHILSM	0	0	2	2	OFF
PHILLG	0	0	1	1	OFF
TNGSM	0	0	2	6	ON
TNGMED	0	0	1	3	ON
TNGLG	0	0	1	2	ON
OLDIAGS	0	0	3	5	ON
TEST	0	0	4	4	ON

>
>SNAP

PDM

DSC - Dataset Catalog

The DSC resides on disk and is divided into 512-word pages, each page consisting of a block control word, a 7-word header, and eight 63-word entries.

There are two types of pages, hash pages and overflow pages.

The PDN is hashed to determine the hash page number to be searched for a matching or available DSC entry.

If that hash page is full and the function is SAVE or MODIFY, the entry is placed in the sequential overflow page area.

DXT - Dataset Catalog Extension Table

The DXT serves as a repository for information that does not fit conveniently into the DSC.

The DXT is a system dataset similar to the DSC itself. It is located or created during Deadstart; or it can be created during an Install.

It is a permanent dataset with the name \$DSC-EXTENSION and edition number 4095.

PDS - Permanent-Dataset Table

The PDS is STP resident and contains an entry for each active (accessed) permanent dataset.

A PDS entry indicates how a dataset is accessed and, if multiple access exists, how many users are accessing the dataset.

QDT - Queued Dataset Table

The Queued Dataset Table is an STP-resident table that describes the multitype attributes for a dataset that has been disposed.

This table is managed by PDM and EXP.

The number of entries in the QDT equals the SDT entry count.

PDD - Permanent Dataset Definition

A PDD is a parameter list that accompanies a Permanent Dataset Management request.

DATASET CATALOG

BLOCK CONTROL WORD	
	← Page Full
Page Overflow	PAGE HEADER 7 Words
DATASET ENTRY	
63 Words	
DATASET ENTRY	
63 Words	
DATASET ENTRY	
63 Words	
DATASET ENTRY	
63 Words	
DATASET ENTRY	
63 Words	
DATASET ENTRY	
63 Words	
DATASET ENTRY	
63 Words	
DATASET ENTRY	
63 Words	
DATASET ENTRY	
63 Words	

1 DSC PAGE
EQUALS
512 WORDS
OR
1 DISC SECTOR

JOB SCHEDULER - JSH

Schedules user jobs by priority
Performs memory management
Controls rolling and recovery of job
Saves user registers when rolled
Selects job from SDT
Initiates jobs
Controls jobs in execution, time limit - memory requests
Writes Class Structure Definition CSD
Enters job in JXT by
 Class
 Priority
 Time of submission
 Job priorities may be 0-15
 JXT can contain 63 entries
Zeros out the Job Table Area (JTA) and Job Communication Block (JCB)
Initializes the JCB pointers
Creates the DNT entries for \$CS, \$LOG, \$IN, \$OUT in the JTA
Initializes the Roll Job Index (RJI) table entry for possible job recovery.

STATUS

CRAY STATION. VERSION 1.13, IOS. L S R M

06/07/84 12:43:35

CRAY SYSTEM STATUS

CSIN = TDAY-X

QUEUES E

FRAME 2

JSQ	DC	DATASET	CLASS	STATUS	PRI	TIME	FIELD	LENGTH	ID	TID
USED LIMIT										
1995	IN	G\$IOLIB	SMALL	EXECUTE	-7.0	54	100	94	V3	U1199
1978	IN	G82DEB	MEDIUM	WAIT-I/O	-7.0	17	200	173	DX	00VSP1AAR
1851	IN	IOSSIM	MEDIUM	ROLL-IN	-7.0	198	800	273	V3	U1112
2021	IN	TEST	SMALL	WAIT-SYS	-7.0	0	20	37	V3	U0378
2013	IN	SEG	SMALL	WAIT-I/O	-7.0	5	45	220	DX	0UKSAHAA
2023	IN	SUBSYS	TEST	WAIT-I/O	-7.0	0	99	45	V3	UTS
1887	IN	GEN0049	MEDIUM	WAIT-CPU	-7.0	129	700	87	V3	U1314
1933	IN	PP8	MEDIUM	ROLLED	-7.0	25	600	530	V3	U1454

END OF DATA

>
>SNAP

JOB ROLLOUT

The Job Scheduler (JSH) task determines a need by a job for user memory and creates a request to have another job in memory rolled-out.

JSH searches the Job Execution Table (JXT) for a candidate for job roll-out.

JSH after locating a candidate may have to compact memory to obtain a large enough free segment of memory.

JSH sets the proper job states of the waiting job and the job about to be rolled-out.

The Disk Queue Manager (DQM) task allocates disk space if needed, and has the memory resident job area written to disk.

The Job Scheduler (JSH) task updates the Memory Segment Table (MST).

The Job Scheduler (JSH) task lifts the suspension on the job waiting for memory liberation.

JOB ROLLING

Memory priority rises while a job is waiting for memory

Memory priority falls when a job is in memory and executing

High priority jobs may pre-empt memory from lower priority jobs which writes the lower priority job to disk (Rolled-Out)

Rolled job priority is rising so that eventually it will be written back to memory (Rolled-in)

Priority Algorithm is tunable

Rolled jobs may be continued or rerun after a restart

Dynamic memory allocation for user

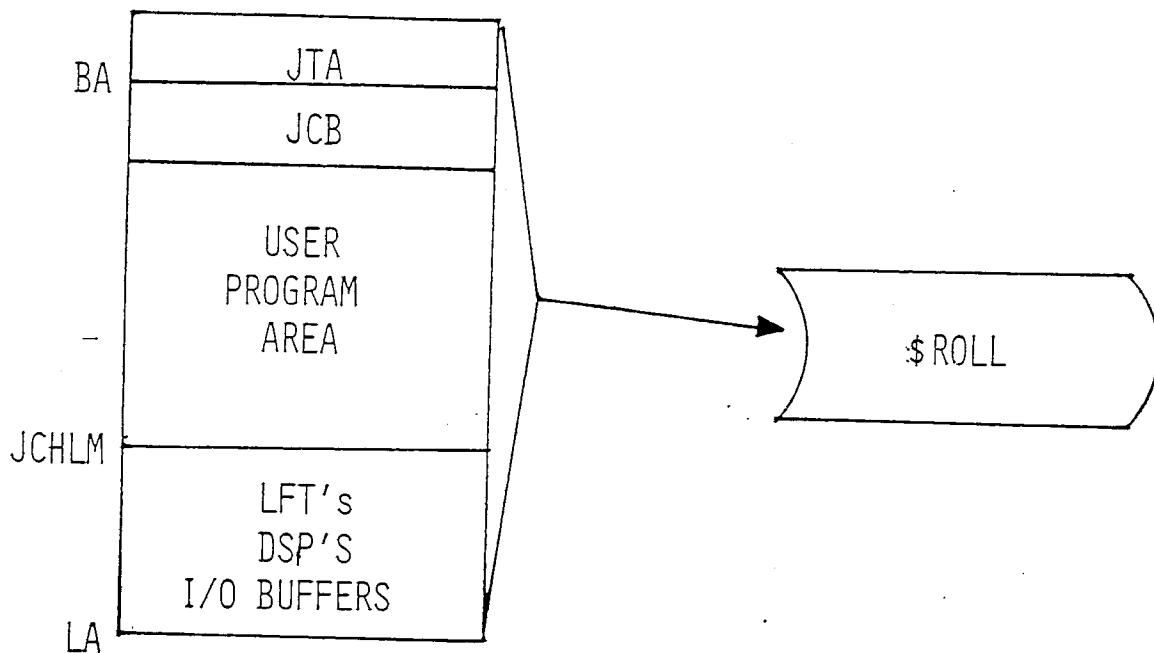
First fit method - allocates memory by block beginning at lower memory

Moves jobs if required, beginning at high end of memory

Encourages large blocks of free memory at the high end of memory

JOB RECOVERY

The entire user area (JTA-LA) is rolled to the disk.



There must be an image of a job on \$ROLL in order for the startup routine to recover a job.

JSH TABLES

SDT - System Dataset Table

An SDT entry is created in System Task Processor (STP) resident memory for each dataset that is spooled to or from a front-end system, or submitted for execution by an executing job.

For datasets that are submitted as jobs to the CRAY, the first control statement (the JOB statement) must be cracked to obtain job scheduling information.

JXT - Job Execution Table

The JXT contains current status of job, its location in memory or if rolled and the current priorities of the job.

RJI - Rolled Job Index Table

The RJI table contains entries for each defined JXT entry describing the job assigned to the JXT entry and controlling the recovery of jobs from mass storage entires.

Entry zero issued mainly to validate the roll index dataset.

Other entires indicate which JXT entries have active jobs, and hold information used to locate roll images.

MST - Memory Segment Table

The MST in STP memory contains a 1-word entry for each segment of memory that has been allocated by the Job Scheduler plus additional entries that describe free segments.

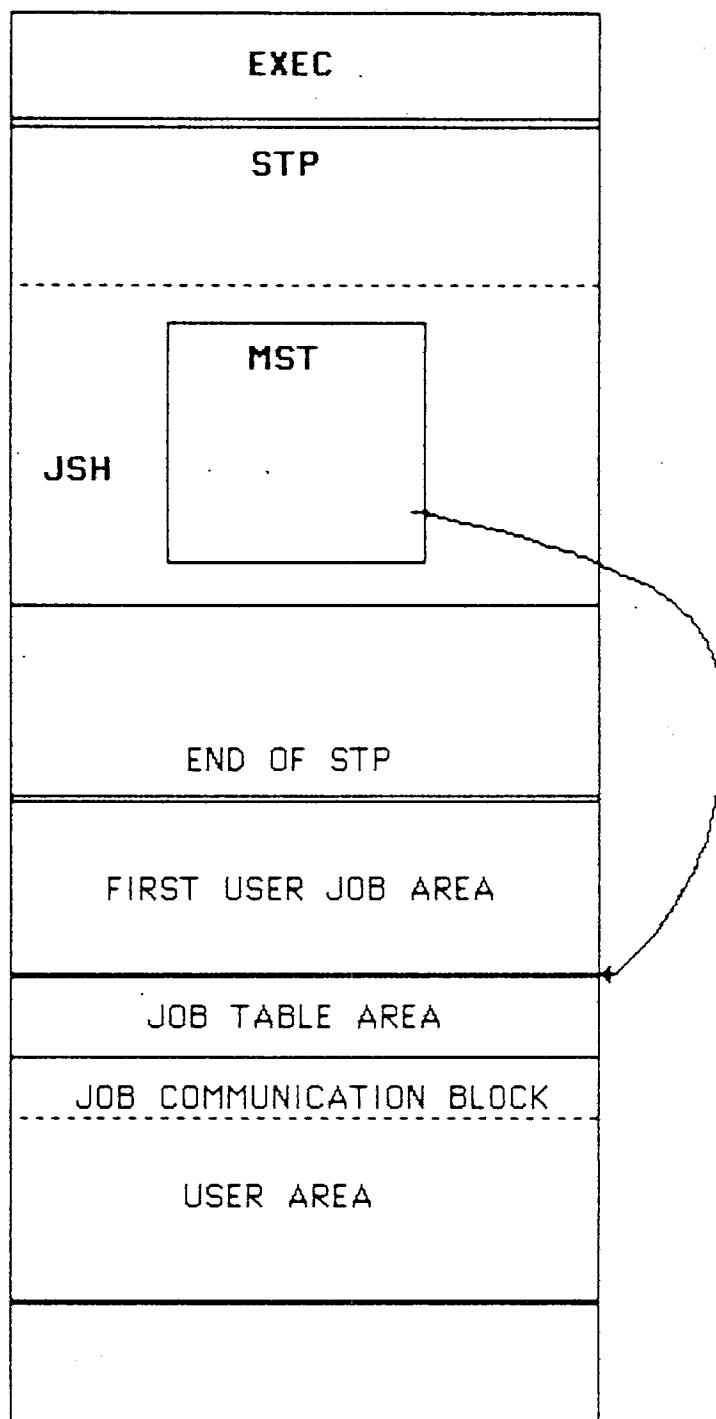
MST entries are stored in ascending order according to the beginning address of the segment (MSADDR).

Any free space between two allocated segments is consolidated and is represented by a single entry stored in the MST between entries for the two allocated segments.

The last entry in the table is always followed by a zero word.

To provide for the case where every allocated segment is surrounded by free segments, the MST must have twice as many words in it as the maximum number of allocated segments, plus two more.

MEMORY SEGMENT TABLE



USER EXCHANGER PROCESSOR (EXP)

Processes all user normal action requests

Processes all user error exits

Processes JSH requests to initiate or abort a user job

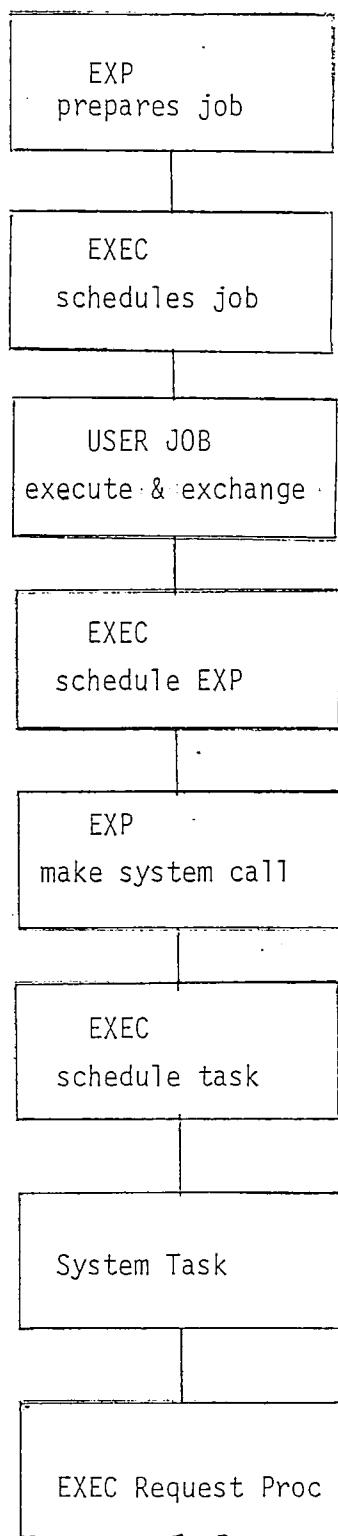
All requests made through the exchange processor request word (JTEP) in a user JTA. JTEP is word 67₈

Copies CSP into user field

Request octal call goes to S0 and then the user executes an Exit

See SM-40, page 8.3 for System Requests

User Exchange Processor



USER AND CSP

Not a task but a routine copied into user areas.

A system program that executes in the user field.

When a job is submitted, JSH calls EXP to copy CSP in User Area BA+200

Makes system requests through a normal exchange.

EXP then processes the exchange and makes the requests to EXEC.

CSP initiates the Job and cracks the JCL statements, processes errors and terminates the job

Uses DSP,LFT and JCB

Makes \$IN, \$OUT, \$CS and \$LOG

Under all the same restrictions as the user

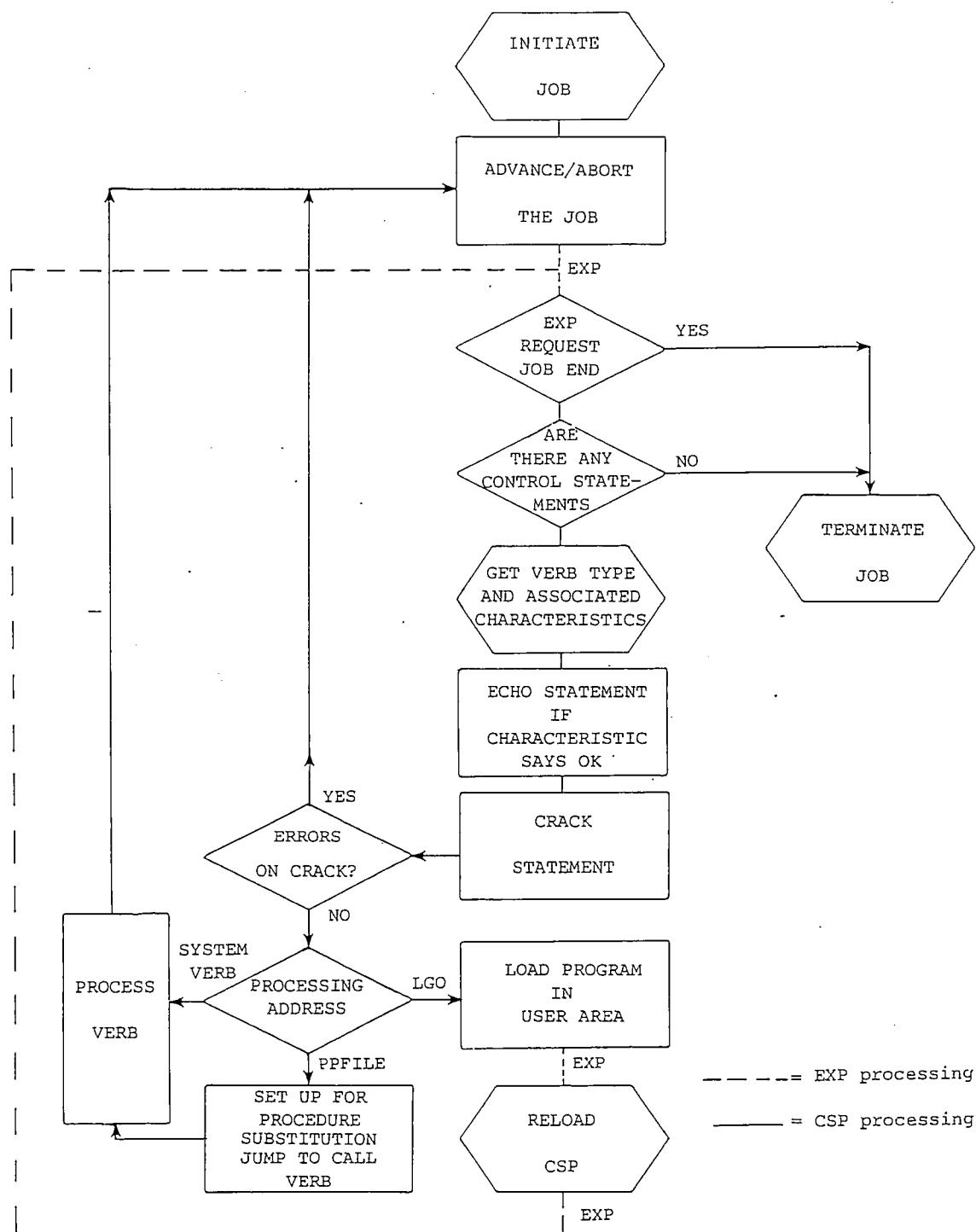
Passes messages to \$LOG

A copy of CSP resides on disk or after STP

Initiate the job:

1. Copy system bulletin to logfile if it exists and is required by the installation.
2. Enter system header into logfile.
3. Process JOB statement, ending job if any errors encountered. Skip this step if job is interactive.
4. If CSP received recovery station:
 - a. Issue corresponding message to logfile.
 - b. Terminate job if status indicates to do so.
5. Validate the structure of \$CS if the job is not interactive; terminate the job if any errors found.
6. Assign the datasets \$IN and \$OUT.
7. Jump to job advancement.

CSP FLOW



USER AREAS

The user area of the program is a contiguous area of memory. The system appends a Job Table Area (JTA) to the user's BA-LA. Users may access any address between their BA-LA but cannot access out of the area thus making the JTA area unaccessible to a user job, except through system calls.

The JTA contains job related information such as accounting data, a user's \$LOG buffer, XP area, TCBs etc; just about anything you would want to know about a job but the coding itself. The JTA is a dynamic area of memory and may expand and contract.

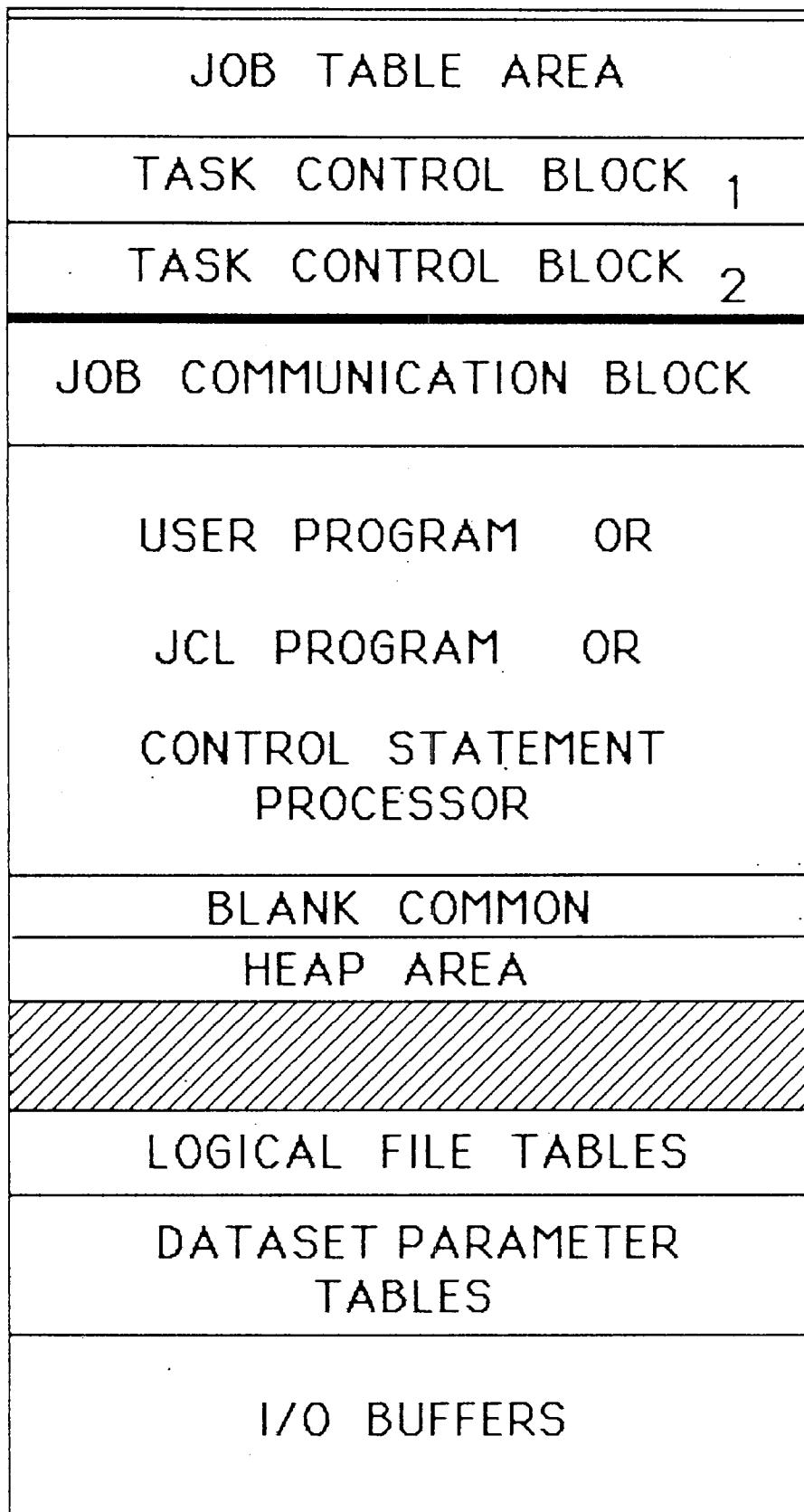
Next in the user area is the job communication block of 128 decimal words. This area is the operating system/job communication area. It contains I/O table pointers, the latest control statement image, COS revision level, etc; the Control Statement Processor (CSP) uses this area for the interpretation of control statements.

Next is the user code/data area. CSP, utilities, compilers and other programs are loaded into this area. Then comes the Heap area that is used when multitasking. The length of the user area is determined by the contents of JCB word HLM. JCHLM is the last address of user code. If a user expands or contracts memory it is at this word in memory.

The area immediately following the user program area is reserved for I/O tables used by the system.

The remainder of the user area (to LA) is used for user I/O dataset buffers. Each buffer is at least 512 words (Block) or multiples thereof. The number of buffers is adjustable by the user.

USER AREA



ACCESS A PERMANENT DATASET

A job makes a request for permission to use a permanent dataset.

The Exchange Package Processor (EXP) task examines the request for valid parameters and issues a request to the Permanent Dataset Manager (PDM) task for access and the Job Scheduler (JSH) task for job suspension.

The Permanent Dataset Manager (PDM) task validates access to the selected dataset for the user job.

PDM manages the necessary system tables so that the user has the desired permissions granted.

PDM creates a Permanent Dataset Table (PDS) entry for the active dataset.

PDM creates a Dataset Name Table (DNT) entry for the user and insures the Dataset Allocation Table (DAT) for the dataset is made resident in the requesting job area thus making the dataset local for that job.

The Job Scheduler (JSH) task sets the job's status bit to SUSPK thus disallowing the job from roll-out and memory movement until the job's Dataset Allocation Table entry is read into the user area from the Disk Dataset Catalog (DSC).

JSH resets the job's status to the wait state following completion of generation of the local dataset by the Permanent Dataset Manager (PDM) task.

DATASET ALLOCATION

DAT - Dataset Allocation Table

A Dataset Allocation Table defines the mass storage logical location of a dataset by specifying the logical devices and the portions that are used in each device.

There is one DAT for each active dataset in the system.

If the dataset is permanent, the DAT is entered in the catalog and can be used by more than one user.

The DAT is composed of as many 16-word pages as necessary to represent the mass storage occupied by the dataset.

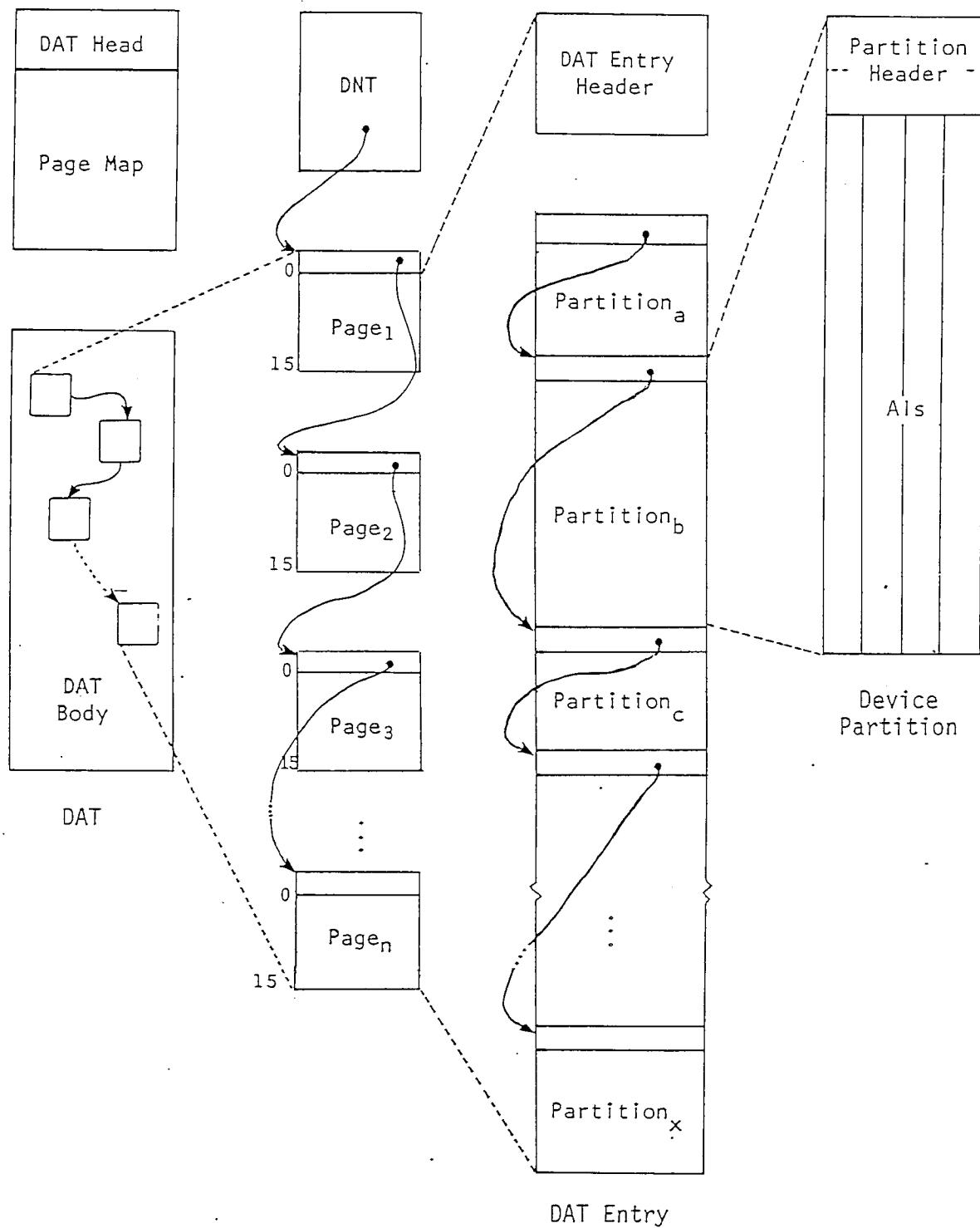
The first page includes a page header, an entry header, at least one partition header, and the first 11 words of the table, which is divided into partitions.

A partition represents a portion of the dataset on a single logical device.

Each page after the first includes a page header and can include partition headers to begin new partitions.

The table consists of a header and a bit map.

DATASET ALLOCATION TABLE



LOCAL I/O

A job issues a write request through a system macro causing physical I/O.

The Exchange Package Processor (EXP) task examines the request for valid parameters and forwards the request for I/O.

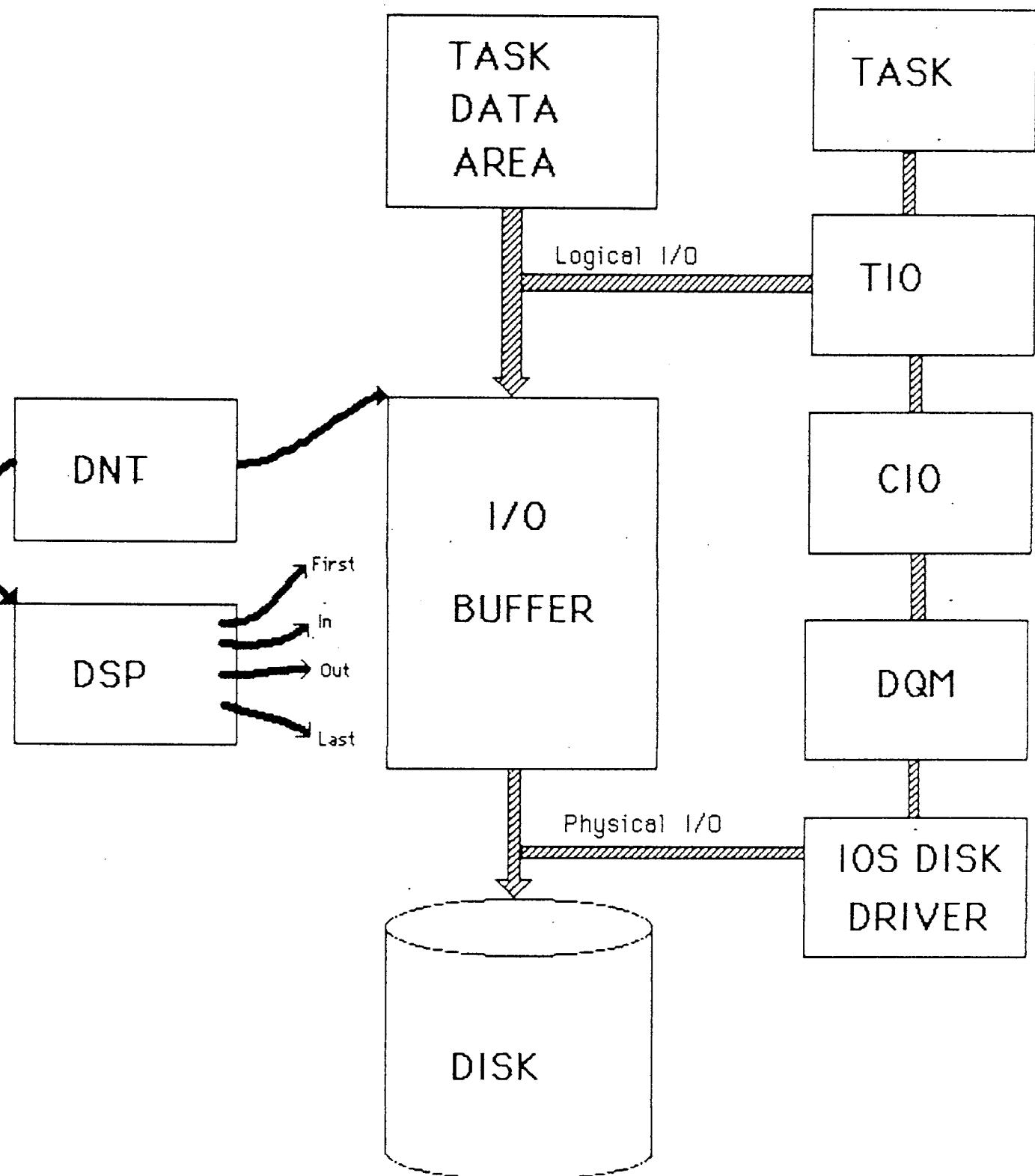
The Task I/O (TIO) routines transfer data, inserting RCWs where needed, from the job data area to the job disk buffers on a write.

TIO determines the disk buffers are greater than half full and calls Circular I/O (CIO).

Circular I/O (CIO) issues a write request of the Disk Queue Manager (DQM) task and has the job suspended by the Job Scheduler (JSH).

The Disk Queue Manager (DQM) task allocates disk on a write if needed.

I/O OVERVIEW



TASK I/O ROUTINES

Task I/O (TIO) is a set of reentrant common routines in STP logically considered part of any system task that calls it.

TIO interprets only COS blocked format and therefore, only operates on blocked datasets.

It allows a systems programmer to do logical I/O at the system task level without being concerned about physical I/O.

The following COS system tasks call TIO:

Exchange Processor (EXP)

Startup

Log Manager (MSG)

The logical I/O may be performed on either a dataset related to the system or a user task related dataset.

TIO does not allocate or deallocate any of the control structures or buffers for the request, but assumes all control structures and buffers are set up correctly before the request by the system task.

CIRCULAR I/O ROUTINES (CIO)

Physical I/O on a dataset uses a circular buffering technique initiated by a set of STP common routines known as CIO.

CIO routines are directly callable from system tasks.

The following system tasks directly call CIO within COS:

Exchange Processor (EXP)

Log Manager (MSG)

Permanent Dataset Manager (PDM)

CIO calls either the:

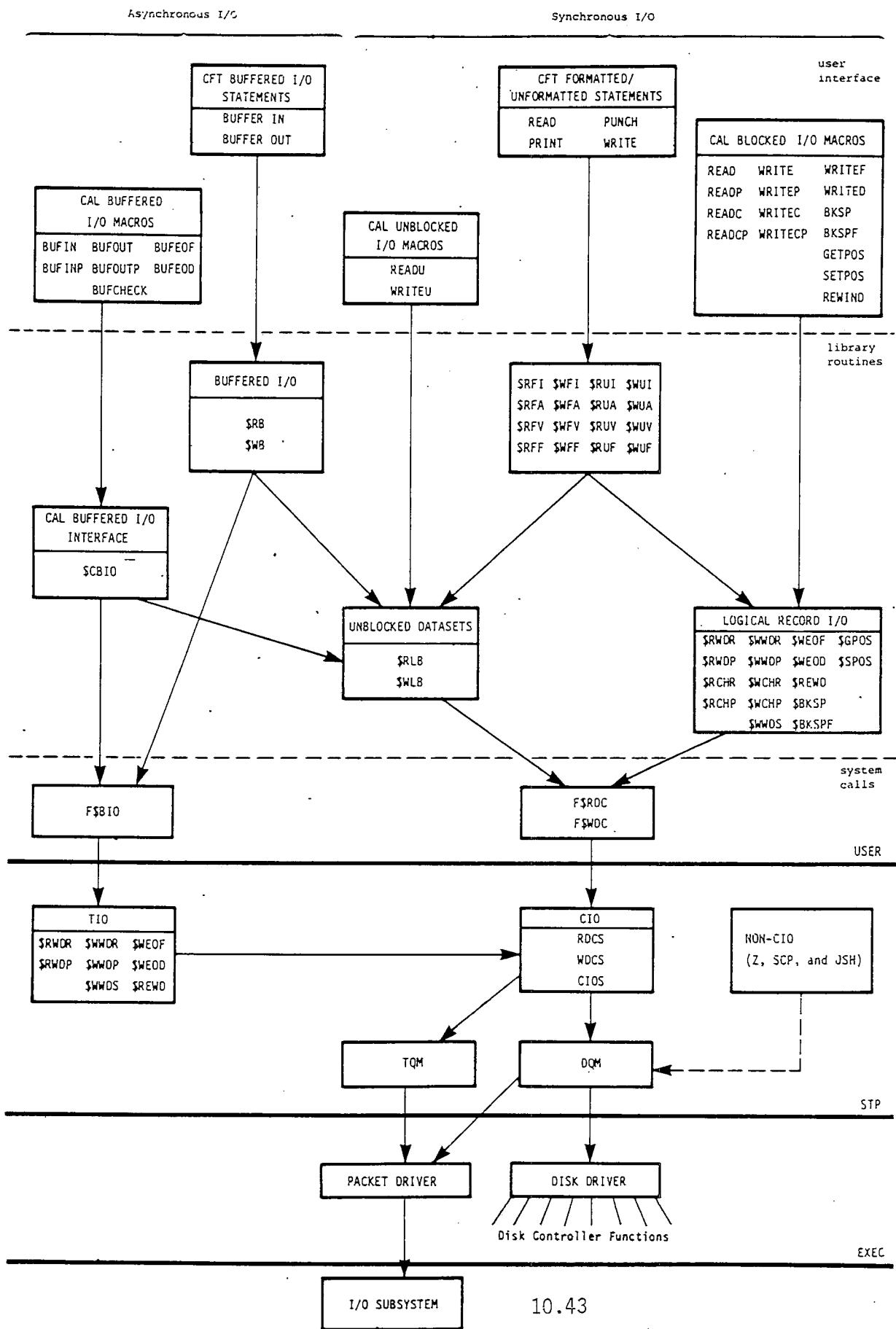
Disk Queue Manager (DQM) or the

Tape Queue Manager (TQM)

to perform physical sector transfers. These calls occur through intertask communication (PUTREQ) from CIO.

These calls are issued by user programs or tasks when data is to be transferred between the I/O buffer defined by the DSP and mass storage.

SYSTEM I/O CALLS



JOB TERMINATION

A job terminates with an exit control statement or EOF interpreted by the Control Statement Processor (CSP).

CSP posts termination messages to the logfiles via the Message (MSG) task.

The Exchange Package Processor (EXP) task copies the jobs \$LOG and \$OUT.

EXP changes \$LOG to scratch and releases scratch datasets as well as \$IN, \$CS.

The Permanent Dataset Manager (PDM) creates a Dataset Catalog Entry (DSC) for each job output dataset thus making the dataset permanent.

EXP has created entries for the job's output datasets in the SDT.

The Disk Queue Manager (DQM) task allocates disk for \$OUT if needed, and has the buffer written to disk via the disk driver.

The Job Scheduler (JSH) task terminates the job by releasing the executing queue entry in the Job Execution Table (JXT).

JSH releases the job's memory and releases the job's dataset and updates the Memory Segment Table (MST).

The Station Call Processor (SCP) task manages system memory buffers and transmits the job's output datasets to the front-end via the front-end driver.

Upon receipt of each output dataset by the front-end, the Station Call Processor (SCP) requests the Permanent Dataset (PDM) to delete the Dataset Catalog (DSC) entry for the dataset and DQM releases the dataset's disk space.

\$LOG

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Dump Exercises

11

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Exercise 1 EXEC & KERNEL Monitor Flow

Skill: To have some familiarity with the COS/IOS monitors

Tasks:

COS

- a. Flowchart what occurs in EXEC on an interrupt starting at EN
(four pages of code only)

IOS

- a. Flowchart what occurs in the KERNEL starting at address
INTERRUPT (two pages of code only)

Resources:

SM-0040	
SM-0046	
EXEC listing	pages 116 to 119
KERNEL listing	pages 112 to 113

Related Reading:

SM-0040	pages 2-1 to 2-6
---------	------------------

SM-0046	pages 2-1 to 2-15
---------	-------------------

Intended Lesson Results: To be able to go into the monitors and know the flow through the monitor on an interrupt and how to find your way around the monitor listings

Exercise 8: KERNEL Mailer Flow

sk11_topesa20091stml1sl1f1AWiththeCCSA102mentions

3x25 T

300

6.3. Employees must receive an EEOC no later than the date of hire (or as soon as feasible).

30

INTERBUT (two sides of card only)

3907029

0200-M2

GAO-M2

EXCELENT

211 of 211 pages

Revised Readiness

3-5 of 1-5 22000

OP-90-H2

21-8 at 1-8 2005

2000-112

the following section. Results will be shown to do justice to the importance of the two factors involved in the movement of wolf populations.

Exercise 2 TASKS AND OVERLAYS

Skill: To have some familiarity with the system programs

Tasks:

COS

- a. List the Functions performed by PDM
- b. What table is the PDM function request code passed in?

IOS

- a. List the Functions performed by ACOM
- b. When is ACOM used and by whom?

Resources:

SM-0040
SM-0046
PDM listing
ACOM listing
KERNEL listing
\$APTEXT (optional)
Classroom set of COS and IOS listings

Related Reading:

SM-0040 pages 10-1 to 10-3

SM-0046 pages 3-1 to 3-8

Intended Lesson Results: To be able to locate a system program listing and determine what the program does and who uses it when.

Exercise 5 TAKS AND OVERLAYS

SPILL TO PIAVE SOME LATERALLY WITH THE SAME DRAINS

Tasks

COS

P. MULI CSD 18 THE POM FUNCTIONAL OVERLAYS

20.

P. MULI 18 ACOU AREA AND THE ACOU

REASSEMBLY

SM-0040

SM-0042

POM 11211d

ACOU 11211d

KERNEEL 11211d

SAPTEX (GELONIS)

CLASSROOM SET OF COS AND 103 JEWELS

REASSEMBLING

E-01 1-01 10-1 10-2
09060 09060 09060

8-8 10 1-1 10 3-8
09060 09060 09060

INTERLEAVED PERSONAL READING TO SP/16 10 100016 A DAY WITH THE SAME DRAINS
AND DRAINS. NAME THE DRILLS AND 09060 09060 09060 09060 09060 09060 09060 09060 09060 09060

Exercise 3 Tasks and Activities

Tasks: COS

- a. List all the Tasks in the STT in sequential order
- b. Give their priority
- c. Give their task ID number
- d. Give the task exchange package address

Tasks: IOS

- a. List all the Activities in the EACT+1 chain in sequential order
- b. Give each activities priority
- c. Give each activities storage module address

Resources:

COS DUMP
IOS DUMP
SM-045
SM-0007
\$ULTLTXT (optional)
\$APTEXT (optional)

Related Reading:

SM-0040	pages	2-1 to 2-14
SM-0046	pages	2-6 to 2-8, 2-11, A1 to A4
SM-0045	pages	470 to 472
SM-0007	page	2-1
\$APTEXT	page	89 (optional)
\$UTLXTT	page	323 (optional)

Intended Lesson Results: To be able to identify the tasks and activites in the system and at what address their executing enviroments are.

Exercise 2 Test and Activities

Test 200

- a. Print all the tasks in the ECT in sequential order
- b. Give them priority
- c. Give them task ID number
- d. Give specific sequence backlog address

Test 100

- a. Print all the activities in the ECT + giving in sequential order
- b. Give each activity priority
- c. Give each activities to the mobile phone

Resources

200 DMS	
100 DMS	
SM-025	
SM-000	
TXT/TXT (optional)	2APTEX
2APTEX (optional)	

Rejected Reasons

PA of 1A, 11-S, 8-S 0-8-S	1-1 0-3-14	badges	0A00-M2
SM-025	8-S 0-8-S	badges	0A00-M2
SM-000	1-1 0-3-14	badges	0A00-M2
TXT/TXT (optional)	8-S	badges	0000-M2
2APTEX (optional)	1-1 0-3-14	badges	2APTEX

Introducing Reason Reasons: To help you to identify the tasks and get a better understanding about what happens when you implement it.

Exercise 4 System Memory Maps

Skill: To be able to find the address that programs are executing.

Tasks:

COS

- a. At what address do the EXEC tables end?
- b. At what address does STP begin?
- c. At what STP relative addresses are the tasks loaded?
- d. At what address does STP end?

IOS

- a. At what address do the Kernel tables end?
- b. At what address does the Overlay memory chain begin?
- c. At what address does the DAL chain begin?
- d. At what address does the free memory chain begin?
- e. At what address does the IO buffer chain begin?

Resources:

EXEC Listing
KERNEL Listing
COS Dump
IOS Dump
STPTABLES

Related Reading:

SM-0040	pages	1-6 to 1-17
SM-0046	pages	2-1 to 2-3
STPTABLES	pages	20 to 25

Intended Lesson Results: To be able to determine where to find address pointers for relative programs and have some perspective on how Cray system memories are utilized

Exercise 4 Safety Moment 1980s

Skills To be able to link the source files together as executable

Steps

200

- 1. AT Wiper square does STP only
- 2. AT Wiper square does STP only
- 3. AT Wiper square does STP only
- 4. AT Wiper square does STP only
- 5. AT Wiper square does STP only

201

- 1. AT Wiper square does STP only
- 2. AT Wiper square does STP only
- 3. AT Wiper square does STP only
- 4. AT Wiper square does STP only
- 5. AT Wiper square does STP only

Answers

STPAGES
102 Dump
C02 Dump
KERMEL Dump
EXEC Dump

Released rapidly

STPAGES	1-0 10 1	1-0 10 2	2-M-0040
STPAGES	2-1 10 2	2-1 10 3	2-M-0040
STPAGES	3-1 10 2	3-1 10 3	STPAGES

Useful for logical file structure. To be able to determine where to find specific
information for each file. To be able to determine where to find specific
information for each file.

Each file will have a file number.

EXERCISE 5 LCP and SCB Sequence Flow

Skill: To be able to breakdown a SCP-Station LCP event.

Tasks:

- a. Fill in the needed LCP fields for the following events
 1. RESTART the station after COS has gone down
 2. LOGON the station to COS
 3. Send a 512-word Job to the Cray input Queue
 4. Go to IDLE SCB's

Resources:

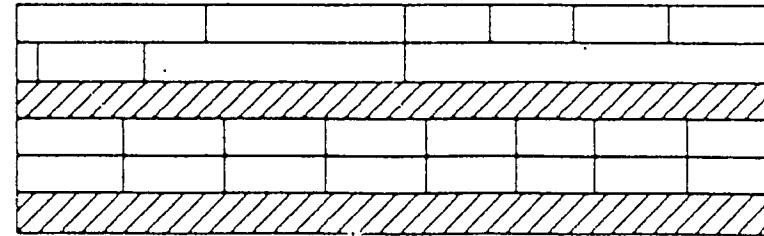
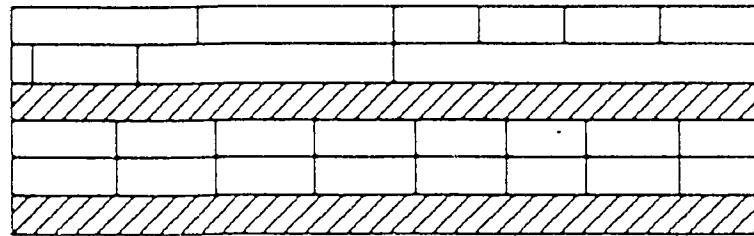
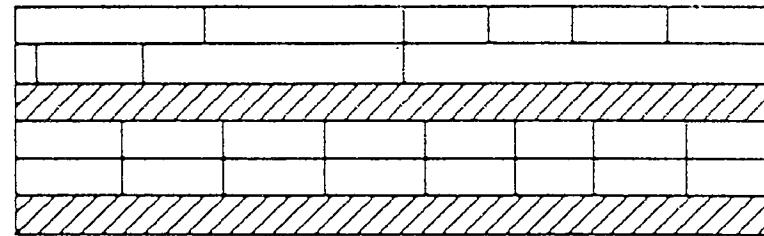
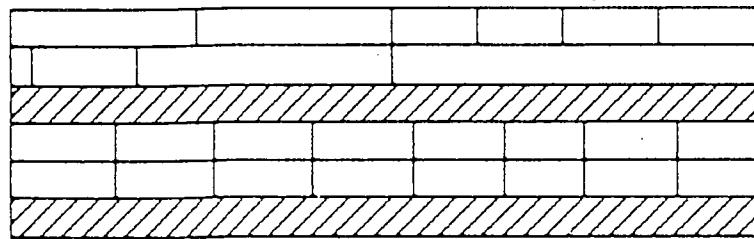
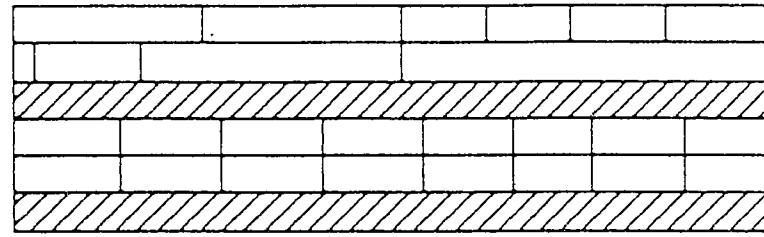
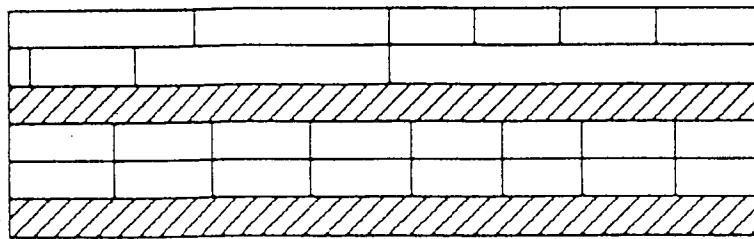
SM-0042
SM-0040
SM-0045
\$APTEXT

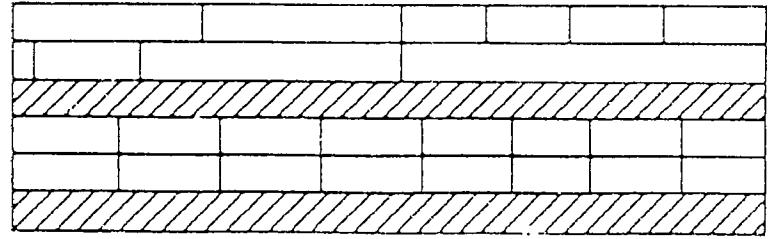
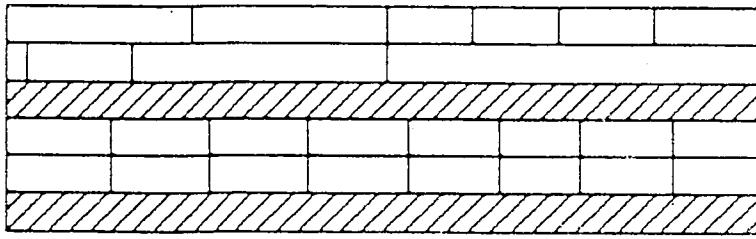
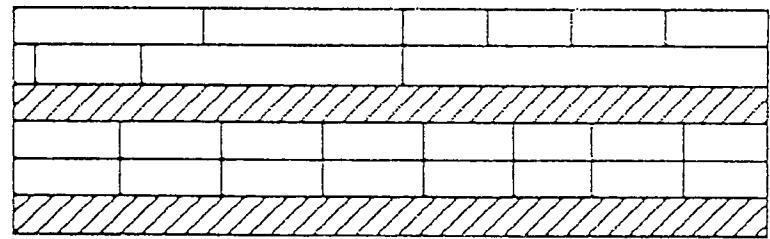
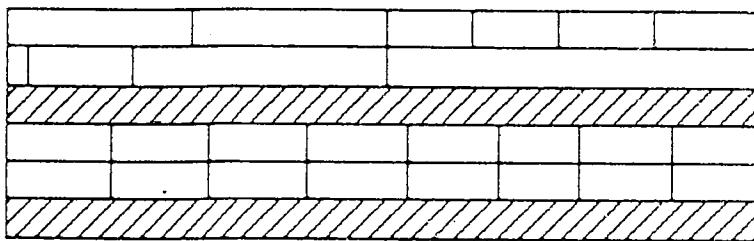
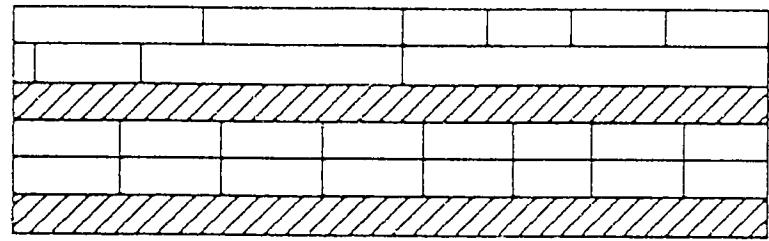
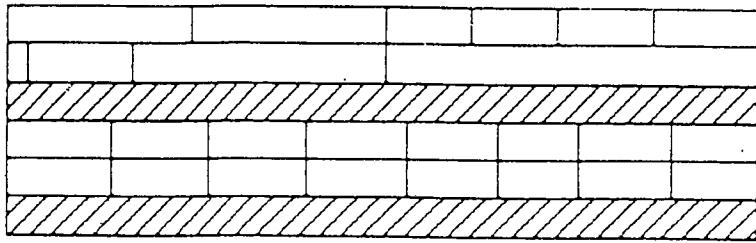
Related Reading:

SM-0042	pages	3-1 to 3-4, 4-10 to 4-15
SM-0040	pages	7-1 to 7-8
SM-0046	pages	6-20 to 6-22 (optional)
\$APTEXT	pages	140 to 157 (optional)

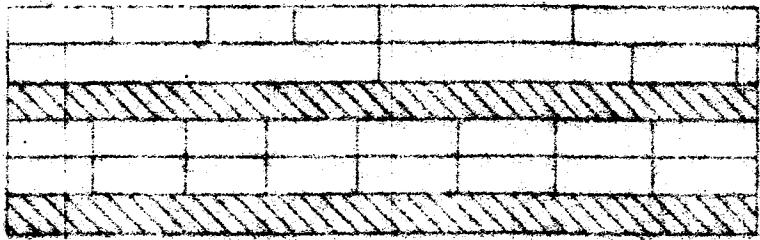
Intended Lesson Results: To familiarize yourself with the SM-42 Protocol definitions and understand the software protocol necessary to transfer a job dataset over the station link.

PS. LCP's are recorded in the History Trace Buffer

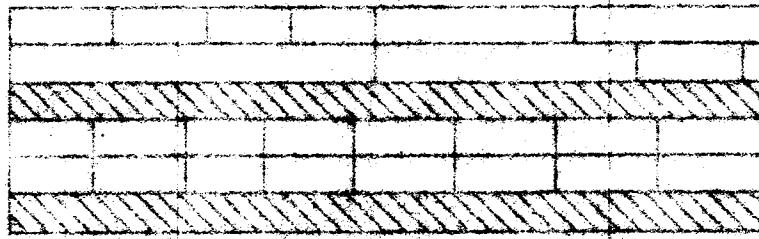




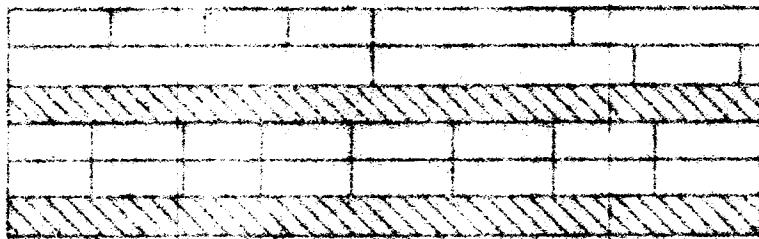
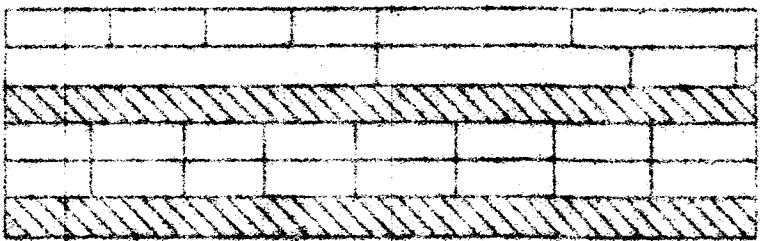
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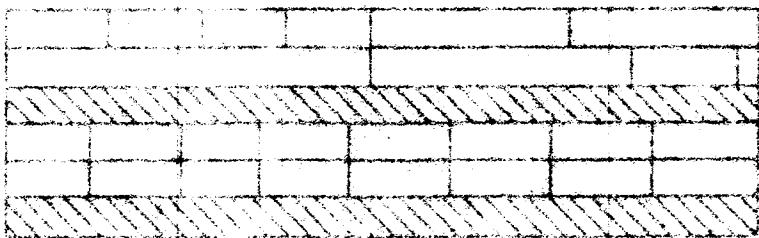
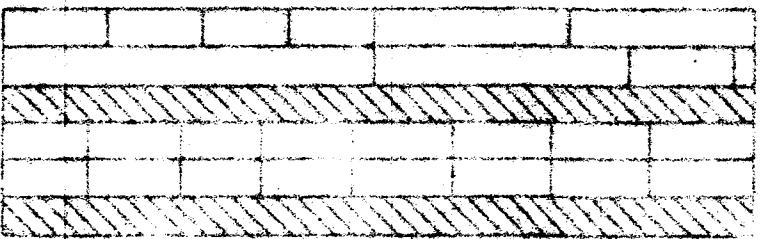
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Exercise 6 COS Dump Analysis

Skill: To analyze a COS dump for Failure

Tasks:

- a. Locate the Stop buffer
- b. Locate the STP hang message if an STP hang
- c. Locate the STOP macro
- d. Locate the ERR macro if an STP hang
- e. Locate the active Task
- f. Locate the active task exchange package
- g. Analyze the EXEC history trace events
- h. Analyze the ITCT intertask communication history trace events

Resources:

SM-0040
SM-0045
COS Listings
COS Dump

Related Reading:

SM-0040 pages 2-74 to 2-95

Intended Lesson Results: To be able to find out which program halted COS and study the events that lead up to the halt and why it halted.

CCE Groups Analysis

Exercise 6

SWINN: To analyse a CCE group for failure

Task

- a) Focus on the S16 parallel
- b) Focus on the S16 used measure it as S16 input
- c) Focus on the S16 macro
- d) Focus on the EGR macro it is S16 input
- e) Focus on the active Task
- f) Focus on the active exchange blocks
- g) Analyse the EBC interface tasks events
- h) Analyse the LTC interface communication interface tasks events

Resources

SM-0040
SM-0040
CCE Plugins
CCE Dumb

Refined Building

SM-0040 5988 5-14 at 5-38

Unfinished Feature Review: To be able to link out which feature failed CCE and study the events first and up to the first one may be failed

Exercise 7 IOS Dump Analysis

Skill: To Analyze an IOS dump for failure

Tasks:

- a. Locate the hang message in the punted IOP
- b. Locate the PUNTIF macro
- c. Analyze the history trace table events
- d. Locate the active overlay number, name and activity descriptor
- e. Locate the active SMOD (ESMD)
- f. Locate the relative P register of the active programs SMOD
- g. Determine what operand registers have useful overlay parameter registers

Resources:

IOS Dump
IOS Listings
SM-0046
SM-0007

Related Reading:

SM-0046 pages 11-4 to 11-17
 Appendix A

Intended Lesson Results: To be able to find out which program halted IOS and the events leading to the halt and why it halted.

**Exelotex 5
102 Dump Analysis**

SKILL TO ANALYSE AS 102 dump lot 15/11/96

Target

- a. Process the visual dump sample in the burnpot 102**
- b. Process the PTFE 102 sample**
- c. Assess the visual sample results**
- d. Assess the scoria over/underburner, name and scoria description**
- e. Assess the scoria 2M00 (GMS)**
- f. Assess the visual 102 residue to the scoria breakdown 2M00**
- g. Determine what colour residue uses visually over/underburner**

Procedure

**102 Dump
102 Teflide
84-00-12
2M-0007**

Initial Results

5-11-96 A-11 11-09-96 84-00-12

Appendix A

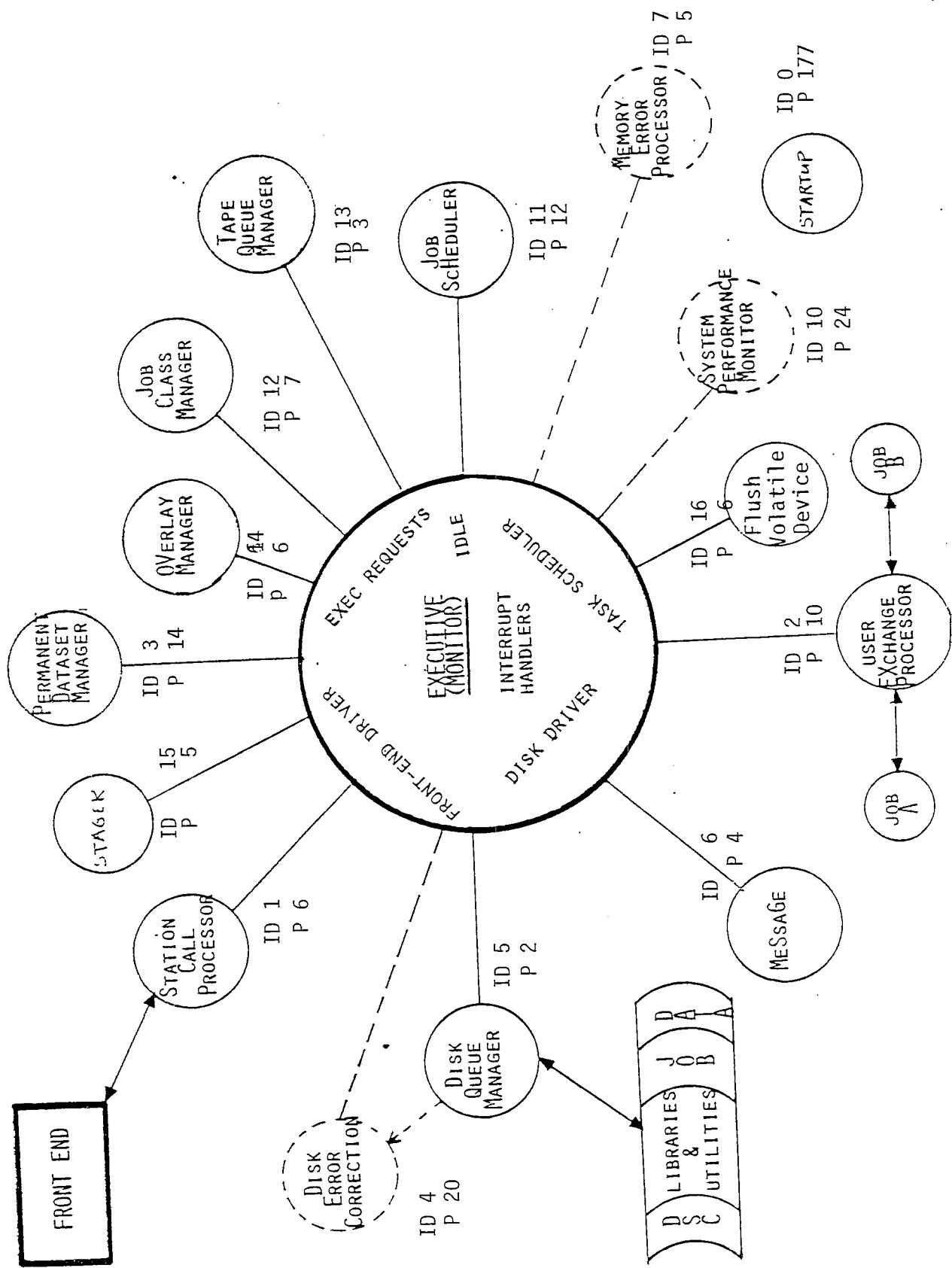
**Initial Results:
To be sure of this one will do a better analysis
and this analysis is based on the first one with**

Appendix A – Quick References

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OVERLAY DESCRIPTIONS

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This appendix describes the overlays used by I/O Subsystem software. The list is alphabetical according to overlay name. Each overlay has one or more of the following type codes.

Type	Description
D	Data overlays. These overlays contain data and cannot be executed.
K	Kernel overlays
F	File system overlays associated with the I/O Subsystem Editor. See Appendix F of the I/O Subsystem (IOS) Operator's Guide, CRI publication SG-0051, for a description of the capabilities of the Editor.
C	Concentrator overlays
S	Station overlays
I	Interactive station overlays
B	Block multiplexer overlays. These overlays drive the block multiplexer channels.
T	Tape overlays
R	System dump and restart overlays
Ns	NSC (Network Systems Corporation) channel concentrator overlays

KERNEL HALT (PUNT) CODES

CODE MEANING

000 No error code specified on \$PUNTIF macro
001 Local memory error (always HARDWARE)
002 Buffer memory error on deadstart (always HARDWARE)
003 Buffer memory error (always HARDWARE)
004 High-speed channel error (always HARDWARE)
005 Invalid message received from CPU
006 Invalid parameter in disk request from CPU
007 Program was executing at location Ø
010 Local memory location Ø was overwritten
011 Undefined message received on IOP communication channel
012 Overlay does not exist
013 Station stack overflow or underflow
014 Local memory buffer not available
015 Buffer memory disk buffer not available
016 Invalid local buffer release call
017 Buffer memory incorrectly configured
020 IOP message channels incorrectly configured
021 SMOD is too large for area on buffer memory
022 Invalid local memory address
023 Illegal interrupt program sequence code
024 Stop request received from CPU
025 Low-speed channel error (always HARDWARE)
026 Block number validation trap
040 Block Mux interrupt processor error
041 Bad CRW address in device table
042 Block mux start I/O error
044 Block Mux configuration error
050 DD49 disk software error
051 The debugger was not loaded
052 Bad buffer memory allocation request
053 Bad local memory address on hi speed I/O call
054 Invalid I/O length specified
055 No hi speed channel configured for request
056 Illegal activity activation requested
057 Buffer memory DAL queue exhausted
060 Illegal Demon call
061 Undefined kernel service request
062 Illegal kernel service request
063 Bad kernel service request parameter
064 Requested device not configured
065 Illegal IOP requested on kernel service request
066 Requested queue full
067 Illegal I/O address specified
070 SMOD error
071 Local memory space exhausted
072 Illegal overlay load requested
073 Corrupted local memory chain
074 Bad local memory release
075 Bad I/O parameter
076 Unexpected interrupt received
077 Disk error
100 AMAP not available for system initialization
101 Illegal overlay number read during initialization
102 IOP initialization error
103 MOS configuration error
104 Overlay too large for loading
105 Premature tape EOF encountered
106 Exit stack fault
107 Illegal device type configured

No.	Ov1	Name	Size	BM-Upr	BM-Lwr	No.	Ov1	Name	Size	BM-Upr	BM-Lwr
ØØ	SDMPA		111Ø	Ø1	Ø21373	ØØ	Ø46Ø	Ø21615	Ø2	Ø21731	ØØ
Ø3	SDMPD		Ø24Ø	Ø1	Ø22023	Ø4	Ø154	Ø22073	Ø1	Ø22126	Ø1
Ø6	AMAP		Ø41Ø	ØØ	Ø2067Ø	Ø7	AMSG	174Ø	Ø5	Ø21362	ØØ
11	BLOCK		1534	ØØ	Ø21756	12	BMOL	461Ø	ØØ	Ø35014	ØØ
14	BTD		Ø254	ØØ	Ø22305	15	BTH	Ø374	ØØ	Ø22457	ØØ
17	BXSET		Ø34Ø	ØØ	Ø22524	2Ø	CALL	2Ø1Ø	ØØ	Ø23216	ØØ
22	CHNST		Ø774	ØØ	Ø23747	23	CLKSNC	1Ø1Ø	ØØ	Ø2435Ø	ØØ
25	CONFIG		1334	ØØ	Ø24573	26	CPOL	2474	ØØ	Ø25062	ØØ
3Ø	CPTEST		237Ø	ØØ	Ø25142	31	CRAY	133Ø	ØØ	Ø31466	ØØ
33	DISKIO		1Ø24	ØØ	Ø2656Ø	34	DIVIDE	Ø444	ØØ	Ø32542	ØØ
36	DKIOEX		Ø43Ø	ØØ	Ø3ØØ12	37	DKLOOK	Ø1ØØ	ØØ	Ø37311	ØØ
41	DKSETØ		Ø26Ø	ØØ	Ø3Ø22Ø	42	DOM	Ø23Ø	ØØ	Ø37644	ØØ
44	DOMP		ØØ	ØØ	Ø3Ø663	45	DTB	Ø24Ø	ØØ	Ø41320	ØØ
47	ERR		16Ø4	ØØ	Ø31656	5Ø	ERRDMP	1514	ØØ	Ø42744	ØØ
52	F80ME		21Ø4	ØØ	Ø356ØØ4	53	FDMPDR	Ø52Ø	ØØ	Ø53571	ØØ
55	GETEXT		Ø164	ØØ	Ø3737Ø	56	HDRPAG	Ø7Ø4	ØØ	Ø6655Ø	ØØ
6Ø	HSPTES		Ø214	ØØ	Ø4ØØ15	61	INFORM	Ø2ØØ	ØØ	Ø6652Ø	ØØ
63	INDD39		21ØØ	ØØ	Ø41663	64	INDD49	2164	ØØ	Ø774Ø	ØØ
66	LSTP		27ØØ	ØØ	Ø43114	67	LPT	156Ø	ØØ	Ø32217	ØØ
71	MFINIT		233Ø	ØØ	Ø43674	72	MOSTES	1524	ØØ	Ø3644	ØØ
74	MULTIPLY		64	ØØ	Ø456Ø4	75	NOBEAT	Ø4ØØ	ØØ	Ø45721	ØØ
77	OTB		Ø15Ø	ØØ	Ø456Ø1	78	OUTCALL	Ø1ØØ	ØØ	Ø54572	ØØ
81Ø2	PATCH		Ø64Ø	ØØ	Ø5142Ø	81Ø3	PLOTIT	Ø6Ø4	ØØ	Ø51731	ØØ
Ø1Ø5	SCRUB		Ø17Ø	ØØ	Ø52212	Ø1Ø6	SETIME	1474	ØØ	Ø52567	ØØ
Ø11Ø	STARTØ		Ø534	ØØ	Ø53014	Ø111	START1	Ø314	ØØ	Ø53226	ØØ
Ø113	START3		Ø66Ø	ØØ	Ø53415	Ø114	START4	Ø33Ø	ØØ	Ø53657	ØØ
Ø116	STOP		Ø25Ø	ØØ	Ø54Ø22	Ø117	SYSS	Ø56Ø	ØØ	Ø54572	ØØ
Ø121	TCOM		Ø23Ø	ØØ	Ø55262	Ø122	TDUMP	1514	ØØ	Ø55653	ØØ
Ø124	TRACE		133Ø	ØØ	Ø5612Ø	Ø125	TRACK	Ø164	ØØ	Ø55643	ØØ
Ø127	UBTAPE		150Ø	ØØ	Ø56535	Ø13Ø	UNBLK	Ø73Ø	ØØ	Ø57243	ØØ
Ø132	WATCH		Ø23Ø	ØØ	Ø57274	Ø133	X CLOCK	172Ø	ØØ	Ø57721	ØØ
Ø135	XDISK		Ø754	ØØ	Ø5755	Ø136	XDISKA	1454	ØØ	Ø60463	ØØ
Ø14Ø	XDK		Ø23Ø	ØØ	Ø61557	Ø141	XMT	122Ø	ØØ	Ø62227	ØØ
Ø143	XPR		Ø52Ø	ØØ	Ø62466	Ø144	XPRINT	Ø774	ØØ	Ø63011	ØØ
Ø146	XPRNTB		Ø65Ø	ØØ	Ø63205	Ø147	X TAPE	Ø75Ø	ØØ	Ø63551	ØØ
Ø151	XTAPEB		132Ø	ØØ	Ø63712	Ø152	XTAPEC	Ø61Ø	ØØ	Ø64340	ØØ
Ø157	ACOM		21ØØ	ØØ	Ø64511	Ø155	BADDAT	Ø26Ø	ØØ	Ø65205	ØØ
Ø157	D3ERR		255Ø	ØØ	Ø653Ø3	Ø16Ø	D3IOR	2454	ØØ	Ø6655Ø	ØØ
Ø162	D3MSG		Ø46Ø	ØØ	Ø66753	Ø163	D3RLR	Ø7ØØ	ØØ	Ø67247	ØØ
Ø165	D3SLR		114Ø	ØØ	Ø67514	Ø166	D4DEM	Ø75Ø	ØØ	Ø67643	ØØ
Ø17Ø	D4ERR		254Ø	ØØ	Ø7Ø7Ø4	Ø171	D4IOR	275Ø	ØØ	Ø72226	ØØ
Ø173	D4MSG		Ø514	ØØ	Ø72424	Ø174	D4OVR	Ø61Ø	ØØ	Ø72711	ØØ
Ø176	D4RLR		Ø654	ØØ	Ø73Ø36	Ø177	D4SKR	1424	ØØ	Ø73516	ØØ
Ø2Ø1	DASTAT		Ø234	ØØ	Ø73766	Ø2Ø2	DD49	342Ø	ØØ	Ø74741	ØØ
Ø2Ø4	ERRECK		317Ø	ØØ	Ø75516	Ø2Ø5	FIRECODE	Ø414	ØØ	Ø76457	ØØ
Ø2Ø7	MEMIO		2Ø64	ØØ	Ø775Ø7	Ø21Ø	REPORT	Ø7674	ØØ	Ø80054	ØØ
Ø212	XDKERR		2Ø7Ø	ØØ	Ø6Ø711	Ø213	XDKFMT	114Ø	ØØ	Ø100367	ØØ
Ø215	BMXCON		324Ø	ØØ	ØØ553	Ø216	BMXCPU	2674	ØØ	Ø1022Ø2	ØØ
Ø22Ø	BMXOPE		Ø51Ø	ØØ	Ø1Ø3ØØ	Ø221	BMXS10	155Ø	ØØ	Ø103514	ØØ
Ø223	BCOM		173Ø	ØØ	Ø1Ø4Ø2	Ø224	BUFMAN	214Ø	ØØ	Ø105044	ØØ
Ø226	CONMAN		164Ø	ØØ	Ø1Ø4Ø2	Ø224	DSCGET	1055Ø	ØØ	Ø106015	ØØ
Ø231	TAPEIO		4ØØ	ØØ	Ø16176	Ø227	TAPEND	1071Ø	ØØ	Ø107522	ØØ
Ø234	TAFUN		324Ø	ØØ	Ø167534	Ø235	TAPMOV	1174	ØØ	Ø11Ø74	ØØ
Ø237	TDEMØ		Ø50Ø	ØØ	Ø11Ø5Ø	Ø24Ø	TDEMI	246Ø	ØØ	Ø117Ø5	ØØ
Ø242	TEX		Ø224	ØØ	Ø122ØØ	Ø243	TRBOC	1454	ØØ	Ø12626	ØØ
Ø245	TRCLN		Ø23Ø	ØØ	Ø13ØØ3	Ø234	TRDCK	137Ø	ØØ	Ø13347	ØØ
Ø251	TREQC		Ø221	ØØ	Ø1353Ø	Ø231	TRIDB	Ø31Ø	ØØ	Ø13655	ØØ
Ø253	TRORN		Ø554	ØØ	Ø13746	Ø254	TRRDB	132Ø	ØØ	Ø14365	ØØ
Ø256	TRREDO		Ø754	ØØ	Ø14433	Ø257	TRSET	12Ø4	ØØ	Ø15Ø33	ØØ
Ø261	TRWRT		Ø764	ØØ	Ø15356	Ø262	RSTRT	2154	ØØ	Ø16172	ØØ
Ø264	SDMP1		1614	ØØ	Ø17167	Ø265	SDMP2	175Ø	ØØ	Ø17532	ØØ

No.	Ov1	Name	Size	BM-Upr	BM-Lwr	No.	Ov1	Name	Size	BM-Upr	BM-Lwr
ØØ	SDMPC		Ø35Ø	Ø1	Ø21731	ØØ	Ø46Ø	Ø21615	Ø2	Ø22126	ØØ
Ø1	SDMPD		Ø154	Ø1	Ø22073	Ø1	Ø154	Ø22073	Ø1	Ø22126	Ø1
Ø5	MFCHK		Ø1524	Ø1	Ø22457	Ø5	AMSG	174Ø	Ø1	Ø22457	Ø5
ØØ	BEGIN		Ø176Ø	ØØ	Ø22524	ØØ	BEGIN	174Ø	ØØ	Ø22524	ØØ
ØØ	BMOLP		Ø1244	ØØ	Ø2260Ø	ØØ	BMOLP	1244	ØØ	Ø2260Ø	ØØ
Ø13	BTO		Ø2236Ø	ØØ	Ø22724	ØØ	BTO	1244	ØØ	Ø22724	ØØ
Ø16	BTM		Ø2224	ØØ	Ø22872	ØØ	BTM	1244	ØØ	Ø22872	ØØ
Ø21	CDEM		Ø2544	ØØ	Ø23072	ØØ	CDEM	2544	ØØ	Ø23072	ØØ
Ø24	CLOCK		Ø114	ØØ	Ø23274	ØØ	CLOCK	114	ØØ	Ø23274	ØØ
Ø27	CPSPIN		ØØ	ØØ	Ø235265	ØØ	CPSPIN	27	ØØ	Ø235265	ØØ
Ø32	CRTDEM		Ø215Ø	ØØ	Ø2364Ø	ØØ	CRTDEM	215Ø	ØØ	Ø2364Ø	ØØ
Ø35	DKDMP		Ø346Ø	ØØ	Ø23765	ØØ	DKDMP	346Ø	ØØ	Ø23765	ØØ
Ø40	DKSET		Ø3ØØ	ØØ	Ø23874	ØØ	DKSET	3ØØ	ØØ	Ø23874	ØØ
Ø43	DOM		Ø1504	ØØ	Ø23974	ØØ	DOM	1504	ØØ	Ø23974	ØØ
Ø46	ECHOCP		ØØ	ØØ	Ø2403Ø	ØØ	ECHOCP	74	ØØ	Ø2403Ø	ØØ
Ø51	F8ØM		Ø444Ø	ØØ	Ø2413Ø	ØØ	F8ØM	444Ø	ØØ	Ø2413Ø	ØØ
Ø54	FIND		Ø274	ØØ	Ø24217	ØØ	FIND	274	ØØ	Ø24217	ØØ
Ø57	HDATA		Ø364	ØØ	Ø2431Ø	ØØ	HDATA	364	ØØ	Ø2431Ø	ØØ
Ø62	INDD29		Ø1614	ØØ	Ø2441Ø	ØØ	INDD29	1614	ØØ	Ø2441Ø	ØØ
Ø65	LISTO		Ø6Ø4	ØØ	Ø2453Ø	ØØ	LISTO	6Ø4	ØØ	Ø2453Ø	ØØ
Ø70	MAGR		Ø122Ø	ØØ	Ø2463Ø	ØØ	MAGR	122Ø	ØØ	Ø2463Ø	ØØ
Ø73	MSGHND		Ø3364	ØØ	Ø2473Ø	ØØ	MSGHND	3364	ØØ	Ø2473Ø	ØØ
Ø76	OBIT		Ø64Ø	ØØ	Ø2483Ø	ØØ	OBIT	64Ø	ØØ	Ø2483Ø	ØØ
Ø80	OVLNUM		Ø13Ø	ØØ	Ø2493Ø	ØØ	OVLNUM	13Ø	ØØ	Ø2493Ø	ØØ
Ø84	PRTAPE		Ø13Ø	ØØ	Ø2503Ø	ØØ	PRTAPE	13Ø	ØØ	Ø2503Ø	ØØ
Ø104	STATS		Ø12Ø	ØØ	Ø2513Ø	ØØ	STATS	12Ø	ØØ	Ø2513Ø	ØØ
Ø120	SYSTEXT		Ø12Ø	ØØ	Ø2523Ø	ØØ	SYSTEXT	12Ø	ØØ	Ø2523Ø	ØØ
Ø123	TIME		Ø12Ø	ØØ	Ø2533Ø	ØØ	TIME	12Ø	ØØ	Ø2533Ø	ØØ
Ø126	TSTASH		Ø12Ø	ØØ	Ø2543Ø	ØØ	TSTASH	12Ø	ØØ	Ø2543Ø	ØØ
Ø127	START2		Ø12Ø	ØØ	Ø2553Ø	ØØ	START2	12Ø	ØØ	Ø2553Ø	ØØ
Ø128	START4		Ø12Ø	ØØ	Ø2563Ø	ØØ	START4	12Ø	ØØ	Ø2563Ø	ØØ
Ø131	USURP		Ø13Ø	ØØ	Ø2573Ø	ØØ	USURP	13Ø	ØØ	Ø2573Ø	ØØ
Ø134	XCL5		Ø13Ø	ØØ	Ø2583Ø	ØØ	XCL5	13Ø	ØØ	Ø2583Ø	ØØ
Ø137	XDISKB		Ø13Ø	ØØ	Ø2593Ø	ØØ	XDISKB	13Ø	ØØ	Ø2593Ø	ØØ
Ø142	XOPN		Ø14Ø	ØØ	Ø2603Ø	ØØ	XOPN	14Ø	ØØ	Ø2603Ø	ØØ
Ø145	XPRNTA		Ø14Ø	ØØ	Ø2613Ø	ØØ	XPRNTA	14Ø	ØØ	Ø2613Ø	ØØ
Ø150	XTAPEA		Ø15Ø	ØØ	Ø2623Ø	ØØ	XTAPEA	15Ø	ØØ	Ø2623Ø	ØØ
Ø153	XTAPEC		Ø15Ø	ØØ	Ø2633Ø	ØØ	XTAPEC	15Ø	ØØ	Ø2633Ø	ØØ
Ø156	D3ECC		Ø16Ø	ØØ	Ø2643Ø	ØØ	D3ECC	16Ø	ØØ	Ø2643Ø	ØØ
Ø161	D3LOG		Ø16Ø	ØØ	Ø2653Ø	ØØ	D3LOG	16Ø	ØØ	Ø2653Ø	ØØ
Ø164	D3SKR		Ø16Ø	ØØ	Ø2663Ø	ØØ	D3SKR	16Ø	ØØ	Ø2663Ø	ØØ
Ø167	D4ECC		Ø16Ø	ØØ	Ø2673Ø	ØØ	D4ECC	2Ø4	ØØ	Ø2673Ø	ØØ
Ø172	D4LOG		Ø17Ø	ØØ	Ø2683Ø	ØØ	D4LOG	77Ø	ØØ	Ø2683Ø	ØØ
Ø175	D4RES		Ø17Ø	ØØ	Ø2693Ø	ØØ	D4RES	52Ø	ØØ	Ø2693Ø	ØØ
Ø177	D4SLR		Ø17Ø	ØØ	Ø2703Ø	ØØ	D4SLR	124Ø	ØØ	Ø2703Ø	ØØ
Ø178	D4VOR		Ø17Ø	ØØ	Ø2713Ø	ØØ	D				

Ø267	SDMP3A	Ø550	ØØ	12017Ø	Ø27Ø	SDMP4	Ø350	ØØ	120322	Ø271	SDMP5	Ø21Ø	ØØ	120414
Ø272	SDMP6	Ø234	ØØ	120456	Ø273	SDMP7	Ø25Ø	ØØ	120525	Ø274	SDMP8	Ø61Ø	ØØ	120577
Ø275	SDMP9	Ø454	ØØ	120741	Ø276	SDMP1Ø	Ø144	ØØ	121054	Ø277	CLEARØ	Ø52Ø	ØØ	121105
Ø3ØØ	CLEARØ	Ø154	ØØ	121231	Ø3Ø1	COPYØ	Ø1ØØ	ØØ	121264	Ø3Ø2	COPYØ	ØØ	ØØ	121474
Ø3Ø3	COPY1	Ø67Ø	ØØ	121713	Ø3Ø4	COPY2	Ø744	ØØ	122071	Ø3Ø5	COPY3	Ø6ØØ	ØØ	122662
Ø3Ø6	COPY4	Ø474	ØØ	122423	Ø3Ø7	COPY5	Ø55Ø	ØØ	122542	Ø3Ø8	DDUMPØ	Ø61Ø	ØØ	122674
Ø311	DDUMPØ	Ø374	ØØ	123Ø36	Ø312	DEF	Ø65Ø	ØØ	123135	Ø313	DELETEØ	Ø414	ØØ	1233Ø7
Ø314	DELETE	Ø65Ø	ØØ	123412	Ø315	DLOAD	Ø56Ø	ØØ	123564	Ø316	DLOADØ	Ø113Ø	ØØ	12372Ø
Ø317	DSTAT	Ø514	ØØ	124146	Ø32Ø	DSTATØ	Ø5Ø4	ØØ	124271	Ø321	EDDELE	Ø1ØØ	ØØ	124412
Ø322	EDINST	Ø544	ØØ	124433	Ø323	EDIT	Ø644	ØØ	124564	Ø324	EDITØ	Ø14ØØ	ØØ	124735
Ø347	EDIT1	Ø174	ØØ	125251	Ø325	EDRPL	Ø534	ØØ	125510	Ø327	EDRPL	Ø134	ØØ	125637
Ø33Ø	EDTYPE	Ø234	ØØ	125666	Ø331	FDUMPØ	Ø77Ø	ØØ	125735	Ø332	FDUMPØ	Ø1Ø4Ø	ØØ	126133
Ø333	FLAW	Ø63Ø	ØØ	126343	Ø334	FLOAD	Ø714	ØØ	12652Ø	Ø335	FLOADØ	Ø13ØØ	ØØ	127Ø03
Ø336	FSTAT	Ø65Ø	ØØ	127263	Ø337	FSTATØ	Ø55Ø	ØØ	127435	Ø34Ø	INIT	Ø63Ø	ØØ	127567
Ø341	INITØ	Ø774	ØØ	127735	Ø342	LINGET	Ø361	ØØ	13Ø134	Ø343	INPUT	Ø66Ø	ØØ	13Ø226
Ø344	PROC	Ø564	ØØ	13Ø403	Ø345	PROCO	Ø554	ØØ	13Ø54Ø	Ø346	RENAME	Ø6ØØ	ØØ	13Ø673
Ø349	RENAMØ	Ø43Ø	ØØ	131Ø33	Ø35Ø	XFMAC	Ø244	ØØ	131141	Ø351	XFMCLS	Ø24Ø	ØØ	131212
Ø352	XFMCRE	Ø71Ø	ØØ	131262	Ø353	XFMDL	Ø174	ØØ	131444	Ø354	XFMDIR	Ø35Ø	ØØ	131503
Ø355	XFMDS	Ø634	ØØ	131575	Ø356	XFMFL	Ø324	ØØ	131734	Ø357	XFMFST	Ø144	ØØ	132Ø31
Ø36Ø	XFMFLW	Ø244	ØØ	132Ø62	Ø361	XFMFN	Ø4Ø4	ØØ	132133	Ø362	XFMGET	Ø514	ØØ	132234
Ø363	XFMIO	Ø1Ø2Ø	ØØ	132355	Ø364	XFMNIT	Ø1Ø54	ØØ	132561	Ø365	XFMPUT	Ø414	ØØ	132774
Ø366	XFMSTR	Ø2ØØ	ØØ	133Ø77	Ø367	FORMAT	Ø434	ØØ	126511	Ø37Ø	CONC	Ø37Ø	ØØ	133137
Ø371	CONCER	Ø134	ØØ	133436	Ø372	CONCID	Ø124	ØØ	133665	Ø373	CONCIO	Ø37Ø	ØØ	13413Ø
Ø374	ENDCONC	Ø5ØØ	ØØ	134761	Ø375	ENTRID	Ø13Ø	ØØ	1351Ø1	Ø376	REMVID	Ø1ØØ	ØØ	135267
Ø378	ACQTRM	ØØØ	ØØ	135147	Ø4ØØ	ACQUIRE	Ø4Ø4	ØØ	135163	Ø4Ø1	AMPEX	Ø4ØØ	ØØ	135264
Ø4Ø2	BABEL	Ø1634	ØØ	135424	Ø4Ø3	BARDAT	Ø42Ø	ØØ	135773	Ø4Ø4	BIMGET	Ø4Ø4	ØØ	136Ø77
Ø4Ø5	BMGET	Ø374	ØØ	1362ØØ	Ø4Ø6	BXDIS	Ø544	ØØ	136277	Ø4Ø7	CFINIT	Ø1Ø2Ø	ØØ	13643Ø
Ø41Ø	CFREAD	Ø56Ø	ØØ	136634	Ø411	CLI	Ø17Ø	ØØ	13677Ø	Ø412	CLINIT	Ø314	ØØ	137354
Ø413	CLUSR	Ø1ØØ	ØØ	137657	Ø414	CMGET	Ø22Ø	ØØ	14ØØ57	Ø415	CNCDIS	Ø2ØØ	ØØ	14Ø127
Ø416	COMBO	Ø134	ØØ	14Ø527	Ø417	COMM1	Ø374	ØØ	145556	Ø42Ø	COMMØ2	Ø143Ø	ØØ	145556
Ø421	COMMØ3	Ø71Ø	ØØ	142Ø55	Ø42Ø	COMM4	Ø1Ø74	ØØ	142237	Ø423	COMMØ5	Ø121Ø	ØØ	142456
Ø424	COMMØ6	Ø6ØØ	ØØ	14272Ø	Ø425	COMM7	Ø71Ø	ØØ	143Ø61	Ø426	COMMØ8	Ø1164	ØØ	143243
Ø427	COMM9	Ø6Ø	ØØ	1435ØØ	Ø43Ø	COMM10	Ø164	ØØ	144041	Ø431	COMM11	Ø1414	ØØ	144356
Ø432	COMM11	Ø76Ø	ØØ	144763	Ø433	COMM13	Ø1764	ØØ	145663	Ø434	COMM14	Ø131Ø	ØØ	146123
Ø435	COMM15	Ø1Ø4	ØØ	145642	Ø436	COMM16	Ø73Ø	ØØ	14623Ø	Ø437	CONSØ	Ø125Ø	ØØ	146251
Ø44Ø	CPUGET	Ø35Ø	ØØ	146523	Ø441	CRAY10	Ø1ØØ	ØØ	146615	Ø442	DBGET	Ø37Ø	ØØ	147Ø35
Ø443	DECOD2	Ø66Ø	ØØ	147133	Ø444	DECODE	Ø163Ø	ØØ	1473Ø7	Ø445	DELMSG	Ø12ØØ	ØØ	147655
Ø446	DESCRIBE	Ø2Ø4	ØØ	15Ø115	Ø447	DISPLAY	Ø56Ø	ØØ	15Ø156	Ø45Ø	DISPØ1	Ø3714	ØØ	15Ø312
Ø451	DISPØ2	Ø2ØØ	ØØ	151275	Ø452	ERRDIS	Ø15ØØ	ØØ	152261	Ø453	DKDIS	Ø42Ø	ØØ	153Ø6Ø
Ø452	DKSTAT	Ø25Ø	ØØ	152562	Ø455	ERRDIS	Ø424	ØØ	152661	Ø456	ERROR	Ø23Ø	ØØ	153166
Ø457	F8Ø	Ø5754	ØØ	153235	Ø46Ø	F132	Ø5754	ØØ	15463Ø	Ø461	FMGET	Ø43Ø	ØØ	155223
Ø462	GRAPH	Ø123Ø	ØØ	156331	Ø463	GRCDIS	Ø2Ø74	ØØ	156577	Ø464	GRPDIS	Ø122Ø	ØØ	157216
Ø465	GRUDIS	Ø2Ø5Ø	ØØ	157462	Ø466	HELP	Ø456	ØØ	158074	Ø467	HELP2	Ø3714	ØØ	158312
Ø47Ø	HELPØ	Ø2ØØ	ØØ	160511	Ø471	HSPGET	Ø4ØØ	ØØ	164642	Ø472	ICONSØ	Ø42Ø	ØØ	164721
Ø473	IDEBUG	Ø12ØØ	ØØ	16503Ø	Ø474	IDLGET	Ø45Ø	ØØ	165274	Ø475	IDRCT	Ø166Ø	ØØ	1654Ø6
Ø476	IFRMT	Ø53Ø	ØØ	165762	Ø477	KEYBD	Ø514	ØØ	16611Ø	Ø461	LCP	Ø1144	ØØ	166233
Ø5Ø1	LINK	Ø1254	ØØ	166464	Ø463	LOGON	Ø474	ØØ	166737	Ø467	MESSAGE	Ø25Ø	ØØ	167237
Ø5Ø4	MSTAT	Ø1544	ØØ	167312	Ø5Ø5	MSTDIS	Ø154Ø	ØØ	167643	Ø467	NEWDIS	Ø13ØØ	ØØ	168213
Ø5Ø7	NSCNET	Ø1550	ØØ	17ØØ7Ø	Ø5ØØ	NSCSFP	Ø22ØØ	ØØ	17ØØ71	Ø511	OFRMT	Ø33Ø	ØØ	169472
Ø512	ONLINE	Ø44Ø	ØØ	171443	Ø513	POST	Ø514	ØØ	171553	Ø514	PROTINIT	Ø1254	ØØ	171676
Ø515	PROTOCOL	Ø151Ø	ØØ	172151	Ø516	QUEUE	Ø34Ø	ØØ	172473	Ø517	READ	Ø1414	ØØ	172563
Ø52Ø	SNAP	ØØØ	ØØ	173Ø66	Ø521	SOROC	Ø44Ø	ØØ	1732Ø5	Ø522	SSDGRA	Ø1Ø7Ø	ØØ	173315
Ø523	STADIS	Ø214Ø	ØØ	173533	Ø524	STAGEIN	Ø1414	ØØ	174163	Ø525	STATION	Ø15ØØ	ØØ	174466
Ø526	STATCL	Ø1374	ØØ	1750ØØ	Ø527	STATTIN	Ø54Ø	ØØ	1753Ø6	Ø53Ø	STPLØT	Ø6ØØ	ØØ	175436
Ø531	STIO	Ø175453	ØØ	17553Ø	Ø532	STRSTAT	Ø146Ø	ØØ	175542	Ø533	STSGET	Ø35Ø	ØØ	176156
Ø534	STREAMS	Ø1114	ØØ	176317	Ø535	STTAPO	Ø57Ø	ØØ	177323	Ø541	STUBPR	Ø176Ø	ØØ	177Ø56
Ø537	STTAPI	Ø6554	ØØ	17715Ø	Ø538	STXDKO	Ø672Ø	ØØ	177544	Ø541	SUMTØ	Ø177Ø	ØØ	177461
Ø542	STXDKI	Ø1Ø44	ØØ	ØØØØ55	Ø543	TAPEC	Ø55Ø	ØØ	ØØØØ26	Ø544	SYNTAX	Ø26Ø	ØØ	178157
Ø545	SUMHOW	Ø6664	ØØ	ØØØØØØ	Ø551	TKSTAT	Ø165Ø	ØØ	ØØØØ74	Ø552	TEC455	Ø3ØØ	ØØ	178316
Ø553	SYSTAT	Ø1754	ØØ	ØØØØ37	Ø554	TURNPG	Ø774	ØØ	ØØØØ55	Ø555	XJSTAT	Ø774	ØØ	178437
Ø556	UPDATE	Ø1674	ØØ	ØØØØ45	Ø557	XFRMT	Ø150Ø	ØØ	ØØØØ75	Ø558	IACMD	Ø175Ø	ØØ	178522
Ø559	XMPXP	Ø226Ø	ØØ	ØØØØ56	Ø560	IACON1	Ø4Ø4	ØØ	ØØØØ76	Ø561	IACON1	Ø1444	ØØ	178776
Ø564	XMPXP	ØØØØ56	ØØ	ØØØØ77	Ø565	IACON1	Ø4Ø4	ØØ	ØØØØ77	Ø566	IACFUNC	Ø1ØØ	ØØ	178776

Z	SCP	STG	DQM	PDM	JCM	JSH	DQM	DEC	EXP	MEP	MSG	SPM	IQM	FYD	CSP	USER	DISK/SSD	PACKET	
AUT	-AUT	PDD	-DAT	DAT	-CSD	CSD	(DOL)	EQT	CNT	-AEM	AUT	CSD	ILT	EQT	(DSP)	(BGN)	(LDT)	FED	
CNT	-IBT	SDT	DCT	CSD	SDT	-JXT	-(DNT)	-DEX	(DSP)	DCT	(DSP)	VPT	VPT	(OPT)	(BAT)	(OPT)	DRIVER		
DAT	-LCT	-SST	(DNT)	(DNT)	(DNT)	-MST	-(DNT)	(DNT)	JXT	+MCT	JTA	+MCT	VCT	(JCB)	(DDL)	(JBI)	DISK/		
(DNT)	-LIT	-DRT	-DRT	DRT	-DRT	-RJI	JXT	(DNT)	-LFT	+ICT	JXT	+ICT	TRB	(LFT)	(DNT)	(JAC)	DRIVER		
DRT	-LXT	(DSP)	(DSP)	-DSC	(DSP)	SDT	-LFT	(DNT)	-DUX	+STT	-LGJ	+STT	IST	(NCF)	(RCB)	(NCB)	DRIVER		
-DSC	POD	-EQT	(DSP)	DXT	-EQT	-TXT	-ODN	-ODN	-FSH	PDD	GRT	PDD	(JCB)	(JCB)	(JCB)	(JCB)	DRIVER		
(OSP)	-SDT	GRT	DXT	JXT	JXT	EQT	JTA	(POD)	SOT	SDT	SDT	SDT	(LFT)	(LFT)	(LFT)	(LFT)	DRIVER		
-DVL	-SDT	-RJT	JCB	JTA	JTA	-RJT	TCB	-SWT	JXT	-LDT	-SM	-SM	(NCF)	(NCF)	(NCF)	(NCF)	DRIVER		
DXT	-SDT	JTA	PHR	JXT	PHR	JTA	JXT	-UPT	-JTA	-VAX	-JTA	-VAX	(JAC)	(JAC)	(JAC)	(JAC)	DRIVER		
-EFT	-EQT	GRT	SQP	PDD	SQP	-EFT	-UPT	-JTA	-TXT	-VUX	-TXT	-VUX	(JCB)	(JCB)	(JCB)	(JCB)	DRIVER		
-EQT	GRT	TXT	PDI	TXT	PDI	-PDI	-PDI	-QDT	IDD	IDD	IRT	IRT	(JCB)	(JCB)	(JCB)	(JCB)	DRIVER		
JTA	JXT	JXT	-PDS	-QDT	-PDS	-QDT	-XAT	SDT	TRB	TRB	TRB	TRB	(JAC)	(JAC)	(JAC)	(JAC)	DRIVER		
(DNT)	PDI	QDT	-RJI	-RJI	-RJI	-RJI	-RJI	SDT	TCB	TDT	TDT	TDT	(JCB)	(JCB)	(JCB)	(JCB)	DRIVER		

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